



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®2006 = 33.0**

## Gateway GR380 F1 (Intel Xeon E5630)

**SPECint\_base2006 = 30.7**

CPU2006 license: 97

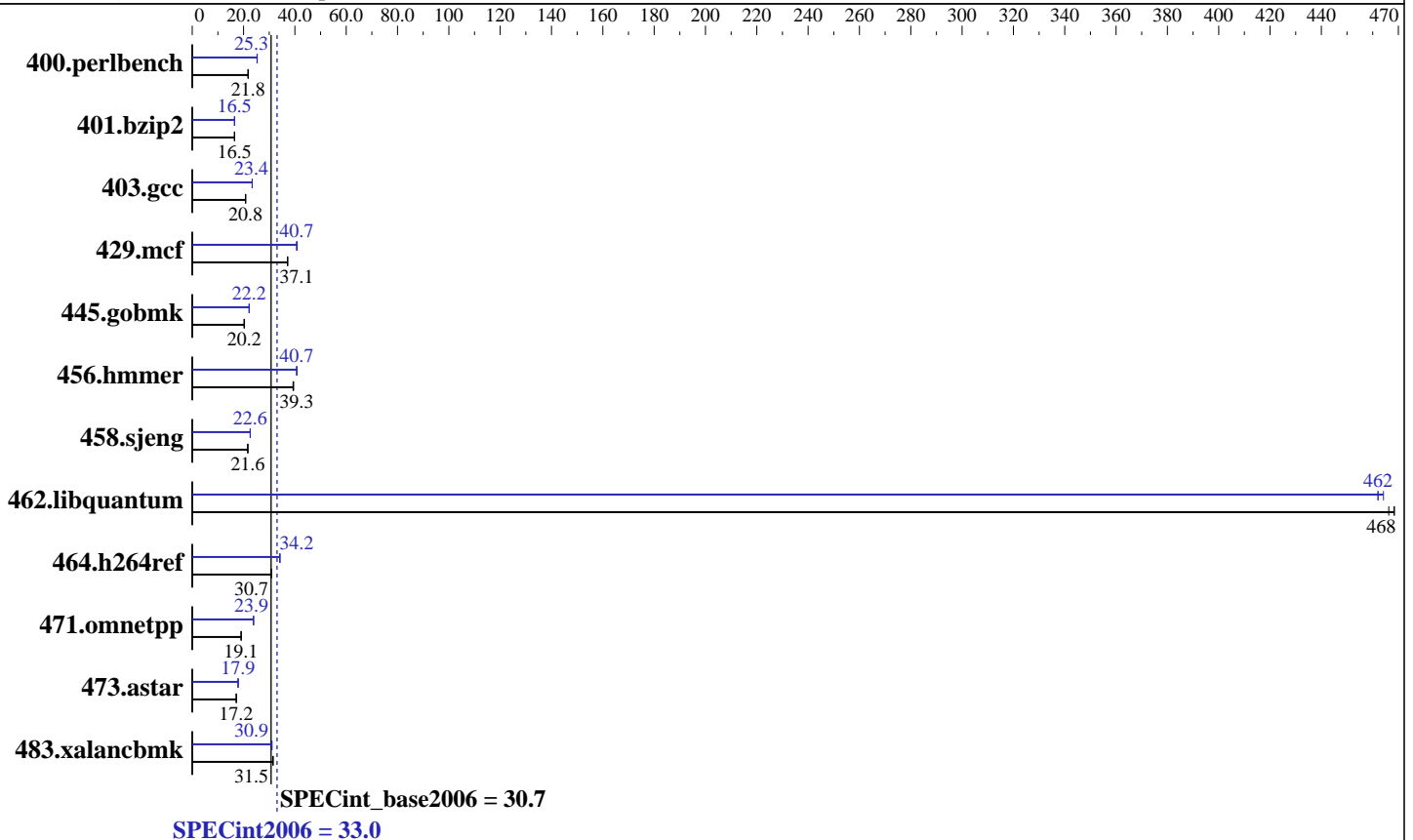
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon E5630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 300 GB 10000RPM SATA HDD  
 Other Hardware: None

### Software

Operating System: SUES Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	449	21.7	448	21.8	<b>448</b>	<b>21.8</b>	386	25.3	387	25.2	<b>386</b>	<b>25.3</b>
401.bzip2	585	16.5	<b>586</b>	<b>16.5</b>	586	16.5	<b>585</b>	<b>16.5</b>	585	16.5	586	16.5
403.gcc	387	20.8	387	20.8	<b>387</b>	<b>20.8</b>	344	23.4	345	23.3	<b>344</b>	<b>23.4</b>
429.mcf	245	37.3	<b>246</b>	<b>37.1</b>	246	37.1	223	40.9	<b>224</b>	<b>40.7</b>	224	40.7
445.gobmk	519	20.2	515	20.4	<b>518</b>	<b>20.2</b>	<b>473</b>	<b>22.2</b>	473	22.2	473	22.2
456.hmmer	<b>237</b>	<b>39.3</b>	237	39.3	236	39.5	<b>229</b>	<b>40.7</b>	229	40.8	229	40.7
458.sjeng	560	21.6	558	21.7	<b>559</b>	<b>21.6</b>	<b>536</b>	<b>22.6</b>	535	22.6	536	22.6
462.libquantum	<b>44.2</b>	<b>468</b>	44.4	466	44.2	468	44.8	462	<b>44.8</b>	<b>462</b>	44.6	464
464.h264ref	718	30.8	<b>720</b>	<b>30.7</b>	722	30.7	648	34.1	<b>648</b>	<b>34.2</b>	648	34.2
471.omnetpp	327	19.1	328	19.1	<b>328</b>	<b>19.1</b>	262	23.8	261	23.9	<b>262</b>	<b>23.9</b>
473.astar	410	17.1	408	17.2	<b>409</b>	<b>17.2</b>	392	17.9	394	17.8	<b>392</b>	<b>17.9</b>
483.xalancbmk	<b>219</b>	<b>31.5</b>	219	31.5	220	31.4	222	31.1	223	30.9	<b>223</b>	<b>30.9</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

OMP\_NUM\_THREADS set to number of cores

KMP\_AFFINITY set to granularity=fine,scatter

This result was measured on the Gateway GR360 F1.

The Acer AR360, Acer AR380 F1, Gateway GR360 F1 and Gateway GR380 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

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## Base Portability Flags (Continued)

464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-64bit -lsmarheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Peak Portability Flags (Continued)

401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
 -opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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## Peak Optimization Flags (Continued)

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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