



# SPEC® CINT2006 Result

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Itautec

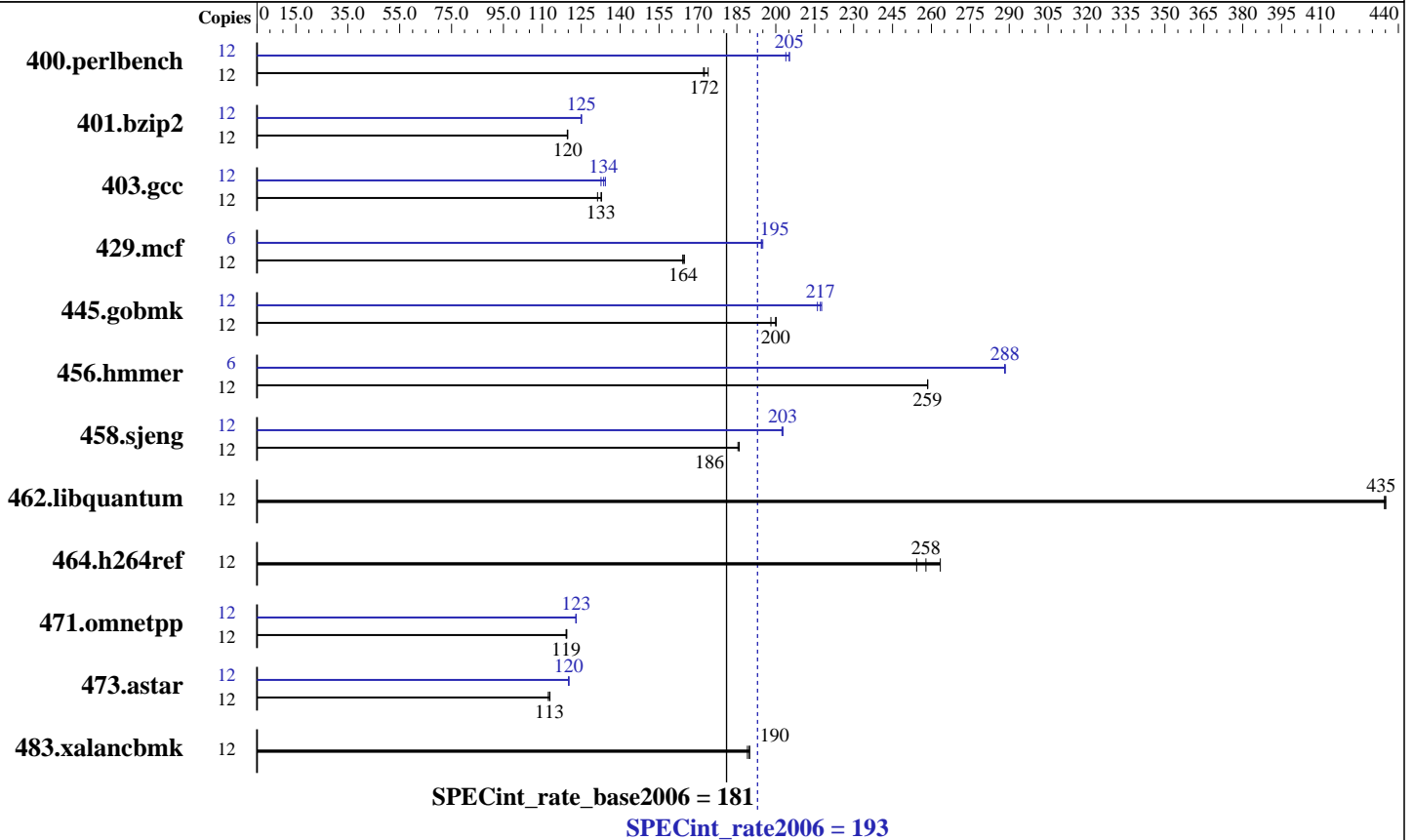
SPECint®\_rate2006 = 193

Servidor Itautec MX224 (Intel Xeon X5680)

SPECint\_rate\_base2006 = 181

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Aug-2010  
Hardware Availability: Feb-2011  
Software Availability: Apr-2010



## Hardware

CPU Name: Intel Xeon X5680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l\_cproc\_p\_11.1.072  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	674	174	681	172	<b>680</b>	<b>172</b>	12	<b>571</b>	<b>205</b>	571	205	575	204
401.bzip2	12	968	120	<b>967</b>	<b>120</b>	967	120	12	<b>926</b>	<b>125</b>	926	125	927	125
403.gcc	12	736	131	<b>728</b>	<b>133</b>	727	133	12	719	134	728	133	<b>723</b>	<b>134</b>
429.mcf	12	<b>666</b>	<b>164</b>	664	165	667	164	6	<b>281</b>	<b>195</b>	281	194	281	195
445.gobmk	12	635	198	629	200	<b>630</b>	<b>200</b>	12	<b>580</b>	<b>217</b>	578	218	583	216
456.hammer	12	433	259	433	258	<b>433</b>	<b>259</b>	6	<b>194</b>	<b>288</b>	194	288	194	288
458.sjeng	12	<b>781</b>	<b>186</b>	781	186	783	185	12	718	202	<b>717</b>	<b>203</b>	716	203
462.libquantum	12	571	435	572	435	<b>572</b>	<b>435</b>	12	571	435	572	435	<b>572</b>	<b>435</b>
464.h264ref	12	<b>1030</b>	<b>258</b>	1044	254	1008	263	12	<b>1030</b>	<b>258</b>	1044	254	1008	263
471.omnetpp	12	629	119	628	119	<b>629</b>	<b>119</b>	12	610	123	<b>610</b>	<b>123</b>	610	123
473.astar	12	750	112	<b>747</b>	<b>113</b>	746	113	12	701	120	701	120	<b>701</b>	<b>120</b>
483.xalancbmk	12	438	189	<b>436</b>	<b>190</b>	436	190	12	438	189	<b>436</b>	<b>190</b>	436	190

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itaotec MX223+.  
The Servidor Itaotec MX223+, the Servidor Itaotec MX203+ and the Servidor Itaotec MX224 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
401.bzip2: icc -m64  
456.hmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks (except as noted below):  
icpc -m32  
473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8/lib -lsmarheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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