SPEC® CFP2006 Result
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Acer Incorporated

$SPECfp®\_rate2006 = 244$

Gateway GR360 F1 (Intel Xeon X5670)

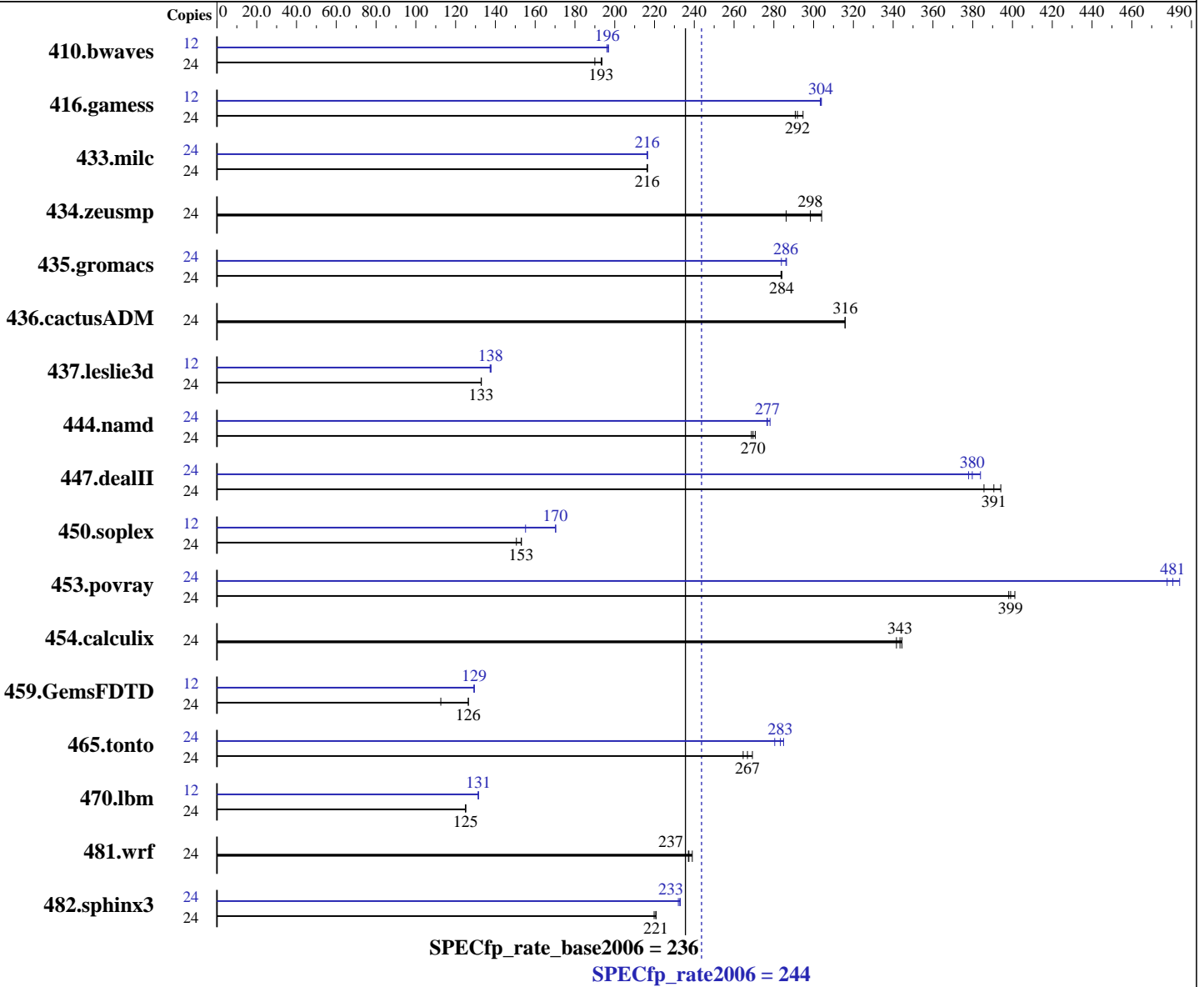$SPECfp\_rate\_base2006 = 236$

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Dec-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

SPECfp_rate_base2006 = 236

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Hardware	
CPU Name:	Intel Xeon X5670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2993
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software	
Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 300 GB SATA, 10000RPM SATA HDD
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1717	190	**1688**	**193**	1685	194	12	831	196	828	197	**831**	**196**
416.gamess	24	**1610**	**292**	1595	295	1616	291	12	773	304	775	303	**774**	**304**
433.milc	24	**1018**	**216**	1018	216	1018	216	24	1018	217	1019	216	**1018**	**216**
434.zeusmp	24	718	304	763	286	**732**	**298**	24	718	304	763	286	**732**	**298**
435.gromacs	24	604	284	603	284	**603**	**284**	24	**599**	**286**	598	286	604	284
436.cactusADM	24	908	316	908	316	**908**	**316**	24	908	316	908	316	**908**	**316**
437.leslie3d	24	**1697**	**133**	1697	133	1698	133	12	821	137	**820**	**138**	819	138
444.namd	24	**714**	**270**	716	269	711	271	24	696	277	692	278	**695**	**277**
447.dealII	24	**703**	**391**	697	394	712	386	24	715	384	727	378	**723**	**380**
450.soplex	24	1329	151	1308	153	**1308**	**153**	12	645	155	587	170	**588**	**170**
453.povray	24	321	398	**320**	**399**	318	401	24	**266**	**481**	264	484	267	478
454.calculix	24	**577**	**343**	580	342	575	344	24	**577**	**343**	580	342	575	344
459.GemsFDTD	24	2262	113	**2015**	**126**	2014	126	12	**985**	**129**	985	129	984	129
465.tonto	24	877	269	**885**	**267**	893	265	24	**834**	**283**	829	285	842	280
470.lbm	24	**2637**	**125**	2636	125	2640	125	12	1255	131	**1254**	**131**	1254	131
481.wrf	24	1122	239	**1130**	**237**	1131	237	24	1122	239	**1130**	**237**	1131	237
482.sphinx3	24	2128	220	**2120**	**221**	2118	221	24	2016	232	2008	233	**2010**	**233**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
This result was measured on the Gateway GR360 F1.
The Acer AR360, Acer AR380 F1, Gateway GR360 F1 and Gateway GR380 F1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
    icc   -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

Base Portability Flags

          410.bwaves: -DSPEC_CPU_LP64
          416.gamess: -DSPEC_CPU_LP64
            433.milc: -DSPEC_CPU_LP64
          434.zeusmp: -DSPEC_CPU_LP64
         435.gromacs: -DSPEC_CPU_LP64 -nofor_main
       436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
        437.leslie3d: -DSPEC_CPU_LP64
            444.namd: -DSPEC_CPU_LP64
           447.dealII: -DSPEC_CPU_LP64
          450.soplex: -DSPEC_CPU_LP64
          453.povray: -DSPEC_CPU_LP64
         454.calculix: -DSPEC_CPU_LP64 -nofor_main
        459.GemsFDTD: -DSPEC_CPU_LP64
           465.tonto: -DSPEC_CPU_LP64
            470.lbm: -DSPEC_CPU_LP64
             481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
         482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

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Peak Compiler Invocation

C benchmarks (except as noted below):
     `icc   -m64`

     482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
     `icpc   -m64`

      450.soplex: `icpc -m32`

Fortran benchmarks:
     `ifort -m64`

Benchmarks using both Fortran and C:
     `icc   -m64 ifort -m64`

Peak Portability Flags

         410.bwaves: `-DSPEC_CPU_LP64`
         416.gamess: `-DSPEC_CPU_LP64`
           433.milc: `-DSPEC_CPU_LP64`
         434.zeusmp: `-DSPEC_CPU_LP64`
        435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
      436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
        437.leslie3d: `-DSPEC_CPU_LP64`
           444.namd: `-DSPEC_CPU_LP64`
          447.dealII: `-DSPEC_CPU_LP64`
         453.povray: `-DSPEC_CPU_LP64`
        454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
      459.GemsFDTD: `-DSPEC_CPU_LP64`
          465.tonto: `-DSPEC_CPU_LP64`
           470.lbm: `-DSPEC_CPU_LP64`
            481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

        433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)`
                `-fno-alias -opt-prefetch`

        470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
                `-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)`
                `-opt-malloc-options=3 -ansi-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

    444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -fno-alias -auto-ilp32

    447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -unroll2 -ansi-alias -scalar-rep-

    450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -opt-malloc-options=3

    453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -unroll4 -ansi-alias

Fortran benchmarks:

    410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

    416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -unroll2 -Ob0 -ansi-alias -scalar-rep-

    434.zeusmp: basepeak = yes

    437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

  459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -unroll2 -Ob0

    465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

    435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
        -opt-prefetch -auto-ilp32

  436.cactusADM: basepeak = yes

    454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revH.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revH.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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