



# SPEC<sup>®</sup> CFP2006 Result

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## Bull SAS

SPECfp<sup>®</sup>\_rate2006 = 177

## BL265+ (Intel Xeon E5620, 2.40 GHz)

SPECfp\_rate\_base2006 = 174

CPU2006 license: 20

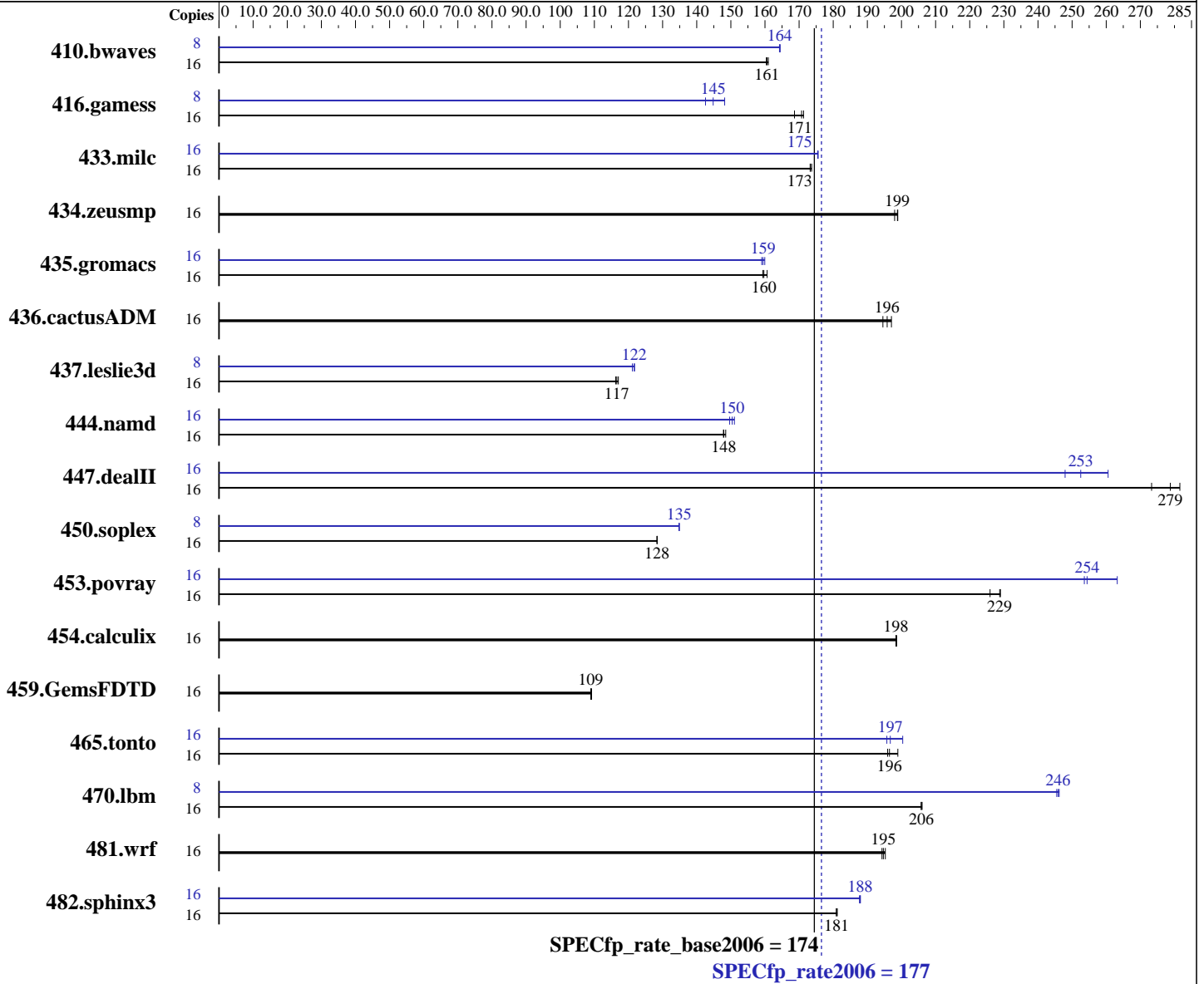
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Dec-2010

Hardware Availability: May-2010

Software Availability: Nov-2010



### Hardware

CPU Name: Intel Xeon E5620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)  
 Disk Subsystem: 2 x 50 GB SATA, SSD  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1351	161	<b>1354</b>	<b>161</b>	1356	160	8	<b>661</b>	<b>164</b>	661	164	662	164
416.gamess	16	<b>1835</b>	<b>171</b>	1829	171	1858	169	8	1099	143	<b>1082</b>	<b>145</b>	1057	148
433.milc	16	846	174	<b>847</b>	<b>173</b>	848	173	16	837	176	<b>837</b>	<b>175</b>	837	175
434.zeusmp	16	<b>732</b>	<b>199</b>	732	199	735	198	16	<b>732</b>	<b>199</b>	732	199	735	198
435.gromacs	16	711	161	<b>715</b>	<b>160</b>	717	159	16	714	160	<b>717</b>	<b>159</b>	718	159
436.cactusADM	16	970	197	<b>976</b>	<b>196</b>	983	195	16	970	197	<b>976</b>	<b>196</b>	983	195
437.leslie3d	16	1285	117	1294	116	<b>1290</b>	<b>117</b>	8	621	121	<b>618</b>	<b>122</b>	617	122
444.namd	16	864	148	868	148	<b>867</b>	<b>148</b>	16	<b>853</b>	<b>150</b>	850	151	857	150
447.dealII	16	650	282	670	273	<b>657</b>	<b>279</b>	16	702	261	738	248	<b>725</b>	<b>253</b>
450.soplex	16	<b>1039</b>	<b>128</b>	1039	128	1040	128	8	495	135	<b>495</b>	<b>135</b>	495	135
453.povray	16	372	229	<b>372</b>	<b>229</b>	377	226	16	<b>335</b>	<b>254</b>	323	263	336	254
454.calculix	16	<b>665</b>	<b>198</b>	665	199	665	198	16	<b>665</b>	<b>198</b>	665	199	665	198
459.GemsFDTD	16	1555	109	1558	109	<b>1558</b>	<b>109</b>	16	1555	109	1558	109	<b>1558</b>	<b>109</b>
465.tonto	16	803	196	791	199	<b>801</b>	<b>196</b>	16	786	200	<b>800</b>	<b>197</b>	804	196
470.lbm	16	<b>1068</b>	<b>206</b>	1068	206	1067	206	8	446	246	448	245	<b>447</b>	<b>246</b>
481.wrf	16	<b>918</b>	<b>195</b>	920	194	915	195	16	<b>918</b>	<b>195</b>	920	194	915	195
482.sphinx3	16	<b>1723</b>	<b>181</b>	1721	181	1724	181	16	1659	188	<b>1660</b>	<b>188</b>	1661	188

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 numactl was used to bind copies to the cores

## Operating System Notes

Hugepages was enabled with the following:  
 'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab  
 echo 7200 > /proc/sys/vm/nr\_hugepages  
 export HUGETLB\_MORECORE=yes  
 export LD\_PRELOAD=/usr/lib64/libhugetlbfs.so



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## Platform Notes

Turbo Mode enabled in BIOS  
Turbo Boost set to Traditional in BIOS  
Power C-states enabled in BIOS  
Demand Scrub disabled in BIOS

## General Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):  
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32  
482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32  
447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT  
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static  
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static  
434.zeusmp: basepeak = yes  
437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

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## Peak Optimization Flags (Continued)

459.GemsFDTD: basepeak = yes

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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