



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

**SPECfp®2006 = 63.2**

**IBM System x3650 M3 (Intel Xeon X5690)**

**SPECfp\_base2006 = 59.2**

CPU2006 license: 11

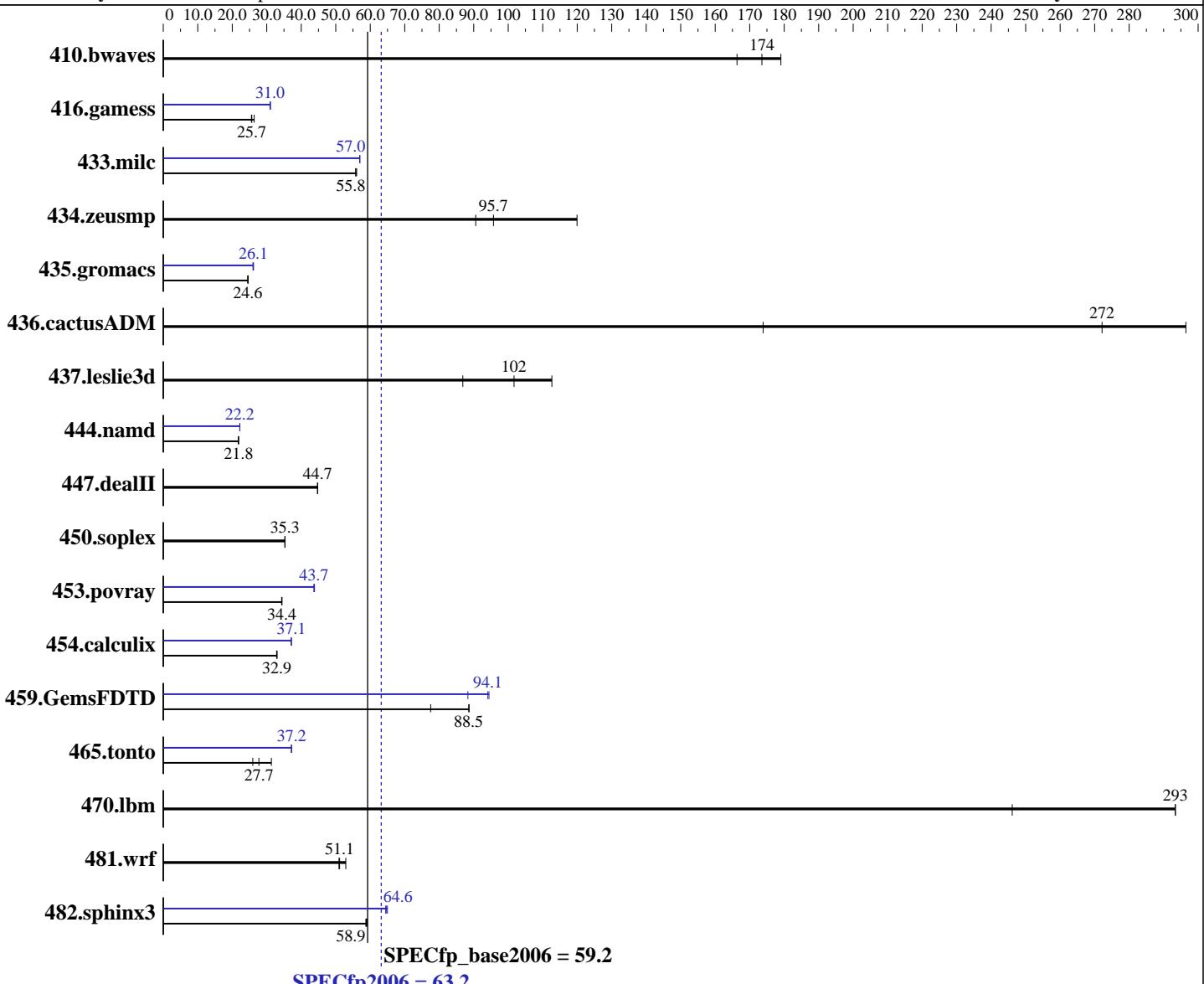
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Mar-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



## Hardware

CPU Name: Intel Xeon X5690  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz  
 CPU MHz: 3467  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

**SPECfp2006 = 63.2**

**IBM System x3650 M3 (Intel Xeon X5690)**

**SPECfp\_base2006 = 59.2**

**CPU2006 license:** 11

**Test date:** Mar-2011

**Test sponsor:** IBM Corporation

**Hardware Availability:** Feb-2011

**Tested by:** IBM Corporation

**Software Availability:** Jan-2011

L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	81.7	166	<b>78.3</b>	<b>174</b>	75.9	179	81.7	166	<b>78.3</b>	<b>174</b>	75.9	179
416.gamess	<b>763</b>	<b>25.7</b>	743	26.4	766	25.6	<b>632</b>	31.0	630	31.1	<b>632</b>	<b>31.0</b>
433.milc	165	55.8	<b>164</b>	<b>55.8</b>	164	56.1	<b>161</b>	57.0	161	56.9	<b>161</b>	<b>57.0</b>
434.zeusmp	75.9	120	100	90.6	<b>95.1</b>	<b>95.7</b>	75.9	120	100	90.6	<b>95.1</b>	<b>95.7</b>
435.gromacs	290	24.6	<b>291</b>	<b>24.6</b>	292	24.5	<b>273</b>	<b>26.1</b>	275	26.0	273	26.2
436.cactusADM	68.7	174	<b>43.9</b>	<b>272</b>	40.3	296	68.7	174	<b>43.9</b>	<b>272</b>	40.3	296
437.leslie3d	108	86.8	83.4	113	<b>92.4</b>	<b>102</b>	108	86.8	83.4	113	<b>92.4</b>	<b>102</b>
444.namd	368	21.8	368	21.8	<b>368</b>	<b>21.8</b>	<b>361</b>	<b>22.2</b>	362	22.2	361	22.2
447.dealII	256	44.8	256	44.7	<b>256</b>	<b>44.7</b>	256	44.8	256	44.7	<b>256</b>	<b>44.7</b>
450.soplex	236	35.3	237	35.3	<b>236</b>	<b>35.3</b>	236	35.3	237	35.3	<b>236</b>	<b>35.3</b>
453.povray	<b>155</b>	<b>34.4</b>	154	34.4	155	34.3	<b>122</b>	<b>43.7</b>	122	43.7	121	43.8
454.calculix	<b>250</b>	<b>32.9</b>	251	32.9	250	33.0	<b>222</b>	<b>37.1</b>	222	37.1	222	37.1
459.GemsFDTD	<b>120</b>	<b>88.5</b>	120	88.7	137	77.5	<b>112</b>	94.4	<b>113</b>	<b>94.1</b>	120	88.3
465.tonto	<b>355</b>	<b>27.7</b>	314	31.3	379	26.0	<b>265</b>	37.2	265	37.2	<b>265</b>	<b>37.2</b>
470.lbm	46.8	293	55.8	246	<b>46.8</b>	<b>293</b>	46.8	293	55.8	246	<b>46.8</b>	<b>293</b>
481.wrf	211	52.9	<b>219</b>	<b>51.1</b>	219	50.9	211	52.9	<b>219</b>	<b>51.1</b>	219	50.9
482.sphinx3	330	59.0	332	58.7	<b>331</b>	<b>58.9</b>	302	64.5	<b>302</b>	<b>64.6</b>	300	65.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

Turbo Mode enabled in BIOS  
 Turbo Boost set to Traditional in BIOS  
 Power C-State enabled in BIOS  
 Data Reuse disabled in BIOS  
 Demand Scrub disabled in BIOS



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation	<b>SPECfp2006 =</b>	<b>63.2</b>
IBM System x3650 M3 (Intel Xeon X5690)	<b>SPECfp_base2006 =</b>	<b>59.2</b>
CPU2006 license: 11	Test date:	Mar-2011
Test sponsor: IBM Corporation	Hardware Availability:	Feb-2011
Tested by: IBM Corporation	Software Availability:	Jan-2011

## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

    icc -m64

C++ benchmarks:

    icpc -m64

Fortran benchmarks:

    ifort -m64

Benchmarks using both Fortran and C:

    icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
    433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
    444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
    470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
    -ansi-alias

C++ benchmarks:

    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

**SPECfp2006 = 63.2**

IBM System x3650 M3 (Intel Xeon X5690)

**SPECfp\_base2006 = 59.2**

CPU2006 license: 11

Test date: Mar-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011

## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation	<b>SPECfp2006 =</b>	<b>63.2</b>
IBM System x3650 M3 (Intel Xeon X5690)	<b>SPECfp_base2006 =</b>	<b>59.2</b>
<b>CPU2006 license:</b> 11	<b>Test date:</b>	Mar-2011
<b>Test sponsor:</b> IBM Corporation	<b>Hardware Availability:</b>	Feb-2011
<b>Tested by:</b> IBM Corporation	<b>Software Availability:</b>	Jan-2011

## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

465.tonto: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfss/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfss-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xsSE4 .2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp2006 = 63.2

IBM System x3650 M3 (Intel Xeon X5690)

SPECfp\_base2006 = 59.2

CPU2006 license: 11

Test date: Mar-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 15:46:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 March 2011.