



# SPEC® CFP2006 Result

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**Acer Incorporated**  
**Acer AT110 F2 (Xeon E3-1280)**

**SPECfp®2006 = 61.0**  
**SPECfp\_base2006 = 58.5**

CPU2006 license: 97

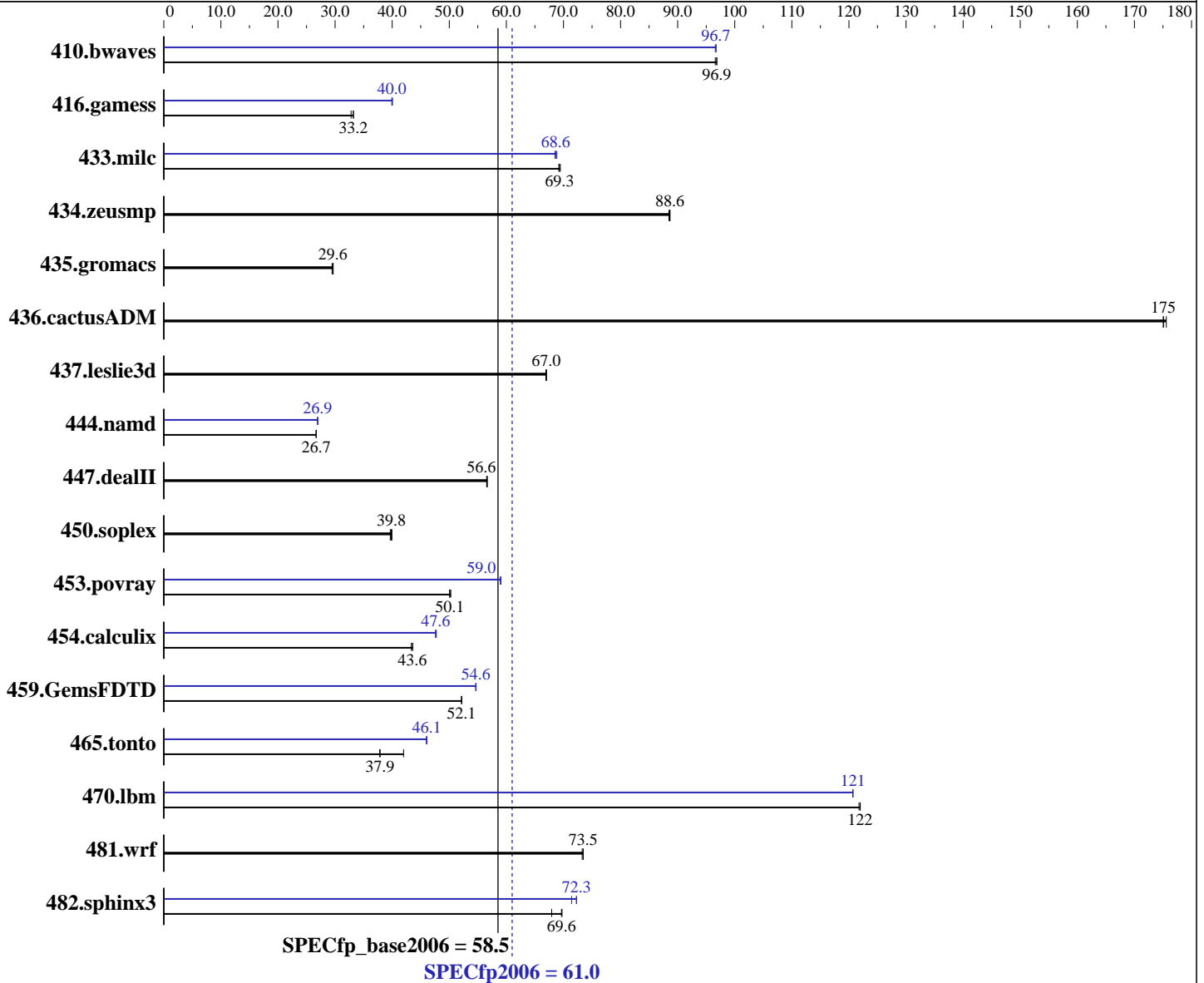
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Apr-2011

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E3-1280  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1 ,  
 Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for  
 applications running on Intel 64  
 Version 12 Alpha Build 20110105  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9,ECC)  
Disk Subsystem: 1 x 640 GB SATA, 7200 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	140	96.9	<b>140</b>	<b>96.9</b>	141	96.6	141	96.7	<b>141</b>	<b>96.7</b>	141	96.6
416.gamess	597	32.8	590	33.2	<b>590</b>	<b>33.2</b>	489	40.1	490	39.9	<b>490</b>	<b>40.0</b>
433.milc	132	69.4	133	69.2	<b>132</b>	<b>69.3</b>	133	68.8	134	68.5	<b>134</b>	<b>68.6</b>
434.zeusmp	<b>103</b>	<b>88.6</b>	103	88.6	103	88.5	<b>103</b>	<b>88.6</b>	103	88.6	103	88.5
435.gromacs	<b>241</b>	<b>29.6</b>	242	29.5	241	29.7	<b>241</b>	<b>29.6</b>	242	29.5	241	29.7
436.cactusADM	<b>68.3</b>	<b>175</b>	68.1	176	68.3	175	<b>68.3</b>	<b>175</b>	68.1	176	68.3	175
437.leslie3d	<b>140</b>	<b>67.0</b>	140	67.0	140	66.9	<b>140</b>	<b>67.0</b>	140	67.0	140	66.9
444.namd	301	26.7	<b>301</b>	<b>26.7</b>	300	26.7	298	27.0	298	26.9	<b>298</b>	<b>26.9</b>
447.dealII	202	56.5	<b>202</b>	<b>56.6</b>	202	56.7	202	56.5	<b>202</b>	<b>56.6</b>	202	56.7
450.soplex	209	40.0	210	39.7	<b>209</b>	<b>39.8</b>	209	40.0	210	39.7	<b>209</b>	<b>39.8</b>
453.povray	<b>106</b>	<b>50.1</b>	106	50.3	106	50.0	<b>90.2</b>	<b>59.0</b>	90.3	58.9	90.1	59.1
454.calculix	<b>189</b>	<b>43.6</b>	190	43.3	189	43.6	173	47.6	<b>173</b>	<b>47.6</b>	173	47.7
459.GemsFDTD	203	52.2	<b>203</b>	<b>52.1</b>	203	52.1	194	54.6	<b>194</b>	<b>54.6</b>	194	54.7
465.tonto	<b>260</b>	<b>37.9</b>	260	37.8	234	42.0	<b>214</b>	<b>46.1</b>	214	46.0	213	46.1
470.lbm	113	122	<b>113</b>	<b>122</b>	113	122	114	121	114	121	<b>114</b>	<b>121</b>
481.wrf	152	73.3	<b>152</b>	<b>73.5</b>	152	73.5	152	73.3	<b>152</b>	<b>73.5</b>	152	73.5
482.sphinx3	279	69.8	<b>280</b>	<b>69.6</b>	287	67.9	<b>270</b>	<b>72.3</b>	270	72.3	273	71.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Large pages were disabled for this run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M  
Binaries compiled on RHEL5.5  
This result was measured on the Gateway GT110 F2.  
The Acer AT110 F2 is electronically equivalent.



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias



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## Peak Compiler Invocation

### C benchmarks:

icc -m64

### C++ benchmarks:

icpc -m64

### Fortran benchmarks:

ifort -m64

### Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

### C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -parallel  
-ansi-alias -static -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

- 410.bwaves: `-xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static`
- 416.gamess: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static`
- 434.zeusmp: `basepeak = yes`
- 437.leslie3d: `basepeak = yes`
- 459.GemsFDTD: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`
- 465.tonto: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

- 435.gromacs: `basepeak = yes`
- 436.cactusADM: `basepeak = yes`
- 454.calculix: `-xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`
- 481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at

- <http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>
- <http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

- <http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>
- <http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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