



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint®_rate2006 = 322

Gateway GW2000h-GW175h F1 (AMD Opteron 6164 HE)

SPECint_rate_base2006 = 282

CPU2006 license: 97

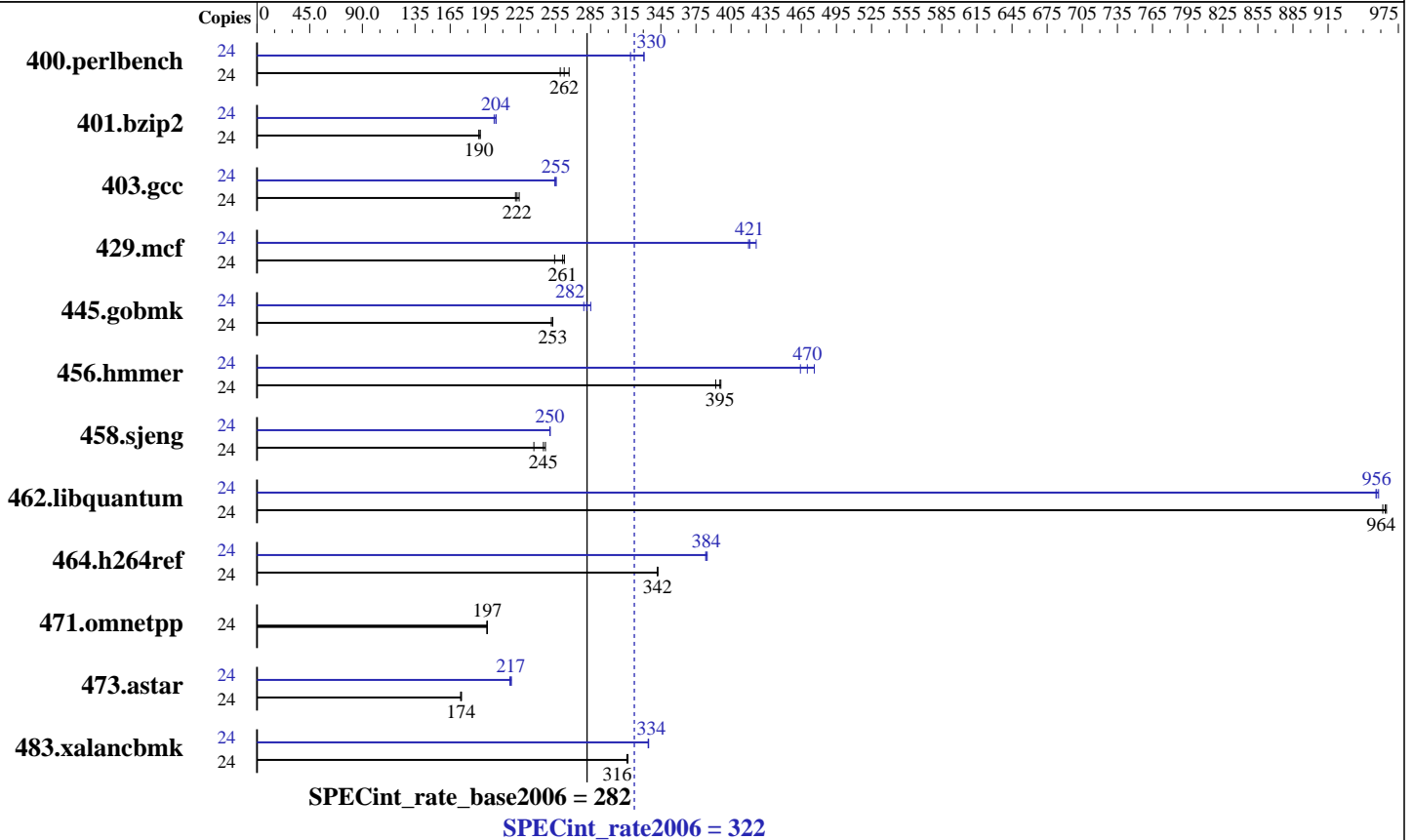
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Aug-2010

Software Availability: Jul-2010



Hardware

CPU Name: AMD Opteron 6164 HE
 CPU Characteristics:
 CPU MHz: 1700
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 6 cores
 Other Cache: None
 Memory: 64 GB (16 x 4 GB 2Rx8 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 SPI
 Kernel 2.6.32.12-0.7-default
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multiuser)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap 8.1 32-bit Library for Linux



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	879	267	905	259	894	262	24	735	319	710	330	709	331
401.bzip2	24	1214	191	1222	190	1221	190	24	1143	203	1135	204	1135	204
403.gcc	24	875	221	870	222	863	224	24	760	254	756	256	756	255
429.mcf	24	861	254	833	263	838	261	24	513	426	521	420	520	421
445.gobmk	24	997	253	1001	251	997	253	24	894	282	883	285	902	279
456.hammer	24	565	396	566	395	571	392	24	482	464	470	476	476	470
458.sjeng	24	1187	245	1178	246	1228	237	24	1159	250	1161	250	1161	250
462.libquantum	24	516	964	517	962	515	965	24	520	956	519	958	520	956
464.h264ref	24	1552	342	1551	343	1553	342	24	1384	384	1381	385	1386	383
471.omnetpp	24	762	197	762	197	764	196	24	762	197	762	197	764	196
473.astar	24	968	174	967	174	964	175	24	775	217	780	216	777	217
483.xalancbmk	24	523	316	524	316	523	317	24	496	334	495	334	495	335

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr_hugepages=10800 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

General Notes

Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "450"
LD_LIBRARY_PATH = "/usr/cpu2006/amd1002-rate-libs-revC/64:/usr/cpu2006/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>
Binaries were compiled on SLES10 SP2 with binutils 2.18
This result was measured on the Gateway GW2000h-GW175h F1.
The Acer AW2000h-AW175h F1 and Gateway GW2000h-GW175h F1 are electronically equivalent.



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Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

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Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
 -OPT:unroll_times_max=8 -OPT:unroll_size=256
 -OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
 -CG:local_sched_alg=1 -CG:unroll_fb_req=on
 -HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
 -OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
 -LNO:prefetch Ahead=10 -CG:cmp_peep=on -m32
 -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
 -CG:gcm=off -GRA:prioritize_by_density=on -m32
 -HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
 -OPT:unroll_times_max=8 -OPT:unroll_size=256
 -OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
 -IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
 -LNO:ignore_feedback=off -CG:p2align=on
 -CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
 -fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
 -OPT:alias=disjoint -OPT:unroll_times_max=8
 -OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
 -CG:local_sched_alg=1 -CG:cflow=0
 -CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
 -HP:bdt=2m:heap=2m

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Peak Optimization Flags (Continued)

```
458.sjeng: -march=barcelona -mso -fb_create fbdata(pass 1)
          -fb_opt fbdata(pass 2) -O3 -ipa -LNO:ignore_feedback=off
          -LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2
          -IPA:pu_reorder=2 -CG:ptr_load_use=0
          -OPT:unroll_times_max=8 -INLINE:aggressive=on
```

```
462.libquantum: -march=barcelona -mso -Ofast -LNO:pf2=0 -CG:gcm=off
               -CG:use_prefetchnta=on -CG:cmp_peep=on -WOPT:aggstr=0
               -HP:bdt=2m:heap=2m -OPT:alias=disjoint
               -INLINE:aggressive=on -IPA:space=1000 -IPA:plimit=20000
```

```
464.h264ref: -march=barcelona -mso -fb_create fbdata(pass 1)
             -fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000
             -OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0
             -CG:push_pop_int_saved_regs=off
```

C++ benchmarks:

```
471.omnetpp: basepeak = yes
```

```
473.astar: -march=barcelona -mso -fb_create fbdata(pass 1)
           -fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
           -WOPT:if_conv=0 -GRA:optimize_boundary=on
           -OPT:alias=disjoint -INLINE:aggressive=on
           -IPA:small_pu=3000 -IPA:plimit=3000 -m32
           -HP:bdt=2m:heap=2m
```

```
483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32
              -CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off
              -fno-emit-exceptions
              -L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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