SPEC® CFP2006 Result

Supermicro

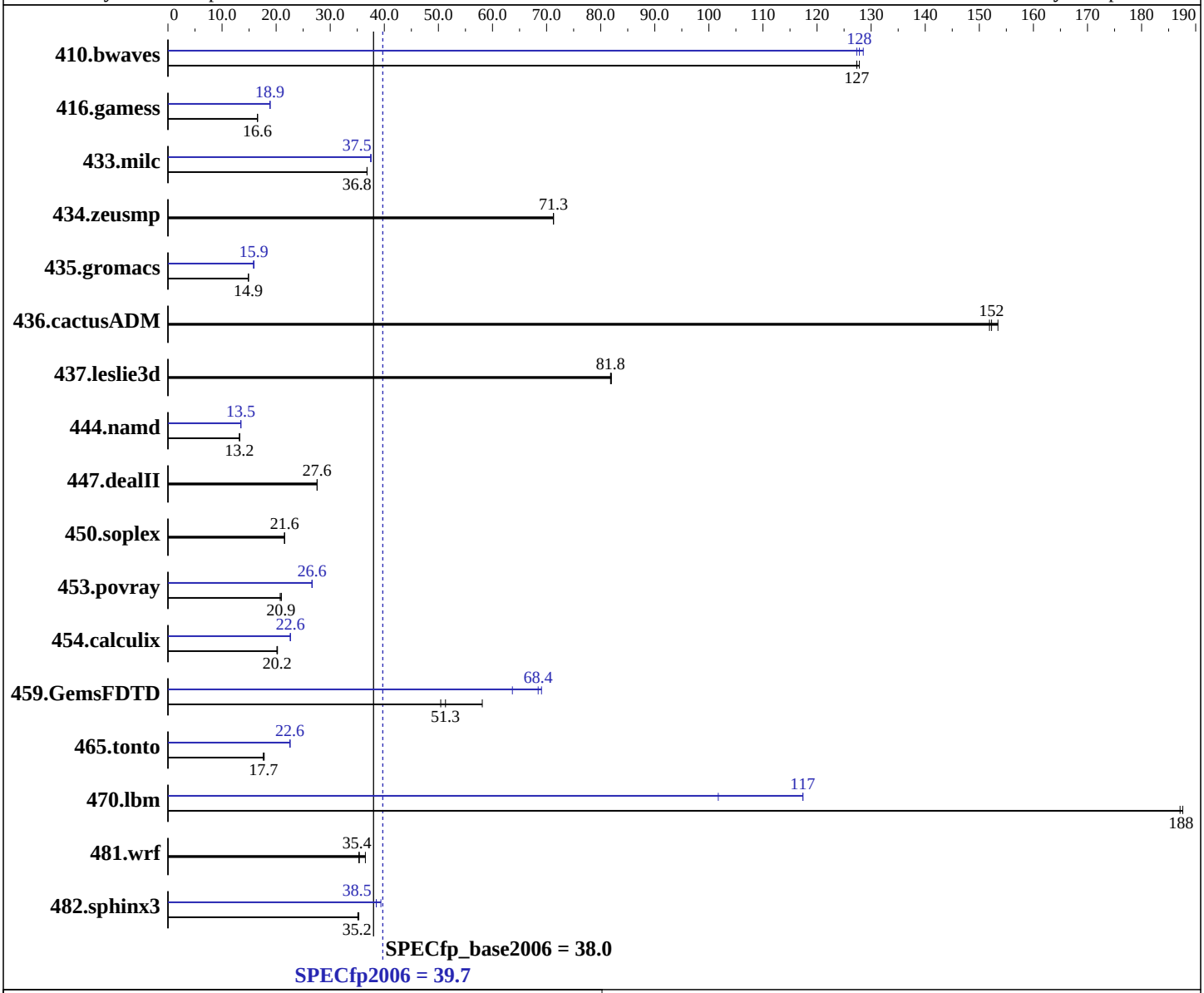
SuperServer 1026T-6RTF+ (X8DTU-6TF+, Intel Xeon E5607)

SPECfp®2006 = 39.7

SPECfp_base2006 = 38.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Xeon E5607
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1 Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12 Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
Auto Parallel:	Yes
File System:	ext3

Continued on next page

SPEC CFP2006 Result

Supermicro

SuperServer 1026T-6RTF+ (X8DTU-6TF+, Intel Xeon E5607)

SPECfp2006 = 39.7

SPECfp_base2006 = 38.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011

L3 Cache:	8 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	64-bit
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-8500R-9, ECC)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 600 GB SAS 6.0 Gb/s, 10000 RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	106	128	**107**	**127**	107	127	107	127	106	129	**106**	**128**
416.gamess	1183	16.6	1183	16.6	**1183**	**16.6**	**1038**	**18.9**	1038	18.9	1037	18.9
433.milc	249	36.8	**249**	**36.8**	249	36.8	**245**	**37.5**	245	37.5	245	37.5
434.zeusmp	128	71.3	**128**	**71.3**	128	71.3	128	71.3	**128**	**71.3**	128	71.3
435.gromacs	**480**	**14.9**	480	14.9	480	14.9	449	15.9	452	15.8	**450**	**15.9**
436.cactusADM	77.9	153	78.7	152	**78.5**	**152**	77.9	153	78.7	152	**78.5**	**152**
437.leslie3d	**115**	**81.8**	115	81.8	115	82.0	**115**	**81.8**	115	81.8	115	82.0
444.namd	606	13.2	606	13.2	**606**	**13.2**	595	13.5	595	13.5	**595**	**13.5**
447.dealII	**415**	**27.6**	415	27.6	415	27.6	**415**	**27.6**	415	27.6	415	27.6
450.soplex	387	21.6	387	21.6	**387**	**21.6**	387	21.6	387	21.6	**387**	**21.6**
453.povray	**254**	**20.9**	256	20.7	254	21.0	200	26.6	**200**	**26.6**	199	26.7
454.calculix	**409**	**20.2**	409	20.2	407	20.3	364	22.6	365	22.6	**364**	**22.6**
459.GemsFDTD	210	50.4	183	58.1	**207**	**51.3**	**155**	**68.4**	167	63.7	154	69.1
465.tonto	558	17.6	553	17.8	**557**	**17.7**	436	22.6	**436**	**22.6**	436	22.6
470.lbm	**73.2**	**188**	73.4	187	73.2	188	**117**	**117**	117	117	135	102
481.wrf	316	35.3	**316**	**35.4**	306	36.5	316	35.3	**316**	**35.4**	306	36.5
482.sphinx3	552	35.3	555	35.1	**554**	**35.2**	495	39.4	**506**	**38.5**	506	38.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
Hugepages was enabled with the following:
nodev   /mnt/hugepages   hugetlbfs  defaults   0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
```

Platform Notes

```
Fan speed set to Full Speed and Data Reuse Optimization disabled in BIOS Setup.
```

General Notes

```
OMP_NUM_THREADS set to number of cores
Binaries compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
     434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -parallel -ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.html

You can also download the XML flags sources by saving the following links:

```
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.xml
```


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 20:57:53 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 May 2011.