



SPEC® CINT2006 Result

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IBM Corporation

SPECint®_rate2006 = 236

IBM System x3690 X5 (Intel Xeon E7-2803)

SPECint_rate_base2006 = 218

CPU2006 license: 11

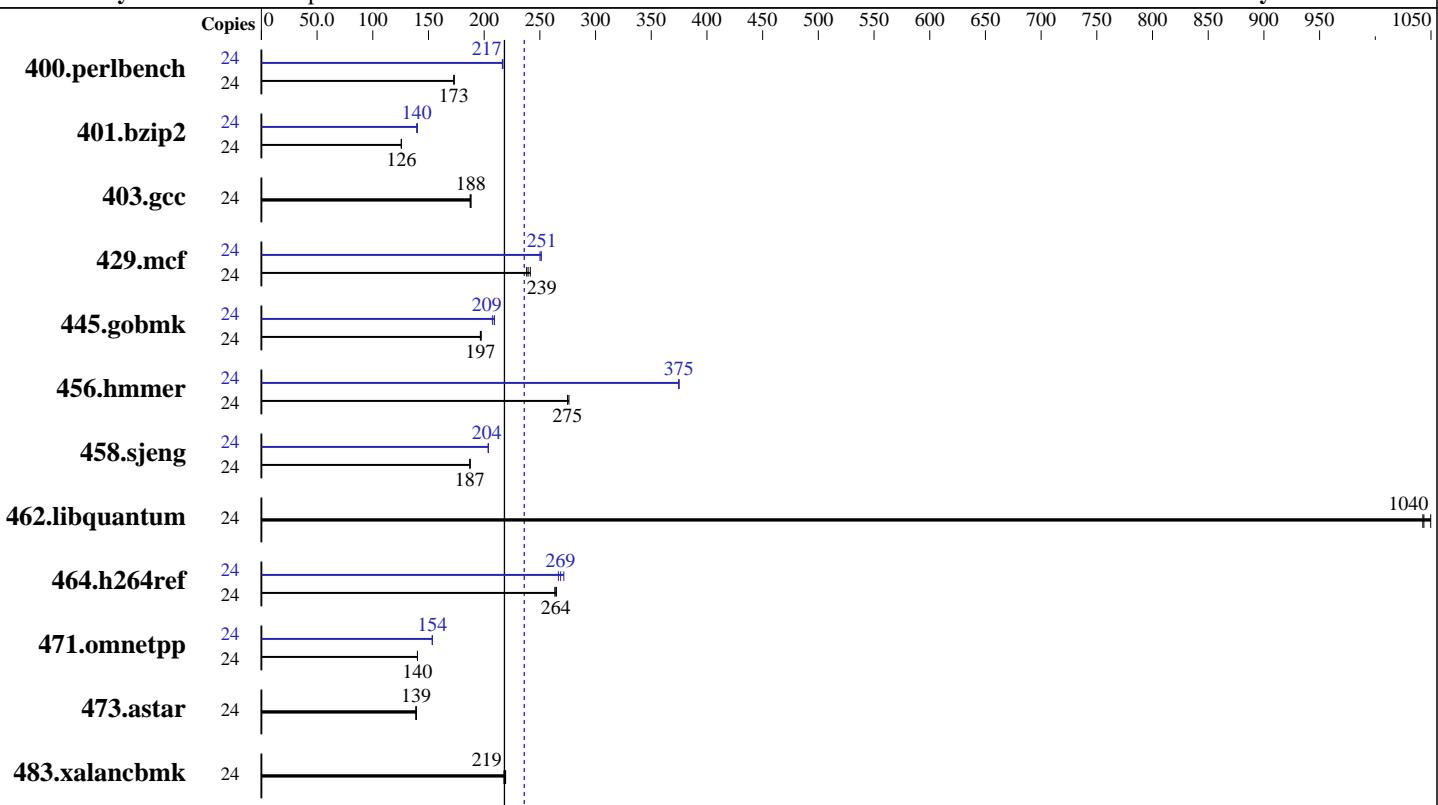
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: May-2011

Hardware Availability: May-2011

Software Availability: Jan-2011



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Hardware

CPU Name:	Intel Xeon E7-2803
CPU Characteristics:	
CPU MHz:	1733
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (32 x 8 GB 4Rx8 PC3-8500R-7, ECC, running at 800 MHz)
Disk Subsystem:	1 x 300 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	1355	173	1357	173	<u>1356</u>	<u>173</u>	24	<u>1082</u>	<u>217</u>	1082	217	1084	216
401.bzip2	24	1844	126	<u>1843</u>	<u>126</u>	1841	126	24	<u>1659</u>	<u>140</u>	1654	140	1659	140
403.gcc	24	<u>1028</u>	<u>188</u>	1027	188	1031	187	24	<u>1028</u>	<u>188</u>	1027	188	1031	187
429.mcf	24	907	241	<u>914</u>	<u>239</u>	919	238	24	<u>871</u>	<u>251</u>	875	250	871	251
445.gobmk	24	1281	197	<u>1279</u>	<u>197</u>	1276	197	24	1214	207	1203	209	<u>1204</u>	<u>209</u>
456.hammer	24	811	276	<u>814</u>	<u>275</u>	815	275	24	598	375	<u>597</u>	<u>375</u>	597	375
458.sjeng	24	<u>1552</u>	<u>187</u>	1549	187	1552	187	24	<u>1424</u>	<u>204</u>	1424	204	1427	203
462.libquantum	24	477	1040	474	1050	<u>477</u>	<u>1040</u>	24	477	1040	474	1050	<u>477</u>	<u>1040</u>
464.h264ref	24	2016	263	<u>2010</u>	<u>264</u>	2005	265	24	1992	267	<u>1977</u>	<u>269</u>	1956	272
471.omnetpp	24	<u>1071</u>	<u>140</u>	1070	140	1071	140	24	978	153	<u>977</u>	<u>154</u>	977	154
473.astar	24	1215	139	<u>1212</u>	<u>139</u>	1211	139	24	1215	139	<u>1212</u>	<u>139</u>	1211	139
483.xalancbmk	24	760	218	755	219	<u>756</u>	<u>219</u>	24	760	218	755	219	<u>756</u>	<u>219</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 14400 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

General Notes

Binaries compiled on RHEL 5.5

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT
```

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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