



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 47.1

Intel DH67BLB3 Motherboard (Intel Core i7-2600)

SPECint_base2006 = 44.6

CPU2006 license: 13

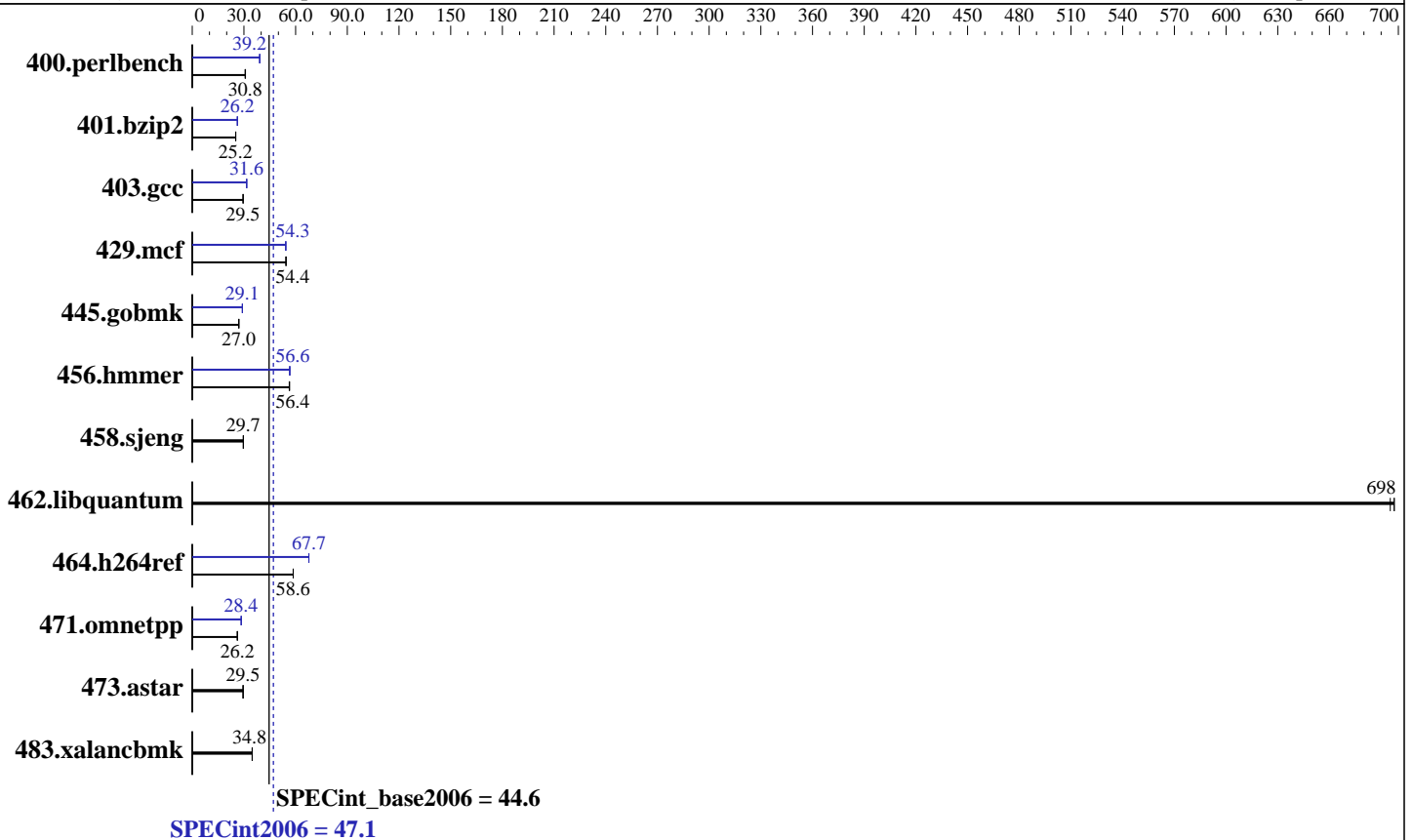
Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i7-2600
 CPU Characteristics: Intel Turbo Boost Technology up to 3.8 GHz
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.3.176 Build 20110309
 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	318	30.7	<u>317</u>	<u>30.8</u>	317	30.9	250	39.1	249	39.2	<u>249</u>	<u>39.2</u>
401.bzip2	<u>383</u>	<u>25.2</u>	383	25.2	382	25.3	368	26.2	368	26.2	<u>368</u>	<u>26.2</u>
403.gcc	<u>273</u>	<u>29.5</u>	273	29.5	272	29.6	<u>255</u>	<u>31.6</u>	255	31.6	254	31.6
429.mcf	<u>168</u>	<u>54.4</u>	168	54.4	167	54.5	168	54.3	168	54.3	<u>168</u>	<u>54.3</u>
445.gobmk	<u>388</u>	<u>27.0</u>	388	27.0	388	27.1	<u>361</u>	<u>29.1</u>	361	29.1	361	29.1
456.hmmer	166	56.3	<u>166</u>	<u>56.4</u>	165	56.5	<u>165</u>	<u>56.6</u>	165	56.5	165	56.7
458.sjeng	408	29.6	408	29.7	<u>408</u>	<u>29.7</u>	408	29.6	408	29.7	<u>408</u>	<u>29.7</u>
462.libquantum	29.8	695	29.7	698	<u>29.7</u>	<u>698</u>	29.8	695	29.7	698	<u>29.7</u>	<u>698</u>
464.h264ref	378	58.6	<u>378</u>	<u>58.6</u>	378	58.6	327	67.7	<u>327</u>	<u>67.7</u>	327	67.7
471.omnetpp	239	26.1	238	26.3	<u>238</u>	<u>26.2</u>	220	28.4	<u>220</u>	<u>28.4</u>	220	28.5
473.astar	238	29.5	237	29.6	<u>238</u>	<u>29.5</u>	238	29.5	237	29.6	<u>238</u>	<u>29.5</u>
483.xalancbmk	198	34.8	198	34.8	<u>198</u>	<u>34.8</u>	198	34.8	198	34.8	<u>198</u>	<u>34.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_P64
 464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTPYPES
 471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
 473.astar: -DSPEC_CPU_P64
 483.xalancbmk: -DSPEC_CPU_P64 -Qoption, cpp, --no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32
 /F512000000

C++ benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
 -Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
 -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32
 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml>

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For other inquiries, please contact webmaster@spec.org.

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