



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®2006 = 46.7

Intel DH67BLB3 Motherboard (Intel Core i3-2120)

SPECfp_base2006 = 45.2

CPU2006 license: 13

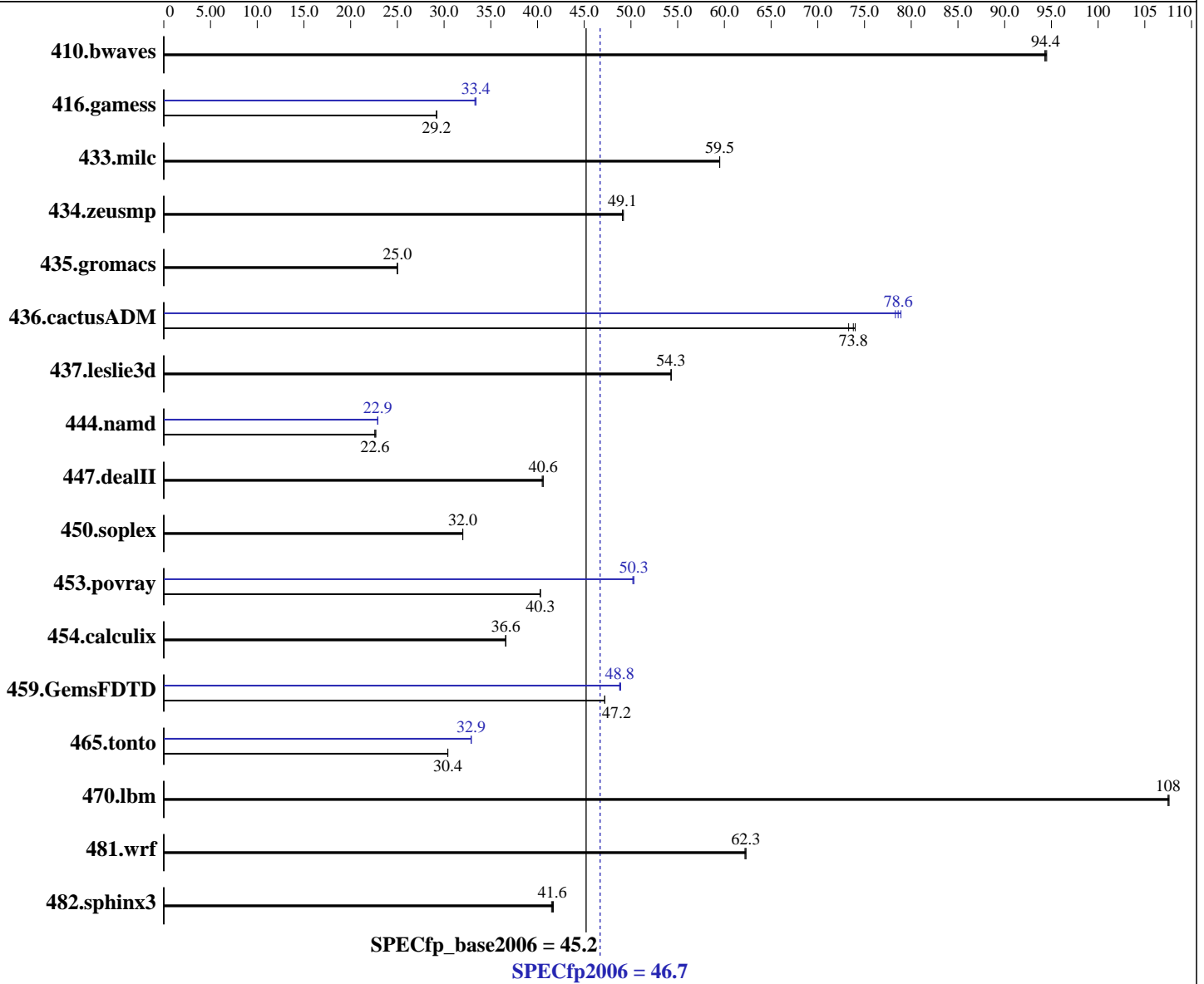
Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i3-2120
 CPU Characteristics:
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Intel Visual Fortran Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)
 Auto Parallel: Yes
 File System: NTFS

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L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	144	94.3	144	94.5	144	94.4	144	94.3	144	94.5	144	94.4
416.gamess	670	29.2	670	29.2	670	29.2	587	33.4	587	33.4	588	33.3
433.milc	154	59.5	154	59.5	154	59.5	154	59.5	154	59.5	154	59.5
434.zeusmp	185	49.2	186	49.1	185	49.1	185	49.2	186	49.1	185	49.1
435.gromacs	285	25.0	285	25.0	285	25.0	285	25.0	285	25.0	285	25.0
436.cactusADM	163	73.3	162	73.8	162	74.0	153	78.3	152	78.6	151	78.9
437.leslie3d	173	54.3	173	54.3	173	54.3	173	54.3	173	54.3	173	54.3
444.namd	354	22.7	354	22.6	354	22.6	350	22.9	350	22.9	350	22.9
447.dealII	282	40.5	282	40.6	282	40.6	282	40.5	282	40.6	282	40.6
450.soplex	261	32.0	261	32.0	261	32.0	261	32.0	261	32.0	261	32.0
453.povray	132	40.3	132	40.3	132	40.3	106	50.3	106	50.3	106	50.2
454.calculix	225	36.6	225	36.6	225	36.6	225	36.6	225	36.6	225	36.6
459.GemsFDTD	225	47.2	225	47.2	225	47.2	217	48.9	217	48.8	217	48.8
465.tonto	324	30.4	324	30.4	324	30.4	299	32.9	299	32.9	299	32.9
470.lbm	128	108	128	108	128	108	128	108	128	108	128	108
481.wrf	179	62.3	180	62.2	179	62.3	179	62.3	180	62.2	179	62.3
482.sphinx3	469	41.5	467	41.7	468	41.6	469	41.5	467	41.7	468	41.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
 PC Power and Cooling 1200W power supply
 OMP_NUM_THREADS set to number of processors cores
 KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qstd=c99

C++ benchmarks:
 icl -Qvc9

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
 437.leslie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 /F1000000000 shlw64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:

-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000



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Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 sh1W64M.lib
-link /FORCE:MULTIPLE

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep-
/F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel
/F1000000000

465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc
/F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2
-Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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