SPEC® CFP2006 Result

Cisco Systems

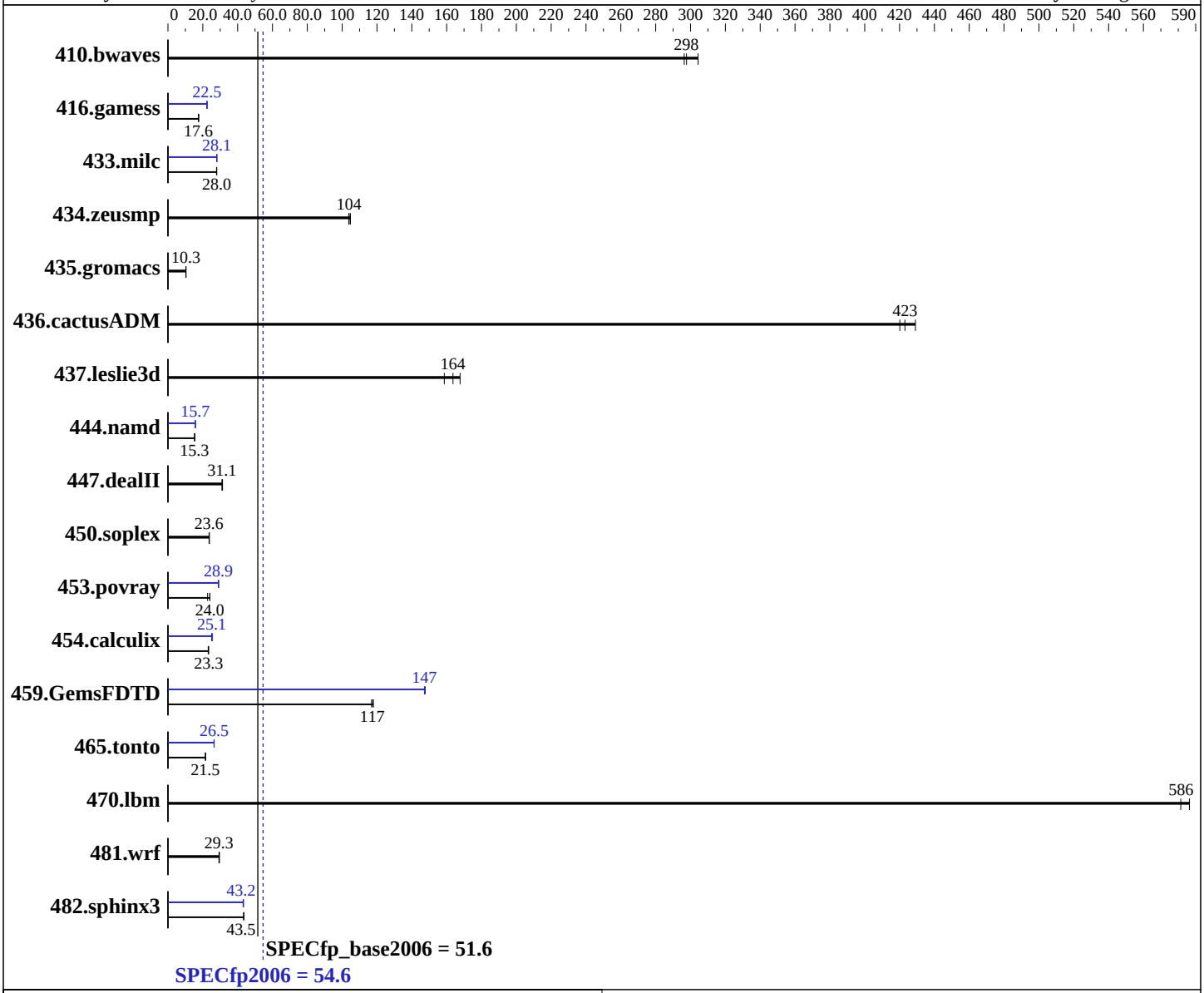
Cisco UCS C460 M2 (Intel Xeon E7-4860, 2.26 GHz)

SPECfp®2006 = 54.6

SPECfp_base2006 = 51.6

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Sep-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011



Hardware

CPU Name:	Intel Xeon E7-4860
CPU Characteristics:	Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz:	2266
FPU:	Integrated
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 beta Kernel 2.6.32-130.el6.x86_64
Compiler:	C/C++/Fortran: Version 12.0.1.116 of Intel Compiler XE Build 20101116
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 4Rx4 PC3-10600R-9, ECC, running at 1067 MHz)
Disk Subsystem: 146 GB SAS, 10K RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**45.7**	**298**	44.7	304	45.9	296	**45.7**	**298**	44.7	304	45.9	296
416.gamess	**1115**	**17.6**	1113	17.6	1116	17.5	870	22.5	877	22.3	**871**	**22.5**
433.milc	328	28.0	**328**	**28.0**	329	27.9	327	28.1	327	28.0	**327**	**28.1**
434.zeusmp	87.7	104	**87.5**	**104**	86.9	105	87.7	104	**87.5**	**104**	86.9	105
435.gromacs	689	10.4	695	10.3	**692**	**10.3**	689	10.4	695	10.3	**692**	**10.3**
436.cactusADM	**28.2**	**423**	28.4	420	27.8	429	**28.2**	**423**	28.4	420	27.8	429
437.leslie3d	56.1	168	59.3	159	**57.5**	**164**	56.1	168	59.3	159	**57.5**	**164**
444.namd	522	15.4	**524**	**15.3**	524	15.3	510	15.7	**510**	**15.7**	510	15.7
447.dealII	365	31.3	368	31.1	**368**	**31.1**	365	31.3	368	31.1	**368**	**31.1**
450.soplex	353	23.6	**353**	**23.6**	351	23.8	353	23.6	**353**	**23.6**	351	23.8
453.povray	**222**	**24.0**	221	24.0	234	22.7	182	29.2	**184**	**28.9**	184	28.9
454.calculix	354	23.3	**354**	**23.3**	357	23.1	322	25.6	330	25.0	**329**	**25.1**
459.GemsFDTD	90.8	117	**90.4**	**117**	90.0	118	**72.0**	**147**	71.8	148	72.0	147
465.tonto	456	21.6	**457**	**21.5**	457	21.5	371	26.5	**371**	**26.5**	372	26.4
470.lbm	23.6	581	23.4	586	**23.4**	**586**	23.6	581	23.4	586	**23.4**	**586**
481.wrf	381	29.3	**381**	**29.3**	378	29.6	381	29.3	**381**	**29.3**	378	29.6
482.sphinx3	448	43.5	447	43.6	**448**	**43.5**	449	43.4	452	43.1	**451**	**43.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/opt/cpu2006/smartheap:/opt/cpu2006/ic12.1-libs/ia32/:/opt/cpu2006/ic12.1-libs/intel64"
OMP_NUM_THREADS = "40"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory
 using RHEL5.5 with binutils-2.17.50.0.6-14.el5
 Stack size set to unlimited using "ulimit -s unlimited"
 Transparent Huge Pages enabled with:
```

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General Notes (Continued)

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings.20111118.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings.20111118.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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