



SPEC[®] CFP2006 Result

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Hewlett-Packard Company

SPECfp[®]_rate2006 = 1210

ProLiant DL980 G7 (2.0 GHz, Intel Xeon E7-2850)

SPECfp_rate_base2006 = 1170

CPU2006 license: 3

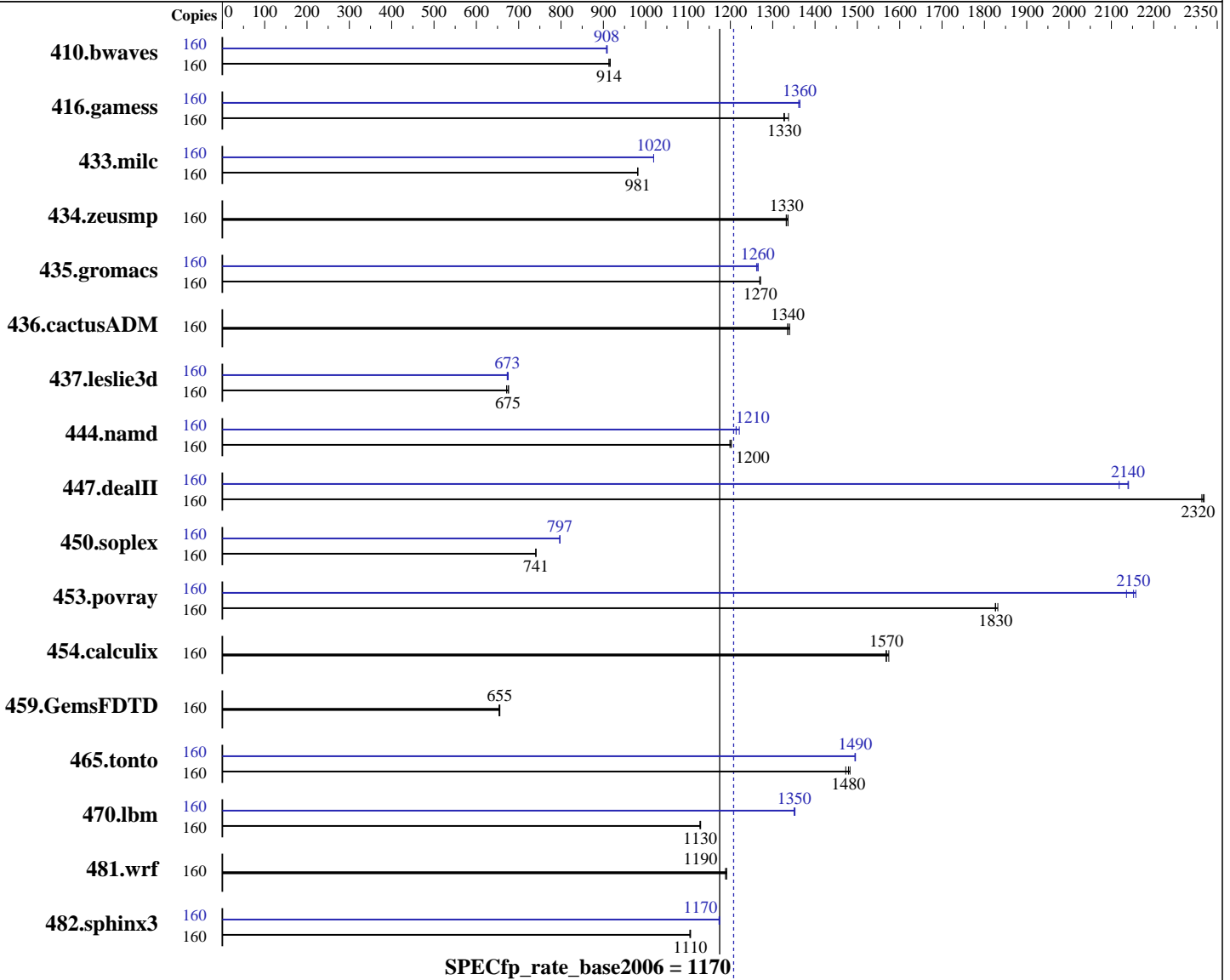
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Aug-2011

Hardware Availability: Aug-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E7-2850
 CPU Characteristics: Intel Turbo Boost Technology up to 2.4 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 80 cores, 8 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 4, 8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Compiler XE 2011 for IA-32 and Intel 64 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (128 x 8 GB 2Rx4 PC3L-10600R-9, ECC, running at 1066 MHz)
 Disk Subsystem: 2 x 146 GB 15 K SAS
 Other Hardware: 512 MB FBWC Module for P410i SmartArray

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	160	2373	916	2381	913	2380	914	160	2391	909	2395	908	2396	908
416.gamess	160	2359	1330	2342	1340	2362	1330	160	2297	1360	2296	1360	2301	1360
433.milc	160	1497	981	1497	981	1497	981	160	1442	1020	1442	1020	1442	1020
434.zeusmp	160	1093	1330	1092	1330	1090	1340	160	1093	1330	1092	1330	1090	1340
435.gromacs	160	900	1270	899	1270	899	1270	160	903	1270	905	1260	903	1260
436.cactusADM	160	1431	1340	1427	1340	1432	1340	160	1431	1340	1427	1340	1432	1340
437.leslie3d	160	2240	671	2224	676	2229	675	160	2233	673	2227	675	2235	673
444.namd	160	1068	1200	1070	1200	1067	1200	160	1057	1210	1057	1210	1051	1220
447.dealII	160	791	2310	790	2320	789	2320	160	855	2140	856	2140	864	2120
450.soplex	160	1802	741	1802	741	1802	741	160	1674	797	1674	797	1673	797
453.povray	160	466	1830	465	1830	466	1830	160	395	2150	394	2160	399	2140
454.calculix	160	838	1570	842	1570	841	1570	160	838	1570	842	1570	841	1570
459.GemsFDTD	160	2589	656	2598	654	2592	655	160	2589	656	2598	654	2592	655
465.tonto	160	1062	1480	1069	1470	1065	1480	160	1053	1490	1054	1490	1053	1500
470.lbm	160	1949	1130	1947	1130	1946	1130	160	1628	1350	1625	1350	1627	1350
481.wrf	160	1504	1190	1500	1190	1501	1190	160	1504	1190	1500	1190	1501	1190
482.sphinx3	160	2821	1110	2824	1100	2820	1110	160	2655	1170	2655	1170	2658	1170

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Hugepages were not configured on the system



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Platform Notes

BIOS Settings:
Power Regulator set to HP Static High Performance Mode

General Notes

Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml>

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For other inquiries, please contact webmaster@spec.org.

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