



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant DL380 G7
(2.40 GHz, Intel Xeon E5645)

SPECfp®2006 = **52.5**

SPECfp_base2006 = **50.3**

CPU2006 license: 3

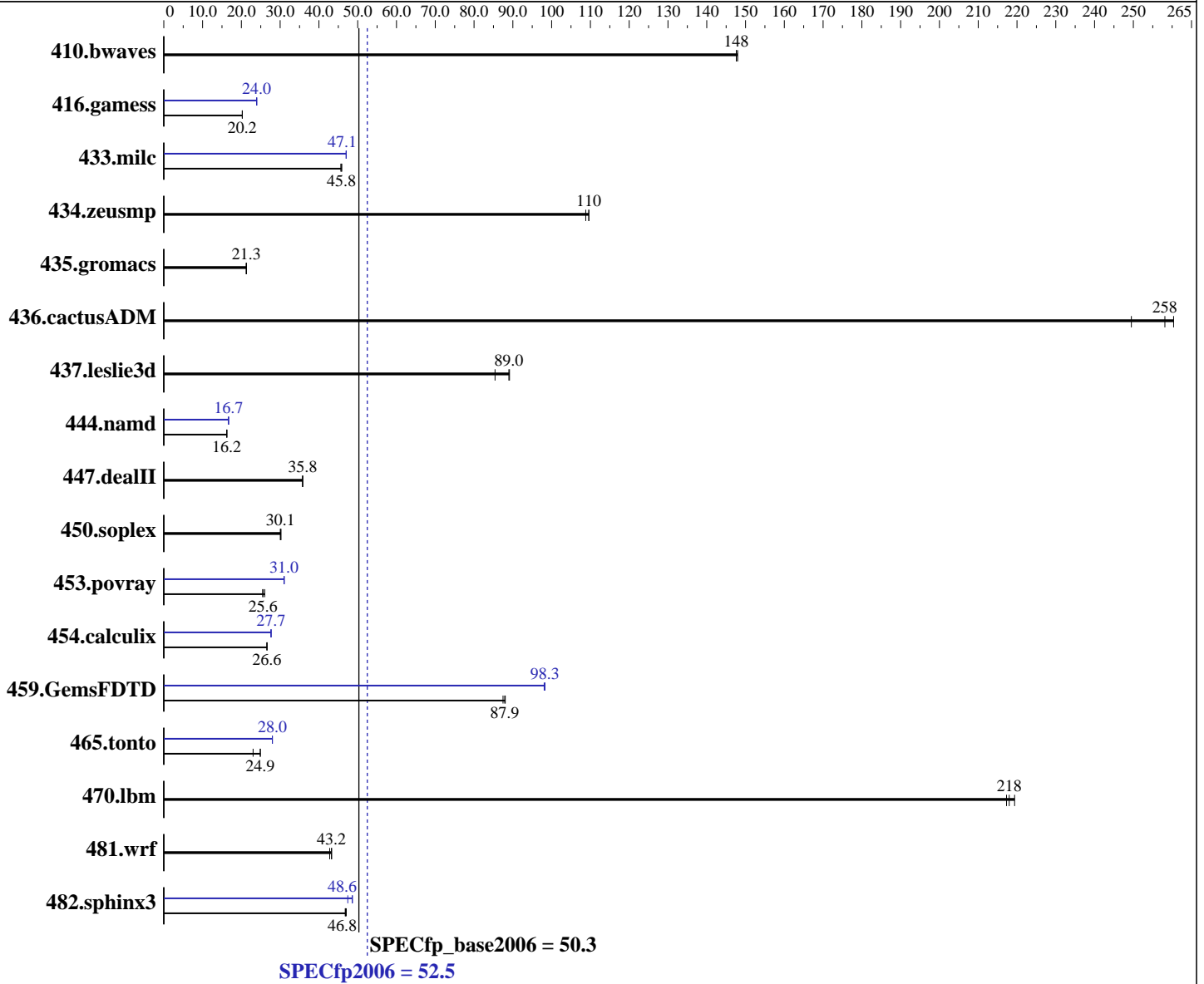
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Oct-2011

Hardware Availability: Mar-2010

Software Availability: Sep-2011



Hardware

CPU Name: Intel Xeon E5645
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++/Fortran: Version 12.1.0.225 of Intel Compiler XE Build 20110803
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 146 GB 15 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	91.8	148	92.0	148	92.0	148	91.8	148	92.0	148	92.0	148
416.gamess	967	20.2	966	20.3	968	20.2	818	23.9	817	24.0	817	24.0
433.milc	200	45.9	200	45.8	201	45.6	195	47.1	195	47.1	195	47.0
434.zeusmp	83.0	110	83.0	110	83.6	109	83.0	110	83.0	110	83.6	109
435.gromacs	335	21.3	335	21.3	336	21.2	335	21.3	335	21.3	336	21.2
436.cactusADM	47.9	249	46.3	258	45.9	260	47.9	249	46.3	258	45.9	260
437.leslie3d	105	89.1	110	85.4	106	89.0	105	89.1	110	85.4	106	89.0
444.namd	494	16.2	494	16.2	494	16.2	481	16.7	480	16.7	480	16.7
447.dealII	320	35.8	319	35.8	320	35.8	320	35.8	319	35.8	320	35.8
450.soplex	276	30.2	277	30.1	277	30.1	276	30.2	277	30.1	277	30.1
453.povray	204	26.0	208	25.6	209	25.5	171	31.0	172	31.0	171	31.0
454.calculix	310	26.6	310	26.6	310	26.6	298	27.7	298	27.7	298	27.6
459.GemsFDTD	121	87.5	121	88.0	121	87.9	108	98.1	108	98.3	108	98.3
465.tonto	395	24.9	427	23.0	396	24.9	351	28.0	352	28.0	351	28.0
470.lbm	63.0	218	63.2	217	62.6	219	63.0	218	63.2	217	62.6	219
481.wrf	258	43.2	261	42.8	258	43.3	258	43.2	261	42.8	258	43.3
482.sphinx3	414	47.1	416	46.8	417	46.8	411	47.4	401	48.6	401	48.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
```

Platform Notes

```
BIOS configuration:
HP Power Profile set to Maximum Performance
Intel Hyperthreading Options set to Disabled
Minimum Processor Idle Power State set to C6 State
Thermal Configuration set to Increased Cooling
Data Reuse set to Disabled
```



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/cpu2006/smartheap:/cpu2006/ic12.1-libs/ia32:/cpu2006/ic12.1-libs/intel64"

OMP_NUM_THREADS = "12"

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

`433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias`

`470.lbm: basepeak = yes`

`482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel`

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.xml>

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