



SPEC® CINT2006 Result

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Supermicro

SPECint®_rate2006 = 174

SuperServer 5017C-MF (X9SCL-F, Intel E3-1270)

SPECint_rate_base2006 = 168

CPU2006 license: 001176

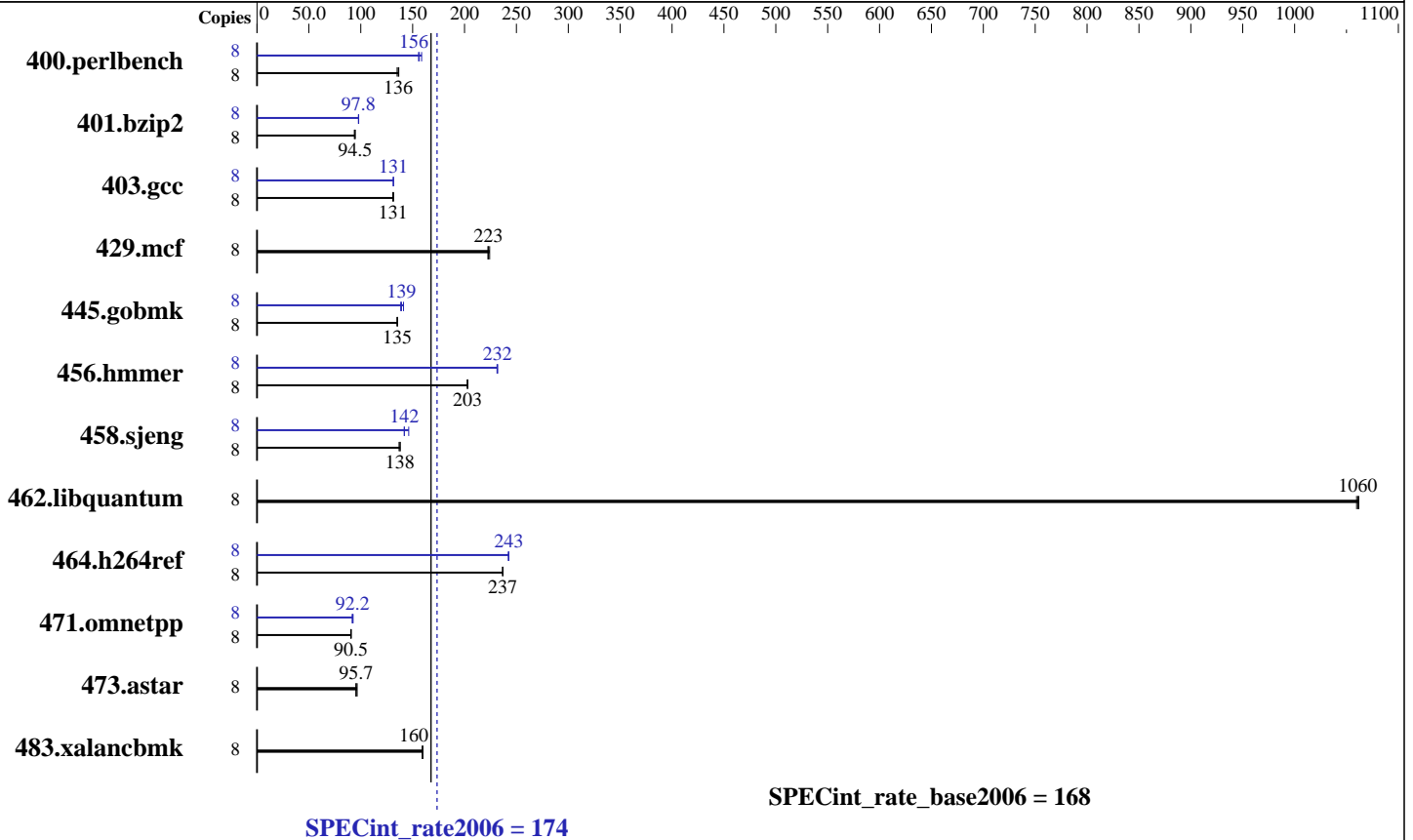
Test date: Nov-2011

Test sponsor: Supermicro

Hardware Availability: Apr-2011

Tested by: Supermicro

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E3-1270
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	572	137	579	135	574	136	8	502	156	493	159	500	156
401.bzip2	8	823	93.8	817	94.5	816	94.6	8	788	98.0	789	97.8	789	97.8
403.gcc	8	492	131	491	131	490	132	8	489	132	490	131	490	131
429.mcf	8	327	223	325	224	328	223	8	327	223	325	224	328	223
445.gobmk	8	620	135	622	135	621	135	8	606	139	603	139	594	141
456.hammer	8	367	203	368	203	368	203	8	322	232	323	231	322	232
458.sjeng	8	707	137	702	138	702	138	8	682	142	682	142	662	146
462.libquantum	8	156	1060	156	1060	156	1060	8	156	1060	156	1060	156	1060
464.h264ref	8	748	237	747	237	748	237	8	730	243	731	242	730	243
471.omnetpp	8	550	90.9	553	90.3	553	90.5	8	545	91.7	542	92.2	540	92.6
473.astar	8	582	96.5	589	95.3	587	95.7	8	582	96.5	589	95.3	587	95.7
483.xalancbmk	8	346	160	346	160	347	159	8	346	160	346	160	347	159

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Fan speed set to Full Speed in BIOS Setup

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries were compiled on RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
 -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
 -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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