



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel E3-1270)

SPECint®_rate2006 = 174

SPECint_rate_base2006 = 168

CPU2006 license: 001176

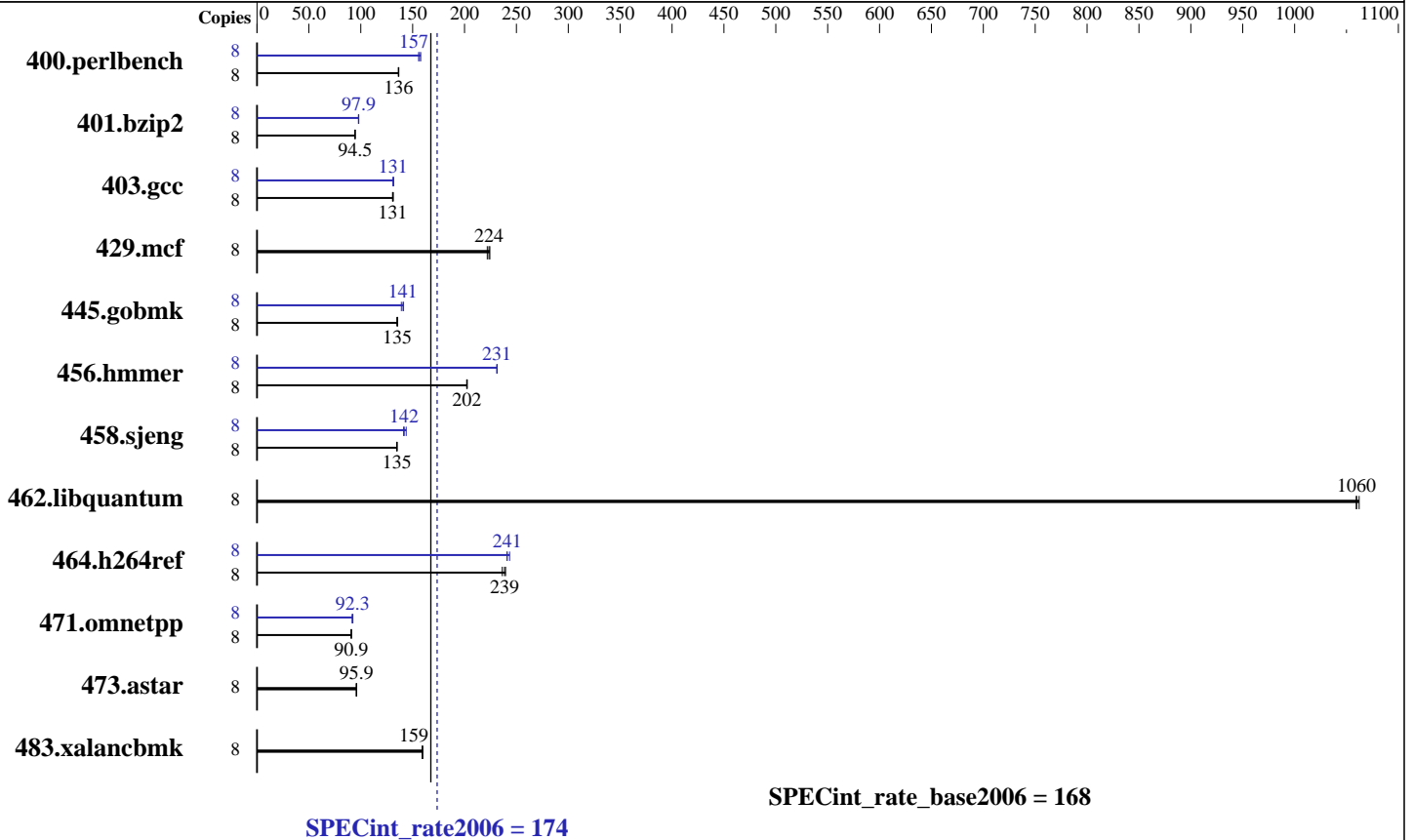
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2011

Hardware Availability: Jul-2011

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E3-1270
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA III, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel E3-1270)

SPECint_rate2006 = 174

SPECint_rate_base2006 = 168

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Nov-2011
Hardware Availability: Jul-2011
Software Availability: Oct-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	574	136	<u>573</u>	136	572	137	8	495	158	<u>499</u>	157	502	156
401.bzip2	8	<u>817</u>	<u>94.5</u>	815	94.7	818	94.4	8	790	97.7	<u>788</u>	<u>97.9</u>	787	98.1
403.gcc	8	<u>492</u>	131	490	131	493	131	8	491	131	<u>491</u>	131	489	132
429.mcf	8	329	222	325	224	326	224	8	329	222	325	224	326	224
445.gobmk	8	<u>620</u>	135	623	135	619	136	8	<u>597</u>	141	603	139	594	141
456.hammer	8	<u>369</u>	202	369	202	368	203	8	322	232	323	231	323	231
458.sjeng	8	<u>718</u>	135	719	135	716	135	8	685	141	673	144	682	142
462.libquantum	8	<u>156</u>	1060	156	1060	156	1060	8	<u>156</u>	1060	156	1060	156	1060
464.h264ref	8	749	236	<u>742</u>	239	739	240	8	727	243	735	241	734	241
471.omnetpp	8	550	91.0	<u>550</u>	90.9	551	90.7	8	546	91.6	541	92.4	<u>542</u>	92.3
473.astar	8	585	96.1	<u>585</u>	95.9	588	95.5	8	585	96.1	<u>585</u>	95.9	588	95.5
483.xalancbmk	8	346	160	347	159	346	159	8	346	160	347	159	346	159

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries were compiled on RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel E3-1270)

SPECint_rate2006 = 174

SPECint_rate_base2006 = 168

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2011

Hardware Availability: Jul-2011

Software Availability: Oct-2011

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmmer: icc -m64
458.sjeng: icc -m64
C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel E3-1270)

SPECint_rate2006 = 174

SPECint_rate_base2006 = 168

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2011

Hardware Availability: Jul-2011

Software Availability: Oct-2011

Peak Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 5037MC-H8TRF (X9SCD-F single node, Intel E3-1270)

SPECint_rate2006 = 174

SPECint_rate_base2006 = 168

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Nov-2011

Hardware Availability: Jul-2011

Software Availability: Oct-2011

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 01:25:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 December 2011.