



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 38.6

PowerEdge R620 (Intel Xeon E5-2620, 2.00 GHz)

SPECint\_base2006 = 36.4

CPU2006 license: 55

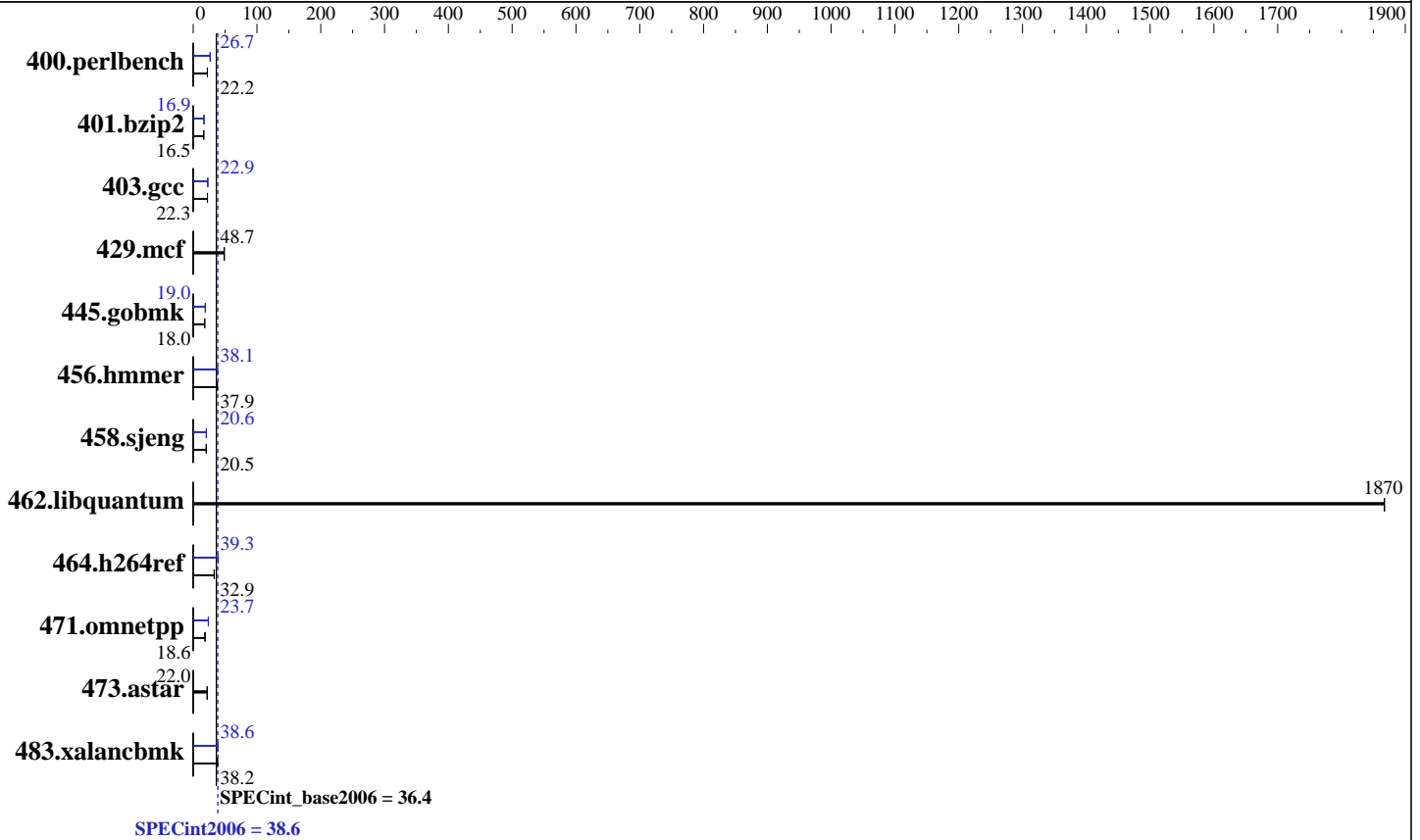
Test date: Mar-2012

Test sponsor: Dell Inc.

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



## Hardware

CPU Name: Intel Xeon E5-2620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 2 x 146 GB 15000 RPM SAS, RAID 0  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.19-default  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>440</b>	<b>22.2</b>	438	22.3	440	22.2	366	26.7	367	26.6	<b>367</b>	<b>26.7</b>
401.bzip2	586	16.5	<b>585</b>	<b>16.5</b>	585	16.5	573	16.8	572	16.9	<b>572</b>	<b>16.9</b>
403.gcc	<b>360</b>	<b>22.3</b>	359	22.4	360	22.3	352	22.8	<b>352</b>	<b>22.9</b>	352	22.9
429.mcf	187	48.7	<b>187</b>	<b>48.7</b>	187	48.7	187	48.7	<b>187</b>	<b>48.7</b>	187	48.7
445.gobmk	<b>582</b>	<b>18.0</b>	582	18.0	582	18.0	553	19.0	552	19.0	<b>553</b>	<b>19.0</b>
456.hammer	247	37.8	246	37.9	<b>246</b>	<b>37.9</b>	245	38.1	<b>245</b>	<b>38.1</b>	246	37.9
458.sjeng	590	20.5	591	20.5	<b>591</b>	<b>20.5</b>	588	20.6	<b>588</b>	<b>20.6</b>	588	20.6
462.libquantum	11.1	1870	<b>11.1</b>	<b>1870</b>	11.1	1870	11.1	1870	<b>11.1</b>	<b>1870</b>	11.1	1870
464.h264ref	668	33.1	675	32.8	<b>672</b>	<b>32.9</b>	565	39.2	<b>563</b>	<b>39.3</b>	562	39.4
471.omnetpp	337	18.6	<b>337</b>	<b>18.6</b>	337	18.5	<b>263</b>	<b>23.7</b>	264	23.7	263	23.8
473.astar	319	22.0	<b>319</b>	<b>22.0</b>	319	22.0	319	22.0	<b>319</b>	<b>22.0</b>	319	22.0
483.xalancbmk	183	37.8	<b>181</b>	<b>38.2</b>	181	38.2	177	38.9	<b>179</b>	<b>38.6</b>	179	38.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```

System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on unsvr Tue Mar 13 11:10:24 2012

```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 12
physical 0: cores 0 1 2 3 4 5

```

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## Platform Notes (Continued)

physical 1: cores 0 1 2 3 4 5  
cache size : 15360 KB

From /proc/meminfo  
MemTotal: 132089860 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
SUSE Linux Enterprise Server 11 (x86\_64)

From /etc/\*release\* /etc/\*version\*  
SuSE-release:  
SUSE Linux Enterprise Server 11 (x86\_64)  
VERSION = 11  
PATCHLEVEL = 2

uname -a:  
Linux unsvr 3.0.13-0.19-default #1 SMP Fri Feb 3 15:38:23 UTC 2012 (7f256ae)  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Mar 13 11:07 last=S

SPEC is set to: /root/CPU2006-1.2  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sdal ext3 265G 68G 183G 27% /

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"  
OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
The Dell PowerEdge R620 and  
the Bull NovaScale R440 F3 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge R620 model



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `-xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

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## Peak Optimization Flags (Continued)

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120328.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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