



# SPEC® CFP2006 Result

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**ACTION S.A.**

**SPECfp®\_rate2006 = 332**

ACTINA SOLAR 820 X5 (Intel Xeon E5-2630L)

**SPECfp\_rate\_base2006 = 326**

CPU2006 license: 9008

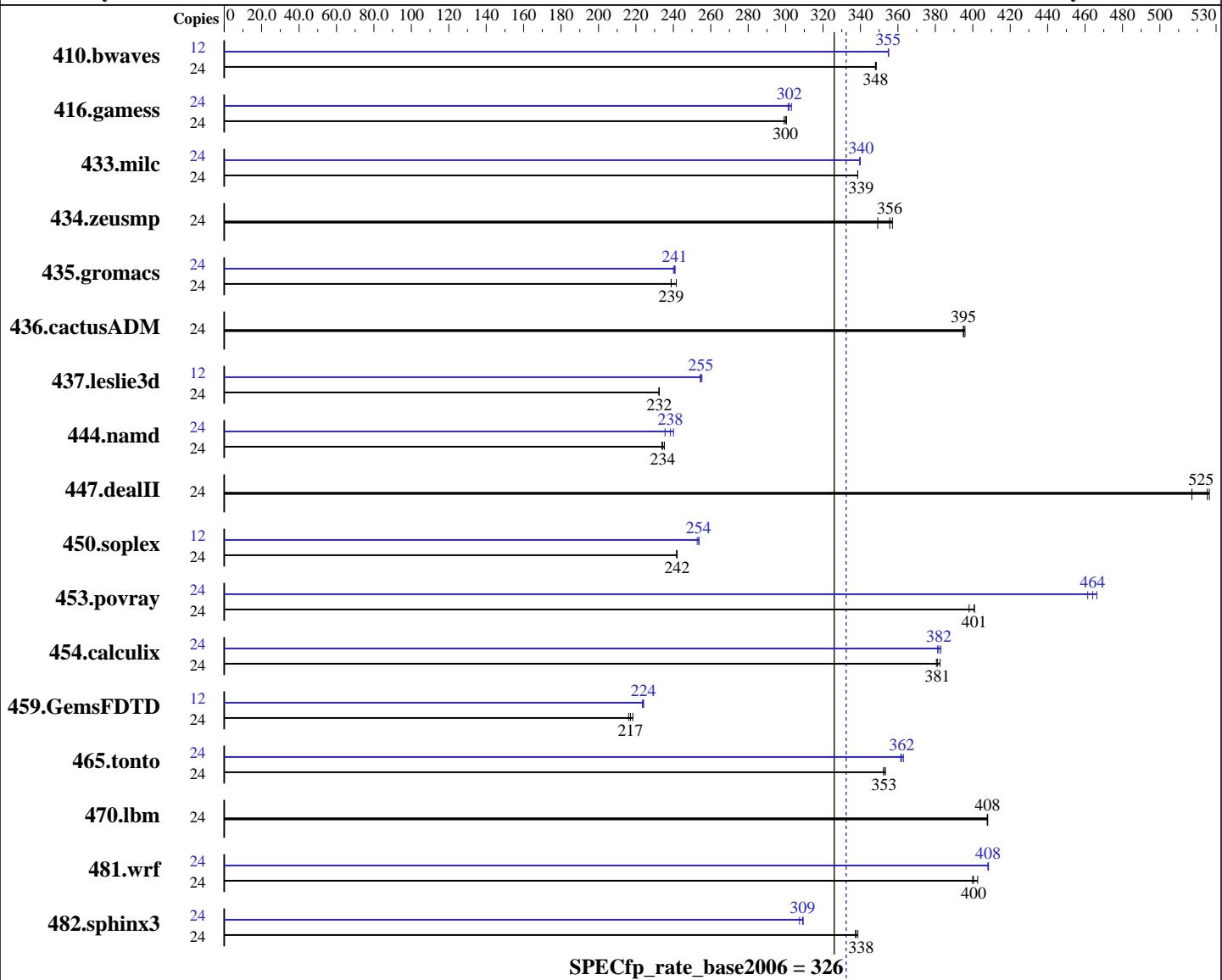
Test date: Apr-2012

Test sponsor: ACTION S.A.

Hardware Availability: Mar-2012

Tested by: ACTION S.A.

Software Availability: Oct-2011



## Hardware

CPU Name: Intel Xeon E5-2630L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64)  
 3.0.13-0.27-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE  
 for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran  
 Studio XE for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 15 MB I+D on chip per chip Other Cache: None Memory: 128 GB (16 x 8 GB 2Rx4 PC3-10600R-9, ECC) Disk Subsystem: 1 x 2 TB 7200 RPM SATA Other Hardware: None	Base Pointers: 32/64-bit Peak Pointers: 32/64-bit Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	936	348	937	348	<b>937</b>	<b>348</b>	12	<b>460</b>	<b>355</b>	459	355	460	355
416.gamess	24	1564	300	<b>1567</b>	<b>300</b>	1570	299	24	<b>1556</b>	<b>302</b>	1550	303	1559	301
433.milc	24	651	339	651	339	<b>651</b>	<b>339</b>	24	649	340	<b>648</b>	<b>340</b>	648	340
434.zeusmp	24	<b>614</b>	<b>356</b>	612	357	625	349	24	<b>614</b>	<b>356</b>	612	357	625	349
435.gromacs	24	709	242	<b>717</b>	<b>239</b>	718	239	24	711	241	714	240	<b>712</b>	<b>241</b>
436.cactusADM	24	<b>726</b>	<b>395</b>	726	395	725	396	24	<b>726</b>	<b>395</b>	726	395	725	396
437.leslie3d	24	<b>970</b>	<b>232</b>	971	232	970	232	12	443	254	442	255	<b>443</b>	<b>255</b>
444.namd	24	<b>821</b>	<b>234</b>	818	235	823	234	24	802	240	<b>807</b>	<b>238</b>	817	236
447.dealII	24	<b>523</b>	<b>525</b>	522	526	531	517	24	<b>523</b>	<b>525</b>	522	526	531	517
450.soplex	24	<b>827</b>	<b>242</b>	827	242	828	242	12	396	253	394	254	<b>395</b>	<b>254</b>
453.povray	24	<b>319</b>	<b>401</b>	318	401	321	398	24	<b>275</b>	<b>464</b>	274	466	277	461
454.calculix	24	<b>519</b>	<b>381</b>	520	381	518	382	24	519	381	<b>519</b>	<b>382</b>	517	383
459.GemsFDTD	24	1166	218	<b>1173</b>	<b>217</b>	1178	216	12	<b>568</b>	<b>224</b>	568	224	570	223
465.tonto	24	668	353	670	352	<b>670</b>	<b>353</b>	24	651	363	653	361	<b>652</b>	<b>362</b>
470.lbm	24	808	408	809	408	<b>808</b>	<b>408</b>	24	808	408	809	408	<b>808</b>	<b>408</b>
481.wrf	24	670	400	666	403	<b>670</b>	<b>400</b>	24	656	408	657	408	<b>657</b>	<b>408</b>
482.sphinx3	24	1387	337	<b>1385</b>	<b>338</b>	1381	339	24	<b>1512</b>	<b>309</b>	1522	307	<b>1513</b>	<b>309</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date::: 2011-10-11 #\\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on linux-j9so Sat Apr 21 13:46:10 2012

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630L 0 @ 2.00GHz
        2 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      132117016 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 11 (x86_64)
    VERSION = 11
    PATCHLEVEL = 2
```

```
uname -a:
Linux linux-j9so 3.0.13-0.9-default #1 SMP Mon Jan 16 17:33:03 UTC 2012
(54ddfaf) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 20 23:08 last=S
```

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext3  1.8T   30G  1.8T   2%  /
```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/lib32:/cpu2006.1.2/lib64"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: `icpc -m32`

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
```

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## Peak Portability Flags (Continued)

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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