



SPEC® CFP2006 Result

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IBM Corporation

IBM System x iDataPlex dx360 M4 (Intel Xeon E5-2620)

SPECfp®2006 = 66.1

SPECfp_base2006 = 62.8

CPU2006 license: 11

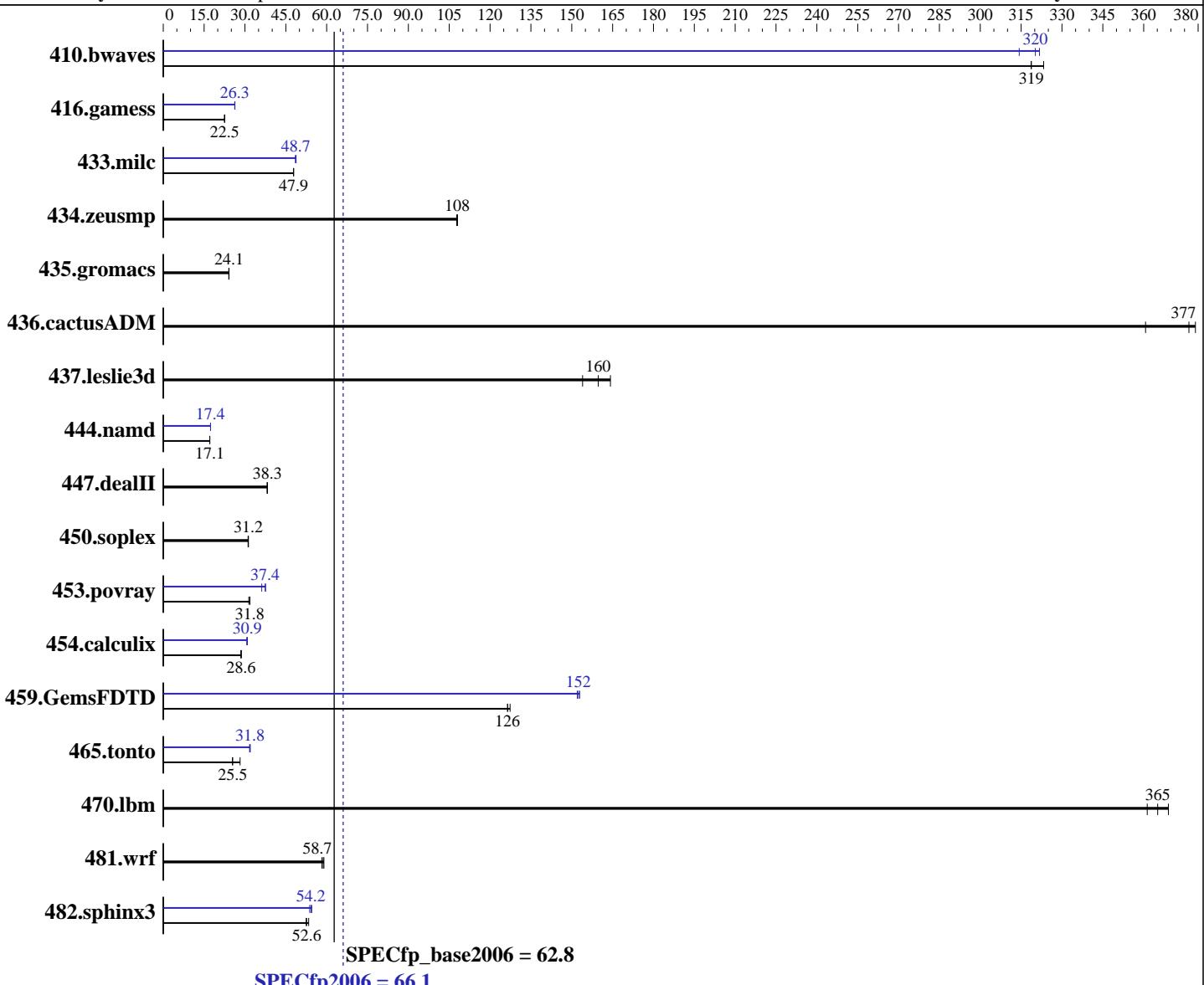
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jun-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 Compiler: 2.6.32-220.el6.x86_64
 Auto Parallel: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 File System: Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Software Availability: ext4

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| | |
|-----------------|---|
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz) |
| Disk Subsystem: | 1 x 250 GB SATA, 7200 RPM |
| Other Hardware: | None |

| | |
|-----------------|--------------------------|
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | None |

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio |
| 410.bwaves | 42.6 | 319 | 42.0 | 323 | 42.6 | 319 | 42.4 | 320 | 43.2 | 314 | 42.2 | 322 |
| 416.gamess | 870 | 22.5 | 868 | 22.6 | 870 | 22.5 | 745 | 26.3 | 744 | 26.3 | 746 | 26.3 |
| 433.milc | 192 | 47.9 | 192 | 47.8 | 192 | 47.9 | 188 | 48.8 | 188 | 48.7 | 189 | 48.5 |
| 434.zeusmp | 84.3 | 108 | 84.5 | 108 | 84.3 | 108 | 84.3 | 108 | 84.5 | 108 | 84.3 | 108 |
| 435.gromacs | 296 | 24.1 | 297 | 24.1 | 296 | 24.2 | 296 | 24.1 | 297 | 24.1 | 296 | 24.2 |
| 436.cactusADM | 31.5 | 379 | 31.7 | 377 | 33.1 | 361 | 31.5 | 379 | 31.7 | 377 | 33.1 | 361 |
| 437.leslie3d | 61.0 | 154 | 57.2 | 164 | 58.8 | 160 | 61.0 | 154 | 57.2 | 164 | 58.8 | 160 |
| 444.namd | 469 | 17.1 | 469 | 17.1 | 470 | 17.1 | 462 | 17.4 | 462 | 17.4 | 462 | 17.4 |
| 447.dealII | 299 | 38.3 | 299 | 38.3 | 300 | 38.1 | 299 | 38.3 | 299 | 38.3 | 300 | 38.1 |
| 450.soplex | 267 | 31.2 | 267 | 31.3 | 268 | 31.1 | 267 | 31.2 | 267 | 31.3 | 268 | 31.1 |
| 453.povray | 169 | 31.4 | 167 | 31.8 | 167 | 31.8 | 147 | 36.2 | 142 | 37.4 | 142 | 37.6 |
| 454.calculix | 289 | 28.5 | 288 | 28.6 | 288 | 28.7 | 269 | 30.6 | 267 | 30.9 | 267 | 30.9 |
| 459.GemsFDTD | 83.3 | 127 | 83.9 | 126 | 83.9 | 126 | 69.6 | 152 | 69.4 | 153 | 69.8 | 152 |
| 465.tonto | 349 | 28.2 | 387 | 25.4 | 386 | 25.5 | 308 | 31.9 | 310 | 31.7 | 309 | 31.8 |
| 470.lbm | 37.2 | 369 | 37.6 | 365 | 38.0 | 361 | 37.2 | 369 | 37.6 | 365 | 38.0 | 361 |
| 481.wrf | 192 | 58.2 | 190 | 58.7 | 189 | 59.0 | 192 | 58.2 | 190 | 58.7 | 189 | 59.0 |
| 482.sphinx3 | 365 | 53.4 | 370 | 52.6 | 371 | 52.5 | 357 | 54.5 | 362 | 53.8 | 359 | 54.2 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Zone reclaim mode enabled with:

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Platform Notes

BIOS Settings:

Operating Mode set to Maximum Performance

Sysinfo program /root/SPECcpu-v1.2/Docs/sysinfo

```
$Rev: 6775 $ $Date::: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c
running on tianden Wed Jun 6 04:44:56 2012
```

This section contains SUT (System Under Test) info as seen by

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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2620 0 @ 2.00GHz
        2 "physical id"s (chips)
        24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 6
    siblings   : 12
    physical 0: cores 0 1 2 3 4 5
    physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      132094664 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux tianden 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 4 15:48
```

```
SPEC is set to: /root/SPECcpu-v1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/vg_tianden-lv_root
                ext4   145G   61G   78G  44%  /
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5

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General Notes (Continued)

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias
```

```
470.lbm: basepeak = yes
```

```
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias  
-parallel
```

C++ benchmarks:

```
444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32
```

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>



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