



SPEC® CINT2006 Result

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IBM Corporation

IBM System x3650 M4
(Intel Xeon E5-2667 v2, 3.30 GHz)

SPECint®2006 = 67.6

SPECint_base2006 = 63.0

CPU2006 license: 11

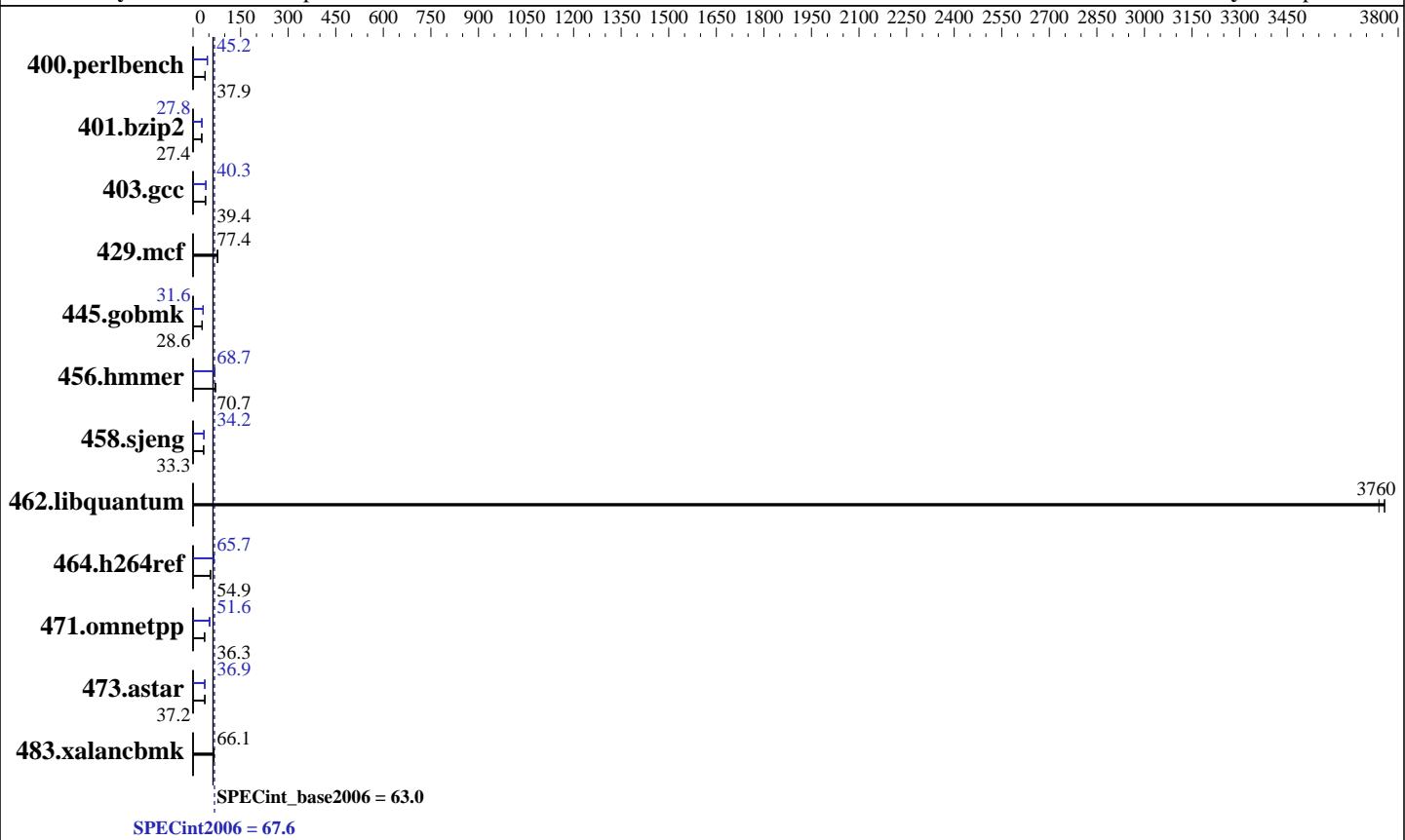
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Sep-2013

Hardware Availability: Dec-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2667 v2
CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
CPU MHz: 3300
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
Compiler: 2.6.32-358.el6.x86_64
Auto Parallel: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	258	37.9	257	38.0	258	37.9	216	45.2	216	45.2	216	45.1
401.bzip2	352	27.4	352	27.4	352	27.4	347	27.8	347	27.8	347	27.8
403.gcc	205	39.3	204	39.4	204	39.4	200	40.3	200	40.3	200	40.3
429.mcf	118	77.4	119	76.8	117	77.6	118	77.4	119	76.8	117	77.6
445.gobmk	367	28.6	368	28.5	367	28.6	332	31.6	332	31.6	332	31.6
456.hmmer	132	70.8	133	70.1	132	70.7	136	68.7	135	68.9	138	67.4
458.sjeng	363	33.3	364	33.3	363	33.3	353	34.2	354	34.2	354	34.2
462.libquantum	5.54	3740	5.51	3760	5.52	3760	5.54	3740	5.51	3760	5.52	3760
464.h264ref	403	54.9	403	54.9	404	54.8	337	65.7	337	65.7	337	65.7
471.omnetpp	172	36.4	173	36.2	172	36.3	121	51.6	122	51.3	118	53.0
473.astar	190	37.0	189	37.2	189	37.2	189	37.1	190	36.9	191	36.8
483.xalancbmk	104	66.1	104	66.1	104	66.3	104	66.1	104	66.1	104	66.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode

Platform Notes

BIOS setting:

Operating Mode set to Maximum Performance

Hyper-Threading set to Disabled

Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\\$ e86d102572650a6e4d596a3cee98f191
running on x3650M4 Sat Sep 14 16:37:49 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2667 v2 @ 3.30GHz
  2 "physical id"s (chips)
    16 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

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Platform Notes (Continued)

```
caution.)  
    cpu cores : 8  
    siblings   : 8  
    physical 0: cores 1 2 3 4 8 9 10 11  
    physical 1: cores 1 2 3 4 8 9 10 11  
    cache size : 25600 KB  
  
From /proc/meminfo  
MemTotal:      264491132 kB  
HugePages_Total:        0  
Hugepagesize:     2048 kB  
  
/usr/bin/lsb_release -d  
Red Hat Enterprise Linux Server release 6.4 (Santiago)  
  
From /etc/*release* /etc/*version*  
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server  
  
uname -a:  
Linux x3650M4 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013  
x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 Sep 14 15:36  
  
SPEC is set to: /home/SPECCpu-new  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/mapper/vg_x3650m4-lv_home  
          ext4   313G  200G   98G  68%  /home  
  
Additional information from dmidecode:  
BIOS IBM -[TESTBUILD-1.50]- 08/09/2013  
Memory:  
 8x Not Specified Not Specified  
 16x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank  
  
(End of data from sysinfo program)  
"Not Specified" memory information from dmidecode indicates unused DIMM slots.
```

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPECCpu-new/libs/32:/home/SPECCpu-new/libs/64:/home/SPECCpu-new/sh"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:

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General Notes (Continued)

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

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Peak Optimization Flags (Continued)

456.hmmr: -xAVX -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
-ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-IVB-A.xml>



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For other inquiries, please contact webmaster@spec.org.

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