



SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 62.1

PowerEdge T620 (Intel Xeon E5-2690 v2, 3.00 GHz)

SPECint_base2006 = 57.9

CPU2006 license: 55

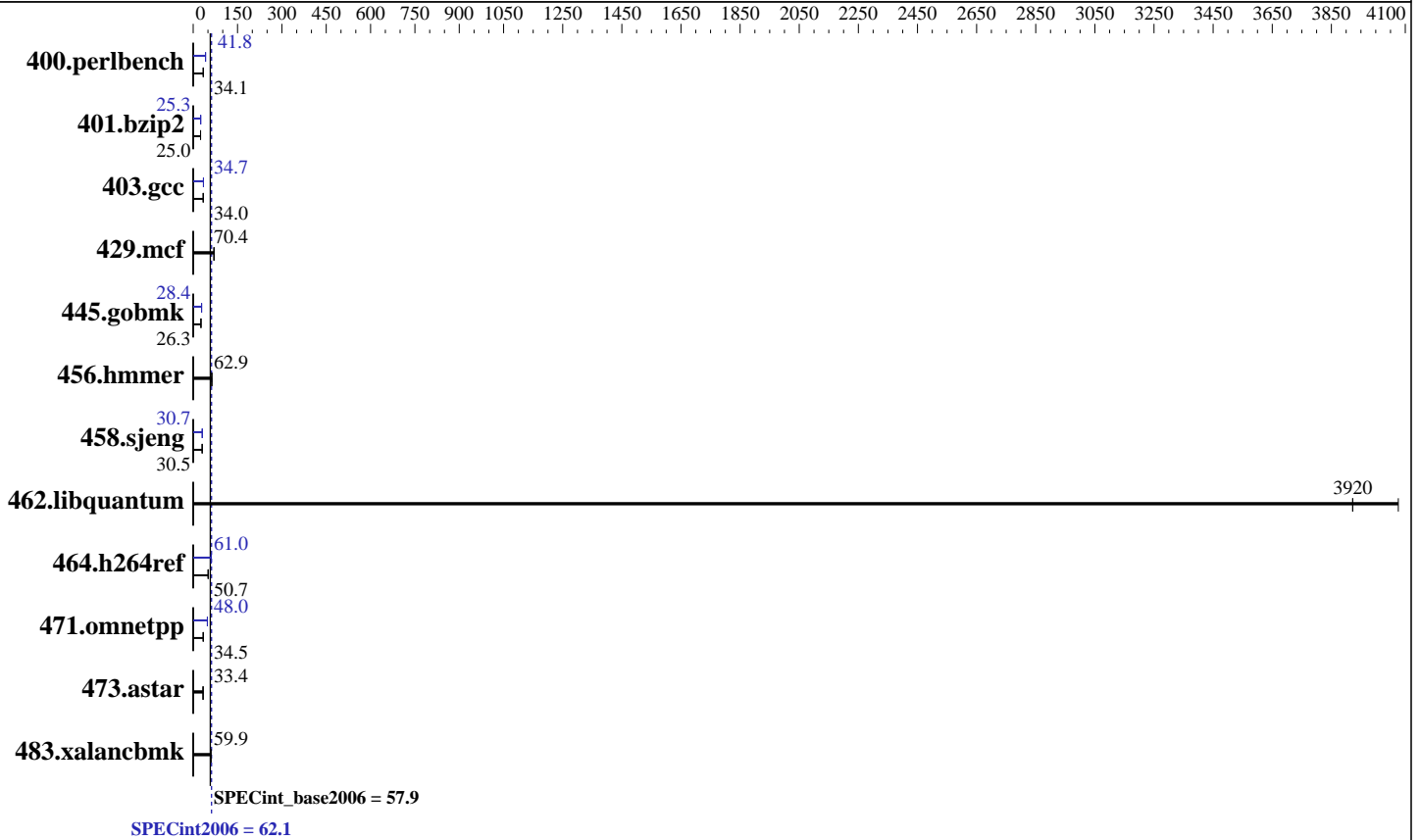
Test date: Aug-2013

Test sponsor: Dell Inc.

Hardware Availability: Sep-2013

Tested by: Dell Inc.

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2690 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP3 (x86_64) 3.0.76-0.11-default
 Compiler: C/C++; Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	287	34.1	286	34.1	287	34.1	234	41.8	234	41.8	234	41.7
401.bzip2	386	25.0	387	25.0	387	25.0	381	25.3	381	25.3	381	25.3
403.gcc	237	33.9	236	34.1	237	34.0	232	34.7	232	34.7	232	34.7
429.mcf	130	70.4	130	70.3	129	70.4	130	70.4	130	70.3	129	70.4
445.gobmk	399	26.3	401	26.2	399	26.3	369	28.4	369	28.4	369	28.4
456.hammer	148	63.0	149	62.8	148	62.9	148	63.0	149	62.8	148	62.9
458.sjeng	397	30.5	397	30.5	397	30.5	394	30.7	394	30.7	394	30.7
462.libquantum	5.28	3920	5.08	4080	5.28	3920	5.28	3920	5.08	4080	5.28	3920
464.h264ref	436	50.7	436	50.7	437	50.6	363	60.9	363	61.0	363	61.0
471.omnetpp	181	34.5	181	34.5	188	33.2	130	48.1	130	48.0	131	47.9
473.astar	210	33.4	209	33.6	210	33.4	210	33.4	209	33.6	210	33.4
483.xalancbmk	115	59.9	115	59.9	115	59.9	115	59.9	115	59.9	115	59.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Virtualization Technology disabled
 Execute Disable disabled
 Logical Processor disabled
 System Profile set to Performance
 Sysinfo program /root/cpu2006.1.2.ic13/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on linux Thu Aug 22 08:47:57 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v2 @ 3.00GHz
 2 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
```

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Platform Notes (Continued)

physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

```
From /proc/meminfo
MemTotal:      264601764 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3
```

```
uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 22 08:41 last=S
```

```
SPEC is set to: /root/cpu2006.1.2.ic13
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext2  267G  199G   67G  75% /
```

Additional information from dmidecode:

```
BIOS Dell Inc. 2.0.18 08/10/2013
Memory:
2x 00AD00B300AD HMT42GR7MFR4C-RD 16 GB 1866 MHz
8x 00AD04B300AD HMT42GR7AFR4C-RD 16 GB 1866 MHz
6x 00CE00B300CE M393B2G70CB0-CMA 16 GB 1866 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006.1.2.ic13/libs/32:/root/cpu2006.1.2.ic13/libs/64:/root/cpu2006.1.2.ic13/sh"
OMP_NUM_THREADS = "20"
```

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
```



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: basepeak = yes

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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