



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 149

PRIMERGY RX100 S8, Intel Xeon E3-1230 v3, 3.30 GHz

SPECfp_rate_base2006 = 144

CPU2006 license: 19

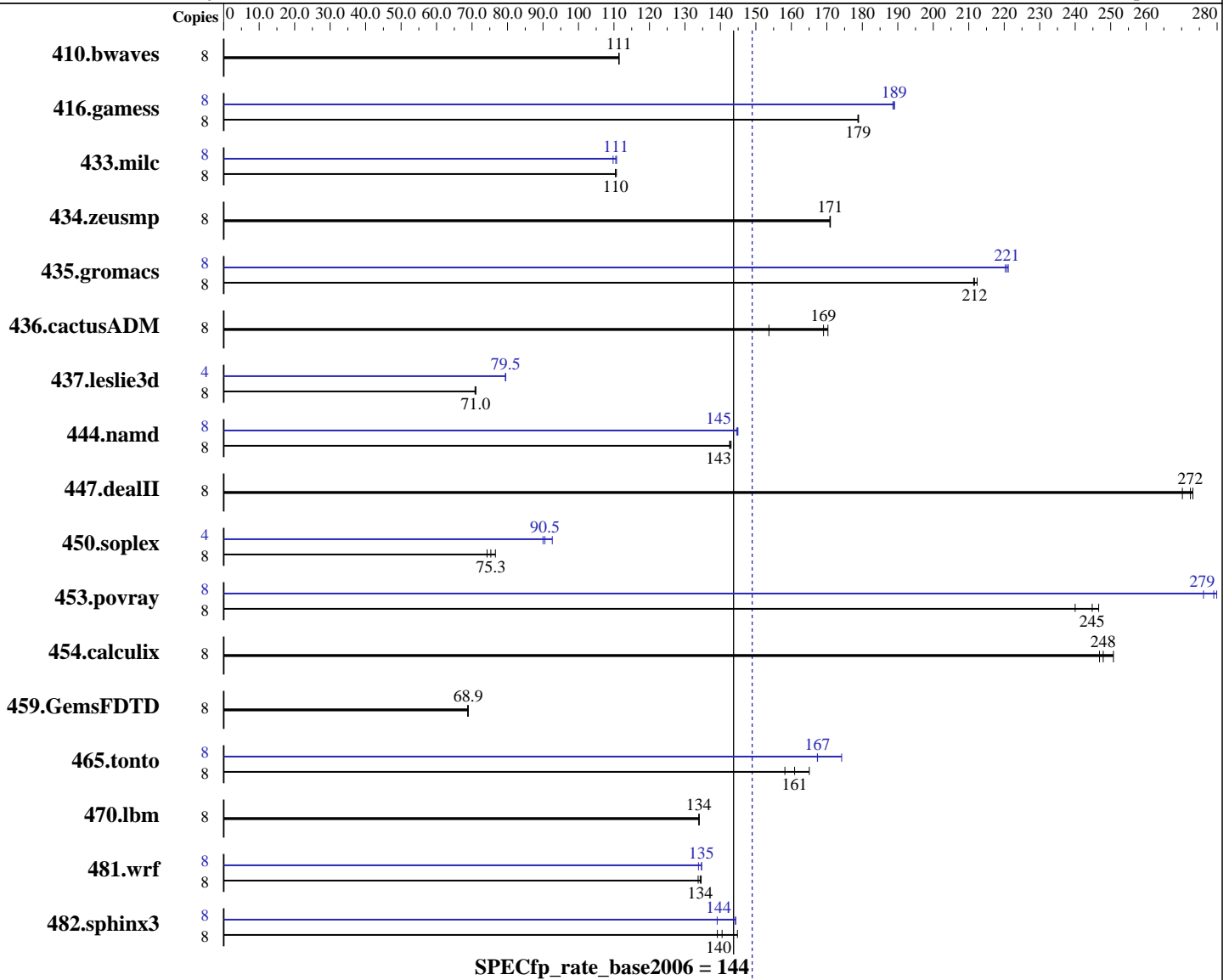
Test date: Sep-2013

Test sponsor: Fujitsu

Hardware Availability: Sep-2013

Tested by: Fujitsu

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E3-1230 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.11.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	975	111	<u>976</u>	<u>111</u>	976	111	8	975	111	<u>976</u>	<u>111</u>	976	111
416.gamess	8	876	179	875	179	<u>876</u>	<u>179</u>	8	<u>829</u>	<u>189</u>	828	189	830	189
433.milc	8	665	110	663	111	<u>665</u>	<u>110</u>	8	663	111	669	110	<u>665</u>	<u>111</u>
434.zeusmp	8	426	171	<u>426</u>	<u>171</u>	426	171	8	426	171	<u>426</u>	<u>171</u>	426	171
435.gromacs	8	270	211	<u>270</u>	<u>212</u>	269	212	8	<u>259</u>	<u>221</u>	258	221	259	220
436.cactusADM	8	561	170	<u>565</u>	<u>169</u>	622	154	8	561	170	<u>565</u>	<u>169</u>	622	154
437.leslie3d	8	1058	71.1	1062	70.8	<u>1059</u>	<u>71.0</u>	4	473	79.5	474	79.4	<u>473</u>	<u>79.5</u>
444.namd	8	450	143	<u>450</u>	<u>143</u>	449	143	8	<u>443</u>	<u>145</u>	444	145	443	145
447.dealII	8	335	273	<u>336</u>	<u>272</u>	339	270	8	335	273	<u>336</u>	<u>272</u>	339	270
450.soplex	8	<u>886</u>	<u>75.3</u>	871	76.6	899	74.2	4	<u>368</u>	<u>90.5</u>	371	90.0	360	92.6
453.povray	8	<u>174</u>	<u>245</u>	177	240	173	247	8	154	276	152	280	<u>153</u>	<u>279</u>
454.calculix	8	263	251	267	247	<u>266</u>	<u>248</u>	8	263	251	267	247	<u>266</u>	<u>248</u>
459.GemsFDTD	8	1232	68.9	<u>1232</u>	<u>68.9</u>	1235	68.8	8	1232	68.9	<u>1232</u>	<u>68.9</u>	1235	68.8
465.tonto	8	498	158	477	165	<u>489</u>	<u>161</u>	8	452	174	470	167	<u>470</u>	<u>167</u>
470.lbm	8	820	134	821	134	<u>821</u>	<u>134</u>	8	820	134	821	134	<u>821</u>	<u>134</u>
481.wrf	8	664	135	668	134	<u>665</u>	<u>134</u>	8	663	135	<u>664</u>	<u>135</u>	668	134
482.sphinx3	8	1121	139	1077	145	<u>1110</u>	<u>140</u>	8	<u>1081</u>	<u>144</u>	1080	144	1121	139

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
 -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
 -unroll2

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
 -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2)
 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -inline-level=0 -scalar-rep-

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml>

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Tested with SPEC CPU2006 v1.2.

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