



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp®_rate2006 = 252

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECfp_rate_base2006 = 245

CPU2006 license: 3

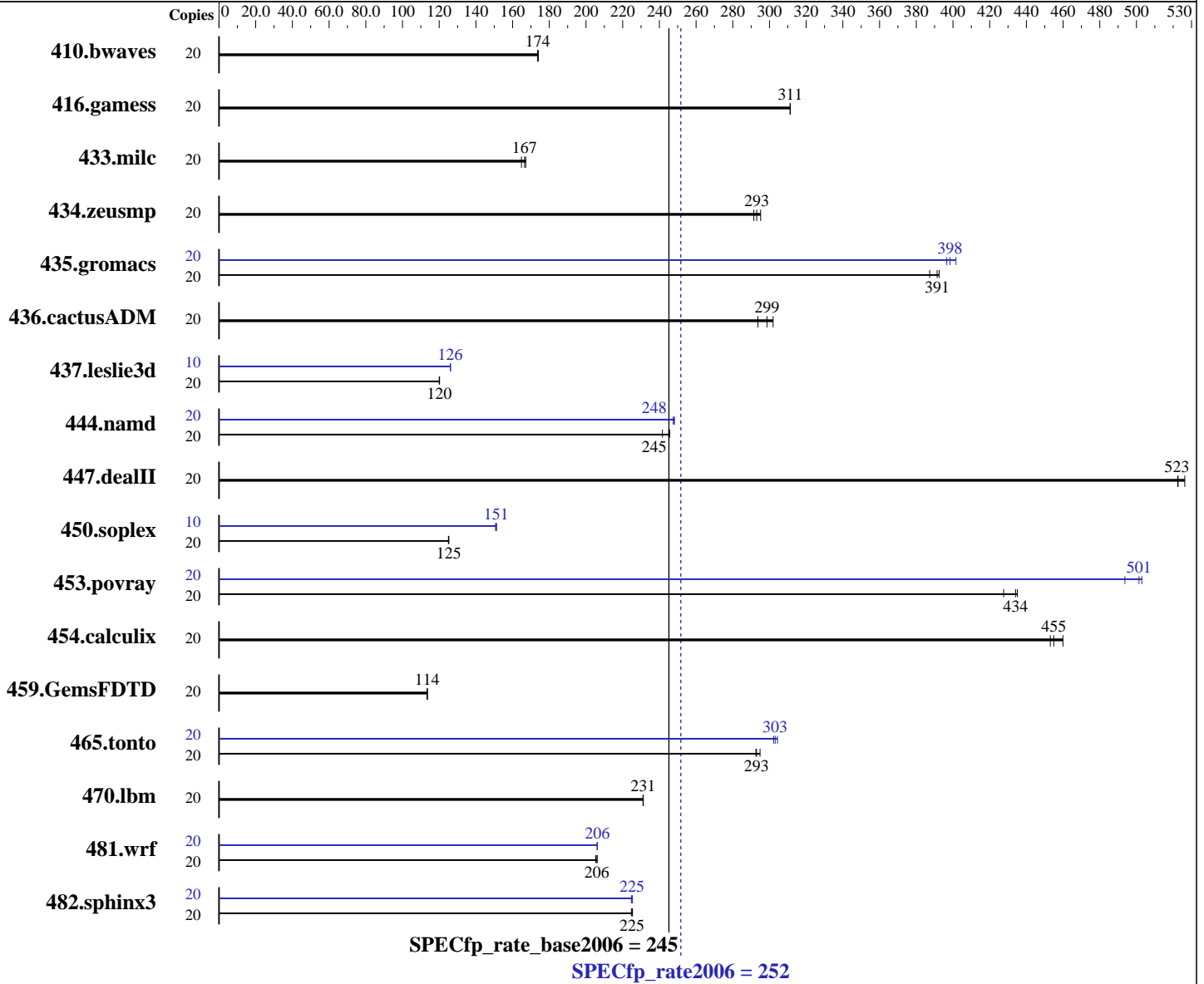
Test date: Dec-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2470 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 10 cores, 1 chip, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 252

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECfp_rate_base2006 = 245

CPU2006 license: 3

Test date: Dec-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 300 GB 15 K SAS, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	20	1562	174	1564	174	1565	174	20	1562	174	1564	174	1565	174
416.gamess	20	1258	311	1258	311	1258	311	20	1258	311	1258	311	1258	311
433.milc	20	1114	165	1102	167	1098	167	20	1114	165	1102	167	1098	167
434.zeusmp	20	624	291	617	295	621	293	20	624	291	617	295	621	293
435.gromacs	20	365	391	369	387	364	392	20	358	398	360	397	356	402
436.cactusADM	20	791	302	800	299	814	294	20	791	302	800	299	814	294
437.leslie3d	20	1566	120	1565	120	1564	120	10	746	126	745	126	746	126
444.namd	20	664	242	654	245	653	246	20	646	248	648	248	648	248
447.dealII	20	435	526	438	522	438	523	20	435	526	438	522	438	523
450.soplex	20	1332	125	1332	125	1334	125	10	551	151	552	151	553	151
453.povray	20	245	435	249	428	245	434	20	212	503	216	494	212	501
454.calculix	20	364	453	359	460	363	455	20	364	453	359	460	363	455
459.GemsFDTD	20	1869	114	1869	114	1871	113	20	1869	114	1869	114	1871	113
465.tonto	20	667	295	673	293	672	293	20	649	303	647	304	651	302
470.lbm	20	1188	231	1189	231	1189	231	20	1188	231	1189	231	1189	231
481.wrf	20	1084	206	1084	206	1088	205	20	1083	206	1083	206	1084	206
482.sphinx3	20	1735	225	1730	225	1731	225	20	1730	225	1732	225	1734	225

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
Disabled unused Linux services through "stop_services.sh" before running.



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 252

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECfp_rate_base2006 = 245

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2013
Hardware Availability: Jan-2014
Software Availability: Sep-2013

Platform Notes

BIOS Configuration:

HP Power Profile set to Maximum Performance
Memory Power Savings Mode set to Maximum Performance
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Dynamic Power Capping Functionality set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x
Sysinfo program /cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on dl360e-gen8-rf0 Thu Dec 12 09:53:54 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-2470 v2 @ 2.40GHz
 1 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 10
  siblings    : 20
  physical 0 : cores 0 1 2 3 4 8 9 10 11 12
cache size    : 25600 KB
```

From /proc/meminfo

```
MemTotal:      49254508 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux dl360e-gen8-rf0 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Dec 11 19:42

SPEC is set to: /cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3        ext4      273G  44G  216G  17% /
```

Additional information from dmidecode:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 252

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECfp_rate_base2006 = 245

CPU2006 license: 3

Test date: Dec-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

Platform Notes (Continued)

BIOS HP P73 11/12/2013

Memory:

6x HP 689911-071 8 GB 1600 MHz 2 rank

6x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 48 GB and the dmidecode description should have one line reading as:

6x HP 689911-071 8 GB 1600 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

453.povray: -DSPEC_CPU_LP64

454.calculix: -DSPEC_CPU_LP64 -nofor_main

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 252

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECfp_rate_base2006 = 245

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2013
Hardware Availability: Jan-2014
Software Availability: Sep-2013

Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 252

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECfp_rate_base2006 = 245

CPU2006 license: 3

Test date: Dec-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: -xAVX -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

```

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32
447.dealII: basepeak = yes
450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3
453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

```

410.bwaves: basepeak = yes

```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 252

ProLiant DL380e Gen8
(2.40 GHz, Intel Xeon E5-2470 v2)

SPECfp_rate_base2006 = 245

CPU2006 license: 3

Test date: Dec-2013

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 20:18:19 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 January 2014.