



# SPEC® CINT2006 Result

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## ACTION S.A.

ACTINA SOLAR 222 S5 (Intel Xeon E5-2640 v2, 2.00 GHz)

**SPECint®\_rate2006 = 544**

**SPECint\_rate\_base2006 = 524**

CPU2006 license: 9008

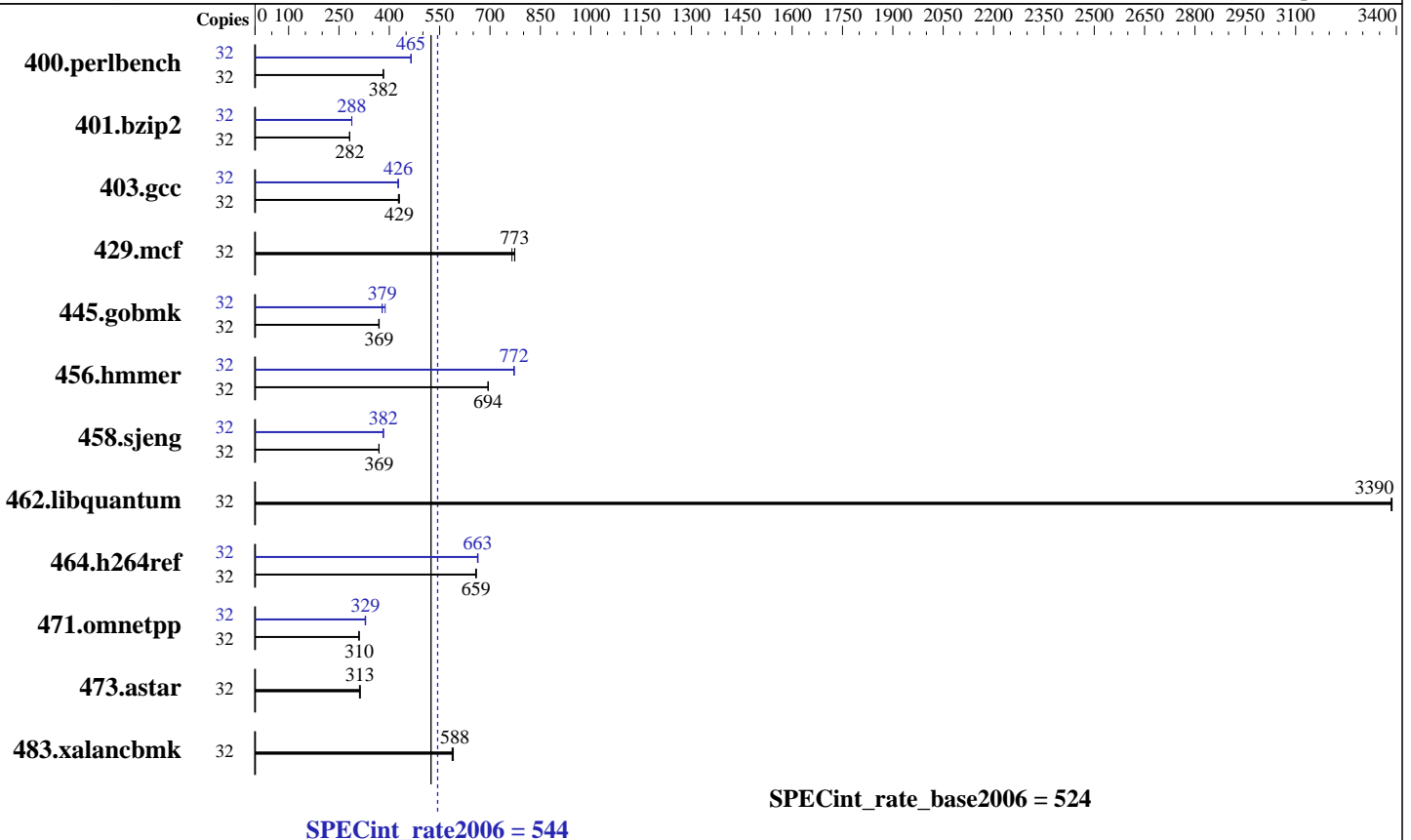
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Mar-2014

Hardware Availability: Oct-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2640 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz and CL11)  
 Disk Subsystem: 1 x 240 GB SATA II SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.11.1.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	817	383	819	382	<u>818</u>	<u>382</u>	32	673	464	672	465	<u>673</u>	<u>465</u>
401.bzip2	32	1094	282	1097	281	<u>1095</u>	<u>282</u>	32	1072	288	1071	288	<u>1071</u>	<u>288</u>
403.gcc	32	<u>600</u>	<u>429</u>	603	427	599	430	32	604	426	603	427	<u>604</u>	<u>426</u>
429.mcf	32	<u>377</u>	<u>773</u>	377	774	381	765	32	<u>377</u>	<u>773</u>	377	774	381	765
445.gobmk	32	911	369	908	370	<u>909</u>	<u>369</u>	32	866	388	<u>886</u>	<u>379</u>	887	379
456.hammer	32	<u>430</u>	<u>694</u>	430	694	430	695	32	<u>387</u>	<u>772</u>	387	771	387	772
458.sjeng	32	1048	370	1049	369	<u>1049</u>	<u>369</u>	32	1013	382	1014	382	<u>1013</u>	<u>382</u>
462.libquantum	32	196	3380	<u>196</u>	<u>3390</u>	196	3390	32	196	3380	<u>196</u>	<u>3390</u>	196	3390
464.h264ref	32	1078	657	<u>1075</u>	<u>659</u>	1074	659	32	1066	664	<u>1068</u>	<u>663</u>	1069	663
471.omnetpp	32	<u>645</u>	<u>310</u>	646	309	645	310	32	610	328	607	329	<u>608</u>	<u>329</u>
473.astar	32	717	313	722	311	<u>718</u>	<u>313</u>	32	717	313	722	311	<u>718</u>	<u>313</u>
483.xalancbmk	32	376	587	374	591	<u>375</u>	<u>588</u>	32	376	587	374	591	<u>375</u>	<u>588</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Bios Settings  
Power Technology = Custom  
Energy Performance = Performance  
Turbo Mode = Enabled  
C1E Support = Disabled  
CPU C3 Report = Disabled  
CPU C6 Report = Disabled  
Package C State Limit = No Limit

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on localhost.localdomain Wed Mar 5 14:47:26 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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### Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2640 v2 @ 2.00GHz
 2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      132125488 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-358.11.1.el6.x86_64 #1 SMP Tue Nov 19
17:43:04 CET 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 5 14:46

```

SPEC is set to: /cpu2006.1.2
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sdal        ext4      193G   46G  137G  26% /

```

```

Additional information from dmidecode:
BIOS American Megatrends Inc. 3.0a 02/13/2014
Memory:
16x      8 GB
14x Hynix Semiconducto HMT31GR7EFR4C 8 GB 1600 MHz 2 rank
2x Hynix Semiconductor HMT31GR7EFR4C- 8 GB 1600 MHz 2 rank

```

(End of data from sysinfo program)  
dmidecode does not properly detect memory modules  
16 modules of 8 GB were used to run the test (128 GB total)



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Binaries compiled on a system with 2x Xeon E5-2650 v2 chips

+ 256 GB memory using RedHat EL 6.4

## Base Compiler Invocation

C benchmarks:

```
icc -m32
```

C++ benchmarks:

```
icpc -m32
```

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
```

```
-Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/cpu2006.1.2/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-apr-2014-For-Supermicro-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-apr-2014-For-Supermicro-Platform.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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