



# SPEC® CINT2006 Result

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## Huawei

SPECint®2006 = 44.4

## Huawei CH240 (Intel Xeon E5-4610 v2)

SPECint\_base2006 = 41.3

CPU2006 license: 3175

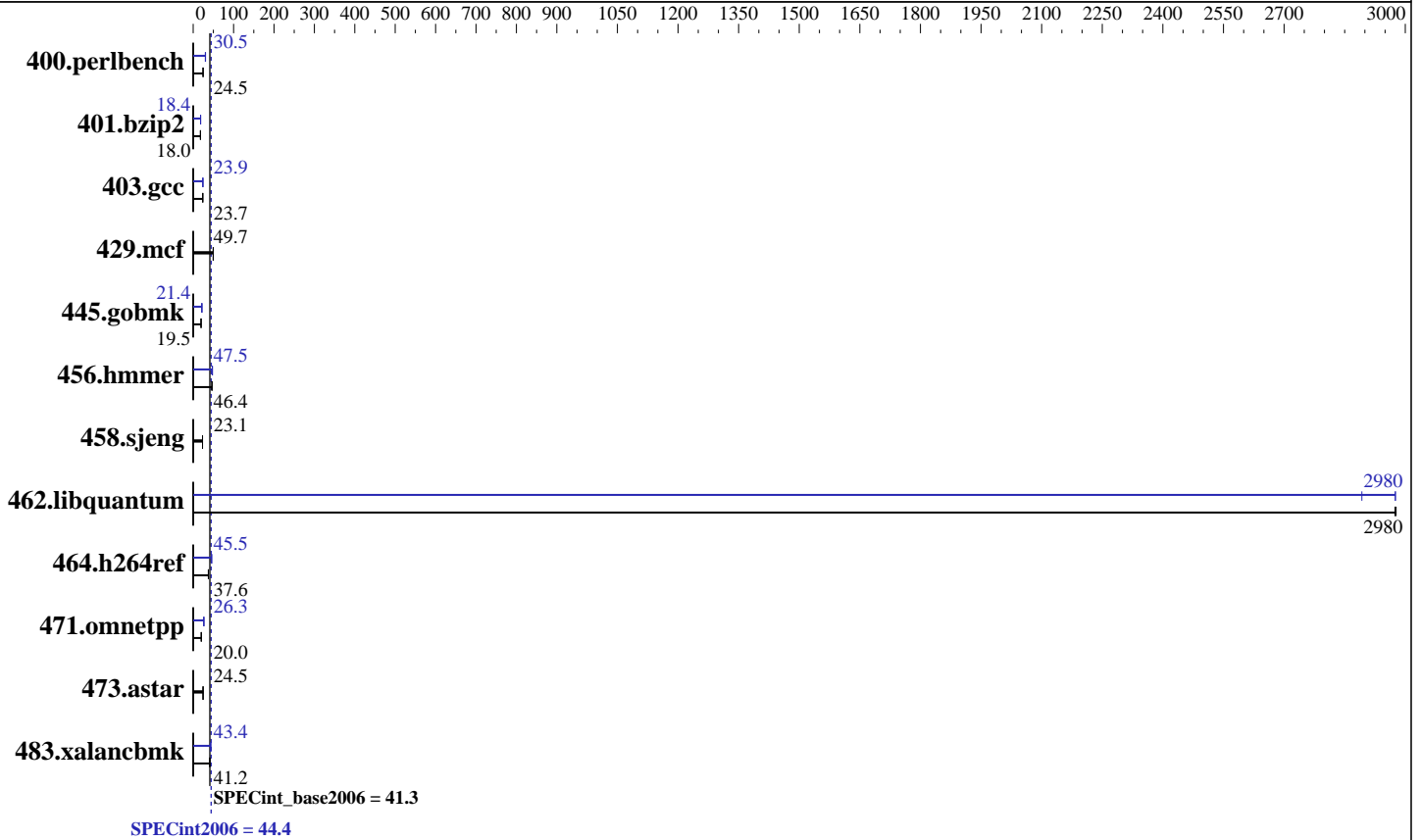
Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2014

Hardware Availability: Mar-2014

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-4610 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 16 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 2Rx8 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>399</u>	<u>24.5</u>	398	24.5	400	24.5	<u>321</u>	<u>30.5</u>	325	30.0	321	30.5
401.bzip2	<u>537</u>	<u>18.0</u>	537	18.0	537	18.0	<u>526</u>	<u>18.4</u>	526	18.4	526	18.3
403.gcc	340	23.7	340	23.7	<u>340</u>	<u>23.7</u>	<u>336</u>	<u>23.9</u>	336	24.0	336	23.9
429.mcf	185	49.4	183	49.8	<u>184</u>	<u>49.7</u>	185	49.4	183	49.8	<u>184</u>	<u>49.7</u>
445.gobmk	538	19.5	<u>538</u>	<u>19.5</u>	538	19.5	489	21.4	489	21.4	<u>489</u>	<u>21.4</u>
456.hammer	201	46.4	<u>201</u>	<u>46.4</u>	201	46.5	196	47.6	197	47.4	<u>197</u>	<u>47.5</u>
458.sjeng	524	23.1	524	23.1	<u>524</u>	<u>23.1</u>	524	23.1	524	23.1	<u>524</u>	<u>23.1</u>
462.libquantum	<u>6.96</u>	<u>2980</u>	6.96	2980	6.96	2970	<u>6.96</u>	<u>2980</u>	6.96	2980	7.16	2890
464.h264ref	<u>589</u>	<u>37.6</u>	587	37.7	591	37.5	<u>486</u>	<u>45.5</u>	486	45.5	487	45.4
471.omnetpp	311	20.1	312	20.0	<u>312</u>	<u>20.0</u>	237	26.3	<u>237</u>	<u>26.3</u>	237	26.4
473.astar	286	24.5	<u>287</u>	<u>24.5</u>	288	24.4	286	24.5	<u>287</u>	<u>24.5</u>	288	24.4
483.xalancbmk	167	41.2	<u>168</u>	<u>41.2</u>	168	41.1	159	43.5	<u>159</u>	<u>43.4</u>	160	43.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /spec/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on localhost.localdomain Sat Jun 28 16:29:28 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4610 v2 @ 2.30GHz
 4 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
physical 2: cores 0 1 2 3 4 5 6 7
physical 3: cores 0 1 2 3 4 5 6 7
cache size : 16384 KB
```

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## Platform Notes (Continued)

From /proc/meminfo

MemTotal: 264479480 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/\*release\* /etc/\*version\*

redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:

Linux localhost.localdomain 2.6.32-431.el6.x86\_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jun 28 16:13

SPEC is set to: /spec

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	ext4	268G	13G	242G	5%	/

Additional information from dmidecode:

Memory:

32x Samsung M393B1G73BH0-CK0 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,0,1"

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64"

OMP\_NUM\_THREADS = "32"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: basepeak = yes

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch
                -auto-p32

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias
              -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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<b>Test sponsor:</b> Huawei	<b>Hardware Availability:</b> Mar-2014
<b>Tested by:</b> Huawei	<b>Software Availability:</b> Nov-2013

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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