



SPEC[®] CFP2006 Result

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NEC Corporation

SPECfp[®]2006 = 112

Express5800/R120f-1M (Intel Xeon E5-2660 v3)

SPECfp_base2006 = 107

CPU2006 license: 9006

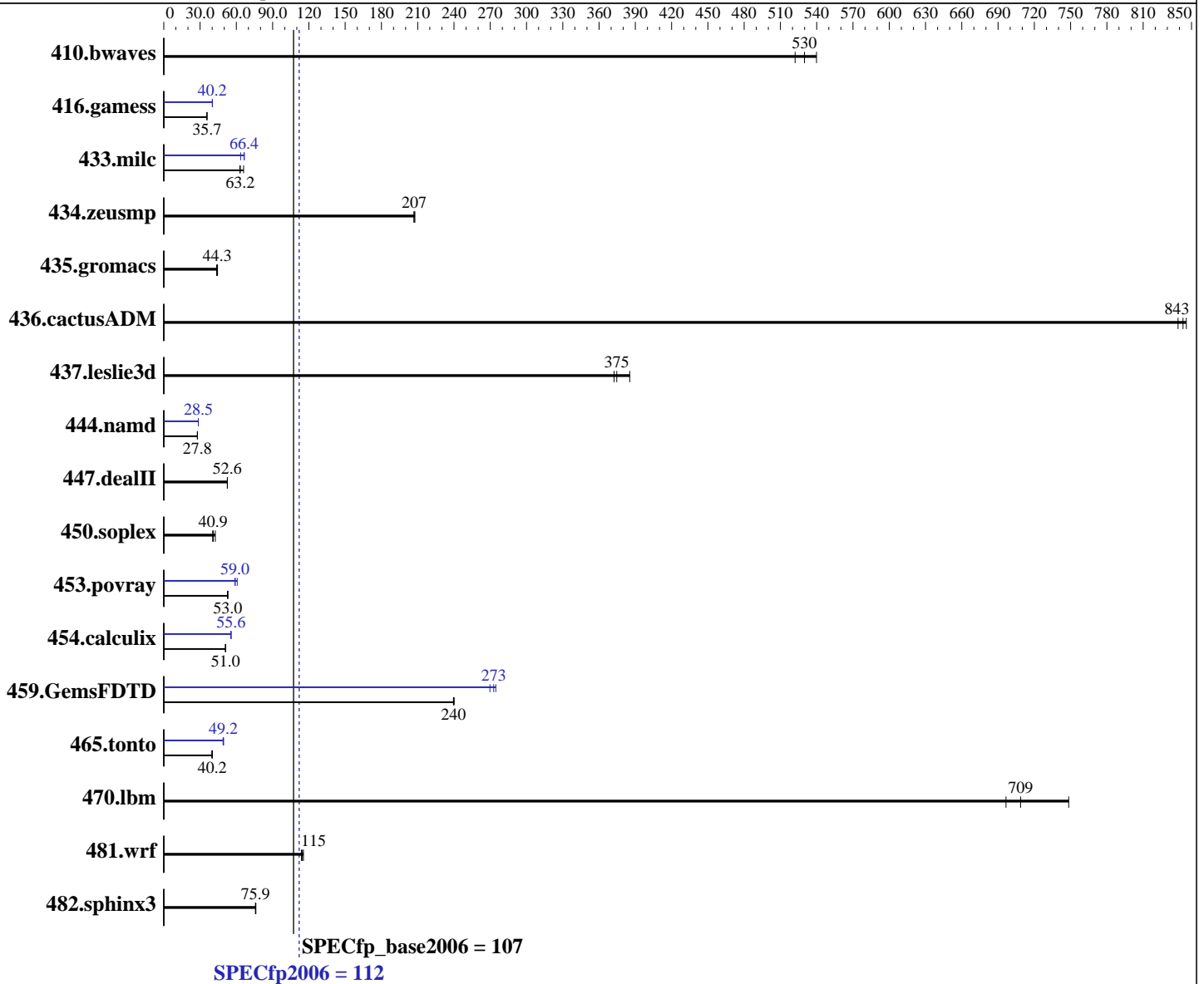
Test date: Oct-2014

Test sponsor: NEC Corporation

Hardware Availability: Feb-2015

Tested by: NEC Corporation

Software Availability: Jul-2014



Hardware

CPU Name: Intel Xeon E5-2660 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.17.1.el6.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.0	522	<u>25.6</u>	<u>530</u>	25.2	540	26.0	522	<u>25.6</u>	<u>530</u>	25.2	540
416.gamess	550	35.6	548	35.7	<u>549</u>	<u>35.7</u>	488	40.1	<u>488</u>	<u>40.2</u>	487	40.2
433.milc	146	62.9	<u>145</u>	<u>63.2</u>	139	66.0	144	63.5	<u>138</u>	<u>66.4</u>	138	66.6
434.zeusmp	44.0	207	<u>43.9</u>	<u>207</u>	43.8	208	44.0	207	<u>43.9</u>	<u>207</u>	43.8	208
435.gromacs	161	44.4	<u>161</u>	<u>44.3</u>	163	43.7	161	44.4	<u>161</u>	<u>44.3</u>	163	43.7
436.cactusADM	<u>14.2</u>	<u>843</u>	14.1	846	14.2	839	<u>14.2</u>	<u>843</u>	14.1	846	14.2	839
437.leslie3d	<u>25.1</u>	<u>375</u>	24.4	385	25.2	372	<u>25.1</u>	<u>375</u>	24.4	385	25.2	372
444.namd	<u>289</u>	<u>27.8</u>	289	27.8	288	27.8	281	28.5	<u>281</u>	<u>28.5</u>	281	28.6
447.dealII	218	52.6	<u>218</u>	<u>52.6</u>	218	52.6	218	52.6	<u>218</u>	<u>52.6</u>	218	52.6
450.soplex	<u>204</u>	<u>40.9</u>	196	42.6	206	40.4	<u>204</u>	<u>40.9</u>	196	42.6	206	40.4
453.povray	<u>100</u>	<u>53.0</u>	100	53.0	101	52.6	87.6	60.7	90.2	59.0	<u>90.2</u>	<u>59.0</u>
454.calculix	161	51.1	162	51.0	<u>162</u>	<u>51.0</u>	149	55.4	<u>148</u>	<u>55.6</u>	148	55.6
459.GemsFDTD	44.3	240	44.2	240	<u>44.3</u>	<u>240</u>	<u>38.9</u>	<u>273</u>	39.3	270	38.6	275
465.tonto	248	39.7	245	40.2	<u>245</u>	<u>40.2</u>	<u>200</u>	<u>49.2</u>	201	49.0	199	49.5
470.lbm	19.7	697	18.4	748	<u>19.4</u>	<u>709</u>	19.7	697	18.4	748	<u>19.4</u>	<u>709</u>
481.wrf	98.1	114	<u>97.5</u>	<u>115</u>	96.7	115	98.1	114	<u>97.5</u>	<u>115</u>	96.7	115
482.sphinx3	257	75.8	256	76.2	<u>257</u>	<u>75.9</u>	257	75.8	256	76.2	<u>257</u>	<u>75.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
 Power Management Policy: Custom
 Energy Performance: Performance
 Patrol Scrub: Disabled
 Demand Scrub: Disabled
 Early Snoop: Disabled
 Hyper-Threading: Disabled



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP_NUM_THREADS = "20"

The Express5800/R120f-1M (Intel Xeon E5-2660 v3) and the Express5800/R120f-2M (Intel Xeon E5-2660 v3) models are electronically equivalent. The results have been measured on the Express5800/R120f-2M (Intel Xeon E5-2660 v3) model.

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml>

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For other inquiries, please contact webmaster@spec.org.

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