



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = 71.9

Huawei RH5885H V3 (Intel Xeon E7-4830 v2)

SPECfp_base2006 = 66.6

CPU2006 license: 3175

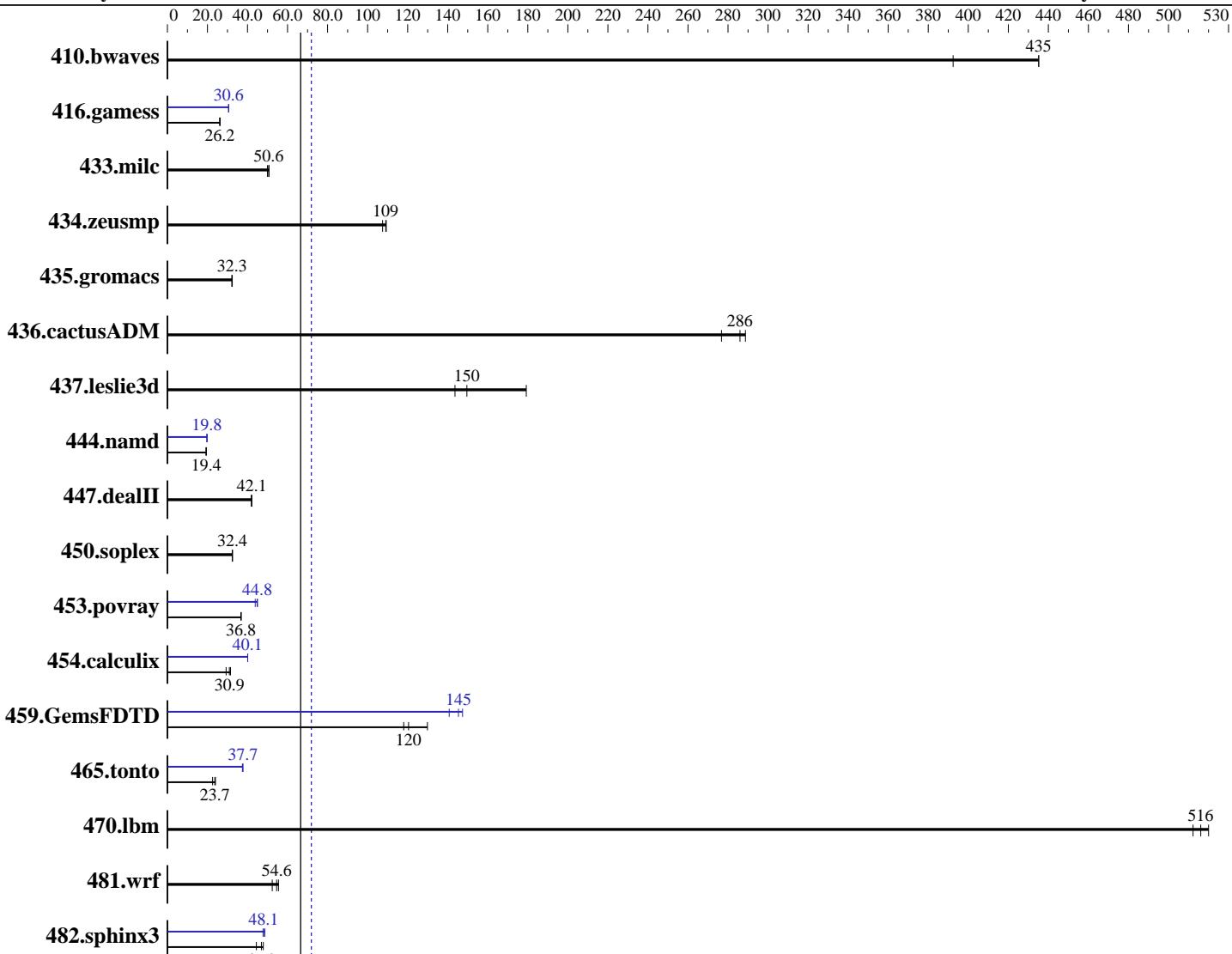
Test date: Oct-2014

Test sponsor: Huawei

Hardware Availability: Feb-2014

Tested by: Huawei

Software Availability: Nov-2013



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Hardware

CPU Name: Intel Xeon E7-4830 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.70 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Compiler: 2.6.32-431.el6.x86_64
 C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (64 x 8 GB 2Rx8 PC3-12800R-11, ECC, running at 1066 MHz and CL7)
 Disk Subsystem: 2 x 300 GB SAS, 10K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	34.6	392	31.2	435	<u>31.2</u>	<u>435</u>	34.6	392	31.2	435	<u>31.2</u>	<u>435</u>
416.gamess	<u>748</u>	<u>26.2</u>	744	26.3	749	26.1	<u>640</u>	<u>30.6</u>	<u>640</u>	<u>30.6</u>	640	30.6
433.milc	<u>181</u>	<u>50.6</u>	181	50.7	184	49.9	<u>181</u>	<u>50.6</u>	181	50.7	184	49.9
434.zeusmp	84.7	107	<u>83.5</u>	<u>109</u>	83.3	109	84.7	107	<u>83.5</u>	<u>109</u>	83.3	109
435.gromacs	222	32.2	220	32.4	<u>221</u>	<u>32.3</u>	222	32.2	220	32.4	<u>221</u>	<u>32.3</u>
436.cactusADM	43.2	277	41.4	289	<u>41.8</u>	<u>286</u>	43.2	277	41.4	289	<u>41.8</u>	<u>286</u>
437.leslie3d	52.4	179	65.4	144	<u>62.8</u>	<u>150</u>	52.4	179	65.4	144	<u>62.8</u>	<u>150</u>
444.namd	<u>414</u>	<u>19.4</u>	414	19.4	414	19.4	406	19.8	406	19.8	<u>406</u>	<u>19.8</u>
447.dealII	272	42.1	<u>272</u>	<u>42.1</u>	273	42.0	272	42.1	<u>272</u>	<u>42.1</u>	273	42.0
450.soplex	<u>257</u>	<u>32.4</u>	256	32.5	258	32.4	<u>257</u>	<u>32.4</u>	256	32.5	258	32.4
453.povray	144	36.8	<u>145</u>	<u>36.8</u>	145	36.7	<u>119</u>	<u>44.8</u>	121	43.9	118	45.0
454.calculix	281	29.3	<u>267</u>	<u>30.9</u>	262	31.5	<u>206</u>	<u>40.1</u>	206	40.1	<u>206</u>	<u>40.1</u>
459.GemsFDTD	81.7	130	89.9	118	<u>88.1</u>	<u>120</u>	75.4	141	72.0	147	<u>73.0</u>	<u>145</u>
465.tonto	410	24.0	436	22.6	<u>416</u>	<u>23.7</u>	<u>261</u>	<u>37.7</u>	263	37.4	261	37.7
470.lbm	<u>26.6</u>	<u>516</u>	26.8	512	26.4	520	<u>26.6</u>	<u>516</u>	26.8	512	26.4	520
481.wrf	<u>205</u>	<u>54.6</u>	213	52.4	201	55.5	<u>205</u>	<u>54.6</u>	213	52.4	201	55.5
482.sphinx3	<u>415</u>	<u>46.9</u>	407	47.9	439	44.4	<u>400</u>	<u>48.7</u>	<u>406</u>	<u>48.1</u>	408	47.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set Lock_step to disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Set Intel Hyper Threading to disabled

Sysinfo program /spec/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date::: 2012-07-17 #\\$ e86d102572650a6e4d596a3cee98f191

running on RH5885HV3 Sat Oct 18 13:39:52 2014

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E7-4830 v2 @ 2.20GHz
        4 "physical id"s (chips)
        40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 10
    siblings   : 10
    physical 0: cores 0 1 2 3 4 8 9 10 11 12
    physical 1: cores 0 1 2 3 4 8 9 10 11 12
    physical 2: cores 0 1 2 3 4 8 9 10 11 12
    physical 3: cores 0 1 2 3 4 8 9 10 11 12
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      529107008 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux RH5885HV3 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 18 07:34
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        ext4  259G  19G  227G  8%  /
```

```
Additional information from dmidecode:
BIOS American Megatrends Inc. BLISV395 07/25/2014
Memory:
 64x    8 GB
 64x Hynix HMT41GR7AFR8C-PB 8 GB 1066 MHz 2 rank
 32x NO DIMM NO DIMM
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of
memory is 512 GB and the dmidecode description should have one line reading as:

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Platform Notes (Continued)

64x Hynix HMT41GR7AFR8C-PB 8 GB 1066 MHz 2 rank

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"
OMP_NUM_THREADS = "40"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

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Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact webmaster@spec.org.

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