



# SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Huawei

SPECint®\_rate2006 = 859

Huawei XH628 V3 (Intel Xeon E5-2650 v3)

SPECint\_rate\_base2006 = 823

CPU2006 license: 3175

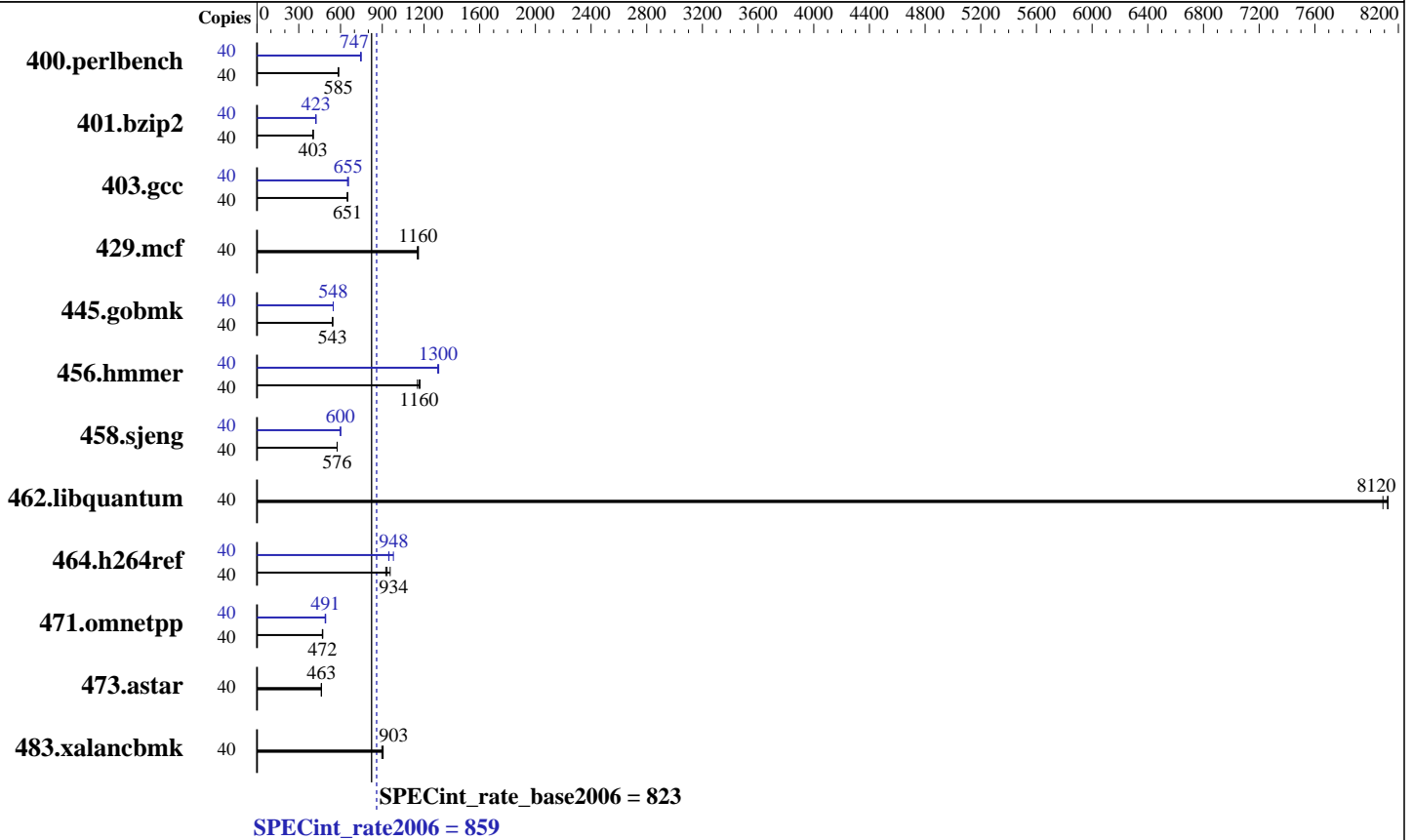
Test sponsor: Huawei

Tested by: Huawei

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2650 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

## Huawei

SPECint\_rate2006 = **859**

Huawei XH628 V3 (Intel Xeon E5-2650 v3)

SPECint\_rate\_base2006 = **823**

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Feb-2015  
Hardware Availability: Sep-2014  
Software Availability: Sep-2014

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	669	584	666	587	<b>669</b>	<b>585</b>	40	<b>523</b>	<b>747</b>	522	748	525	744
401.bzip2	40	<b>957</b>	<b>403</b>	961	402	954	405	40	914	422	912	423	<b>912</b>	<b>423</b>
403.gcc	40	494	651	497	648	<b>495</b>	<b>651</b>	40	495	650	491	656	<b>491</b>	<b>655</b>
429.mcf	40	315	1160	317	1150	<b>315</b>	<b>1160</b>	40	315	1160	317	1150	<b>315</b>	<b>1160</b>
445.gobmk	40	<b>772</b>	<b>543</b>	772	543	773	543	40	767	547	<b>766</b>	<b>548</b>	766	548
456.hammer	40	324	1150	319	1170	<b>321</b>	<b>1160</b>	40	286	1310	<b>287</b>	<b>1300</b>	287	1300
458.sjeng	40	<b>841</b>	<b>576</b>	841	575	840	576	40	806	600	807	600	<b>807</b>	<b>600</b>
462.libquantum	40	<b>102</b>	<b>8120</b>	102	8130	102	8090	40	<b>102</b>	<b>8120</b>	102	8130	102	8090
464.h264ref	40	957	925	925	957	<b>947</b>	<b>934</b>	40	935	946	903	980	<b>934</b>	<b>948</b>
471.omnetpp	40	530	472	533	469	<b>530</b>	<b>472</b>	40	510	490	<b>509</b>	<b>491</b>	507	493
473.astar	40	<b>606</b>	<b>463</b>	606	463	608	462	40	<b>606</b>	<b>463</b>	606	463	608	462
483.xalancbmk	40	305	906	308	897	<b>306</b>	<b>903</b>	40	305	906	308	897	<b>306</b>	<b>903</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Performance  
Set Snoop Mode to COD  
Sysinfo program /spec15/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Fri Feb 13 06:04:42 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2650 v3 @ 2.30GHz  
2 "physical id"s (chips)  
40 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 859

Huawei XH628 V3 (Intel Xeon E5-2650 v3)

SPECint\_rate\_base2006 = 823

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei

Test date: Feb-2015  
Hardware Availability: Sep-2014  
Software Availability: Sep-2014

## Platform Notes (Continued)

```
caution.)
  cpu cores : 5
  siblings  : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 12800 KB
```

```
From /proc/meminfo
MemTotal:      263576084 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Feb 12 04:51

```
SPEC is set to: /spec15
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  448G  180G  246G  43% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Insyde Corp. 1.20 10/25/2014
Memory:
 8x Samsung M393A2G40DB0-CPB 16 GB 1 rank 2133 MHz
 8x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz
```

(End of data from sysinfo program)



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 859

Huawei XH628 V3 (Intel Xeon E5-2650 v3)

SPECint\_rate\_base2006 = 823

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

The Huawei XH622 V3 and Huawei XH628 V3

are electronically equivalent.

The results have been measured on a Huawei XH628 V3 model.

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

## Base Other Flags

C benchmarks:

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 859

Huawei XH628 V3 (Intel Xeon E5-2650 v3)

SPECint\_rate\_base2006 = 823

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Base Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 859

Huawei XH628 V3 (Intel Xeon E5-2650 v3)

SPECint\_rate\_base2006 = 823

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Peak Optimization Flags (Continued)

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml>



# SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECint\_rate2006 = 859

Huawei XH628 V3 (Intel Xeon E5-2650 v3)

SPECint\_rate\_base2006 = 823

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue Mar 10 16:01:18 2015 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 10 March 2015.