



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint®2006 = 56.9

PRIMERGY BX2560 M1, Intel Xeon E5-2650 v3, 2.3 GHz

SPECint_base2006 = 54.6

CPU2006 license: 19

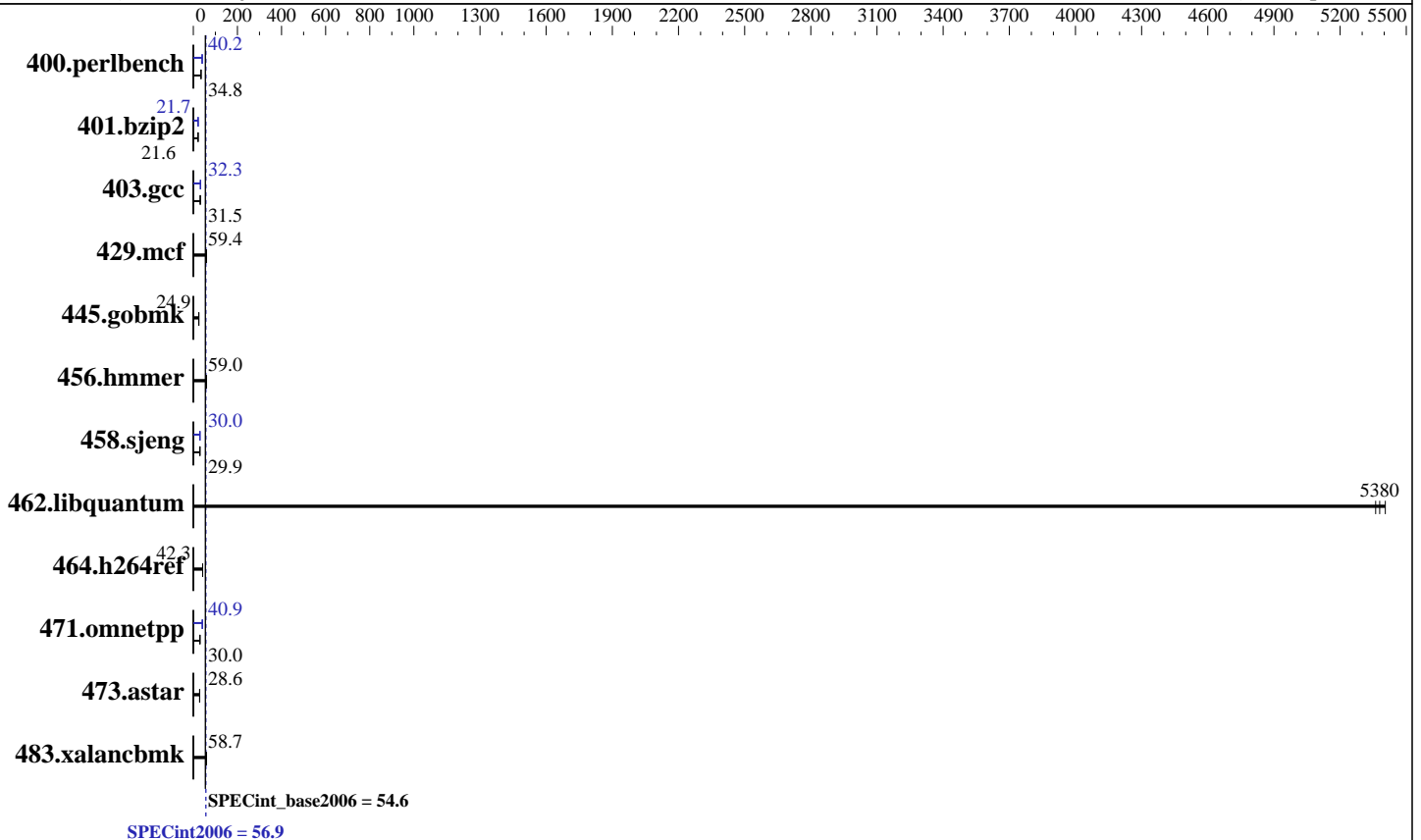
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



Hardware

CPU Name: Intel Xeon E5-2650 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
 Kernel 3.10.0-123.8.1.el7.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 56.9

PRIMERGY BX2560 M1, Intel Xeon E5-2650 v3, 2.3 GHz

SPECint_base2006 = 54.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	281	34.8	280	34.9	<u>281</u>	<u>34.8</u>	<u>243</u>	<u>40.2</u>	244	40.1	243	40.2
401.bzip2	447	21.6	<u>448</u>	<u>21.6</u>	450	21.5	445	21.7	<u>445</u>	<u>21.7</u>	444	21.7
403.gcc	256	31.5	256	31.5	<u>256</u>	<u>31.5</u>	<u>249</u>	<u>32.3</u>	249	32.3	249	32.4
429.mcf	156	58.5	153	59.6	<u>154</u>	<u>59.4</u>	156	58.5	153	59.6	<u>154</u>	<u>59.4</u>
445.gobmk	423	24.8	421	24.9	<u>422</u>	<u>24.9</u>	423	24.8	421	24.9	<u>422</u>	<u>24.9</u>
456.hammer	158	59.1	<u>158</u>	<u>59.0</u>	158	59.0	158	59.1	<u>158</u>	<u>59.0</u>	158	59.0
458.sjeng	<u>405</u>	<u>29.9</u>	405	29.9	405	29.9	<u>403</u>	<u>30.0</u>	403	30.0	403	30.0
462.libquantum	<u>3.85</u>	<u>5380</u>	3.86	5360	3.83	5400	<u>3.85</u>	<u>5380</u>	3.86	5360	3.83	5400
464.h264ref	522	42.4	<u>523</u>	<u>42.3</u>	525	42.2	522	42.4	<u>523</u>	<u>42.3</u>	525	42.2
471.omnetpp	206	30.3	<u>208</u>	<u>30.0</u>	211	29.6	<u>153</u>	<u>40.9</u>	154	40.6	152	41.0
473.astar	<u>245</u>	<u>28.6</u>	246	28.6	242	29.0	<u>245</u>	<u>28.6</u>	246	28.6	242	29.0
483.xalancbmk	118	58.3	118	58.7	<u>118</u>	<u>58.7</u>	118	58.3	118	58.7	<u>118</u>	<u>58.7</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced
QPI snoop mode: Early Snoop
COD Enable = Disabled, Early Snoop = Enabled
CPU C1E Support = Disabled

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"
OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

For information about Fujitsu please visit: <http://www.fujitsu.com>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 56.9

PRIMERGY BX2560 M1, Intel Xeon E5-2650 v3, 2.3 GHz

SPECint_base2006 = 54.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 56.9

PRIMERGY BX2560 M1, Intel Xeon E5-2650 v3, 2.3 GHz

SPECint_base2006 = 54.6

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014

Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

445.gobmk: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

464.h264ref: `-DSPEC_CPU_LP64`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `basepeak = yes`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4`

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 56.9

PRIMERGY BX2560 M1, Intel Xeon E5-2650 v3, 2.3 GHz

SPECint_base2006 = 54.6

CPU2006 license: 19

Test date: Feb-2015

Test sponsor: Fujitsu

Hardware Availability: Sep-2014

Tested by: Fujitsu

Software Availability: Sep-2014

Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Mar 10 16:04:01 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 March 2015.