



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

SPECfp<sup>®</sup>\_rate2006 = 844

PRIMERGY RX2530 M1, Intel Xeon E5-2695 v3, 2.3 GHz

SPECfp\_rate\_base2006 = 820

CPU2006 license: 19

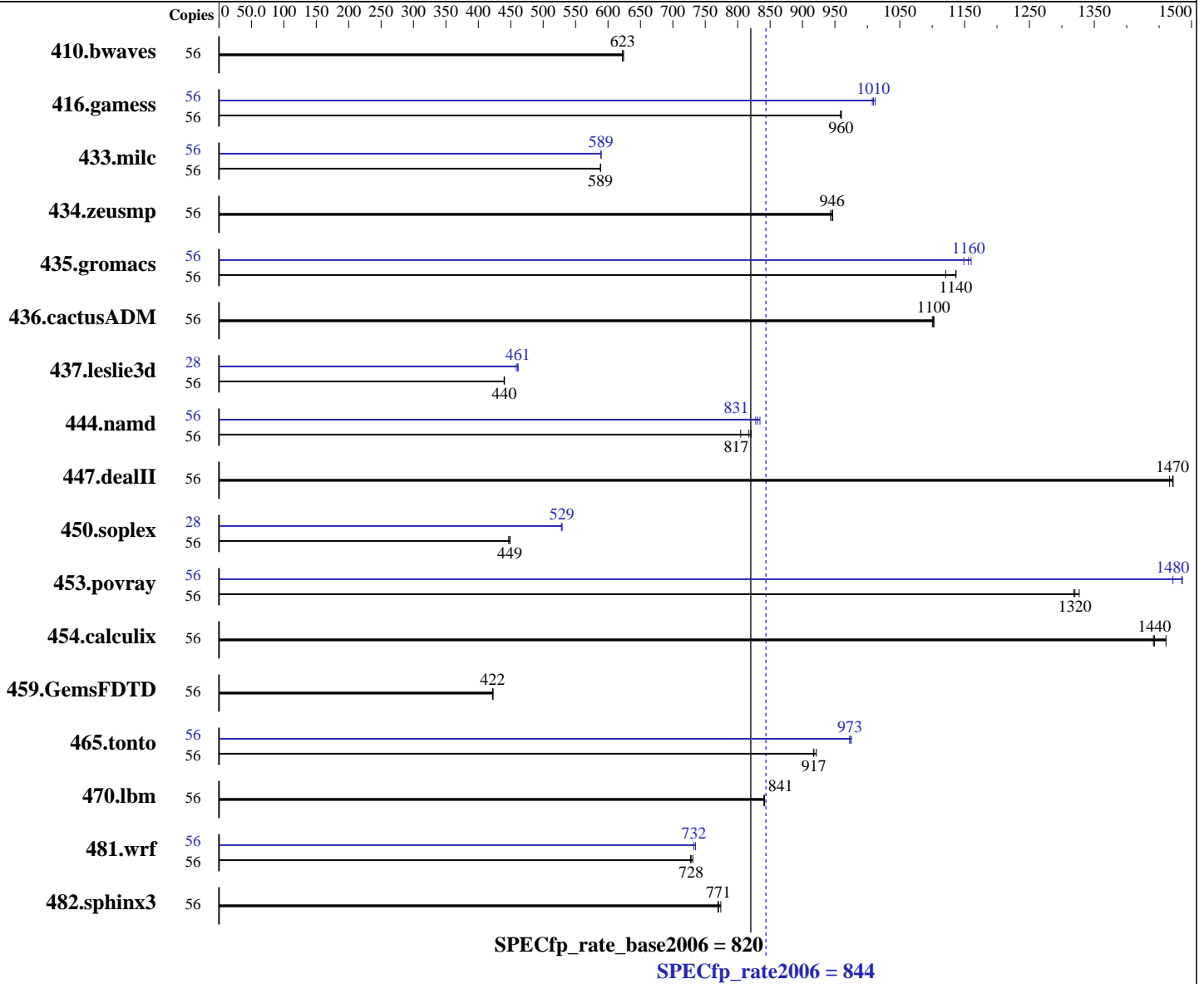
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2015

Hardware Availability: Feb-2015

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2695 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Kernel 3.10.0-123.8.1.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	1223	622	<u>1223</u>	<u>623</u>	1219	624	56	1223	622	<u>1223</u>	<u>623</u>	1219	624
416.gamess	56	1142	960	1144	959	<u>1143</u>	<u>960</u>	56	1088	1010	<u>1086</u>	<u>1010</u>	1083	1010
433.milc	56	873	589	874	588	<u>874</u>	<u>589</u>	56	872	589	873	589	<u>872</u>	<u>589</u>
434.zeusmp	56	<u>539</u>	<u>946</u>	540	943	538	947	56	<u>539</u>	<u>946</u>	540	943	538	947
435.gromacs	56	357	1120	352	1140	<u>352</u>	<u>1140</u>	56	345	1160	348	1150	<u>346</u>	<u>1160</u>
436.cactusADM	56	608	1100	607	1100	<u>607</u>	<u>1100</u>	56	608	1100	607	1100	<u>607</u>	<u>1100</u>
437.leslie3d	56	<u>1196</u>	<u>440</u>	1197	440	1195	440	28	570	461	<u>571</u>	<u>461</u>	574	459
444.namd	56	547	821	<u>550</u>	<u>817</u>	558	805	56	538	834	543	828	<u>541</u>	<u>831</u>
447.dealII	56	<u>436</u>	<u>1470</u>	437	1470	435	1470	56	<u>436</u>	<u>1470</u>	437	1470	435	1470
450.soplex	56	<u>1041</u>	<u>449</u>	1041	449	1045	447	28	442	528	<u>442</u>	<u>529</u>	441	529
453.povray	56	225	1330	226	1320	<u>226</u>	<u>1320</u>	56	202	1470	<u>201</u>	<u>1480</u>	200	1490
454.calculix	56	<u>320</u>	<u>1440</u>	316	1460	321	1440	56	<u>320</u>	<u>1440</u>	316	1460	321	1440
459.GemsFDTD	56	1408	422	<u>1407</u>	<u>422</u>	1405	423	56	1408	422	<u>1407</u>	<u>422</u>	1405	423
465.tonto	56	<u>601</u>	<u>917</u>	601	917	598	921	56	<u>566</u>	<u>973</u>	566	973	565	975
470.lbm	56	914	842	915	841	<u>915</u>	<u>841</u>	56	914	842	915	841	<u>915</u>	<u>841</u>
481.wrf	56	<u>859</u>	<u>728</u>	856	731	860	727	56	854	732	851	735	<u>854</u>	<u>732</u>
482.sphinx3	56	1418	770	<u>1416</u>	<u>771</u>	1410	774	56	1418	770	<u>1416</u>	<u>771</u>	1410	774

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy Performance = Performance  
QPI snoop mode: Cluster on Die

Continued on next page



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## Platform Notes (Continued)

COD Enable = Enabled, Early Snoop = Disabled  
CPU C1E Support = Disabled  
QPI Link Frequency Select = 6.4 GT/s

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

Continued on next page



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## Base Portability Flags (Continued)

459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64

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**Software Availability:** Sep-2014

## Peak Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

### C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -auto-ilp32

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -fno-alias -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
          -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
          -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
          -ansi-alias

```

### Fortran benchmarks:

410.bwaves: basepeak = yes

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## Peak Optimization Flags (Continued)

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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