



# SPEC® CFP2006 Result

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## Huawei

SPECfp®2006 = **95.4**

Huawei CH242 V3 (Intel Xeon E7-8857 v2)

SPECfp\_base2006 = **89.6**

CPU2006 license: 3175

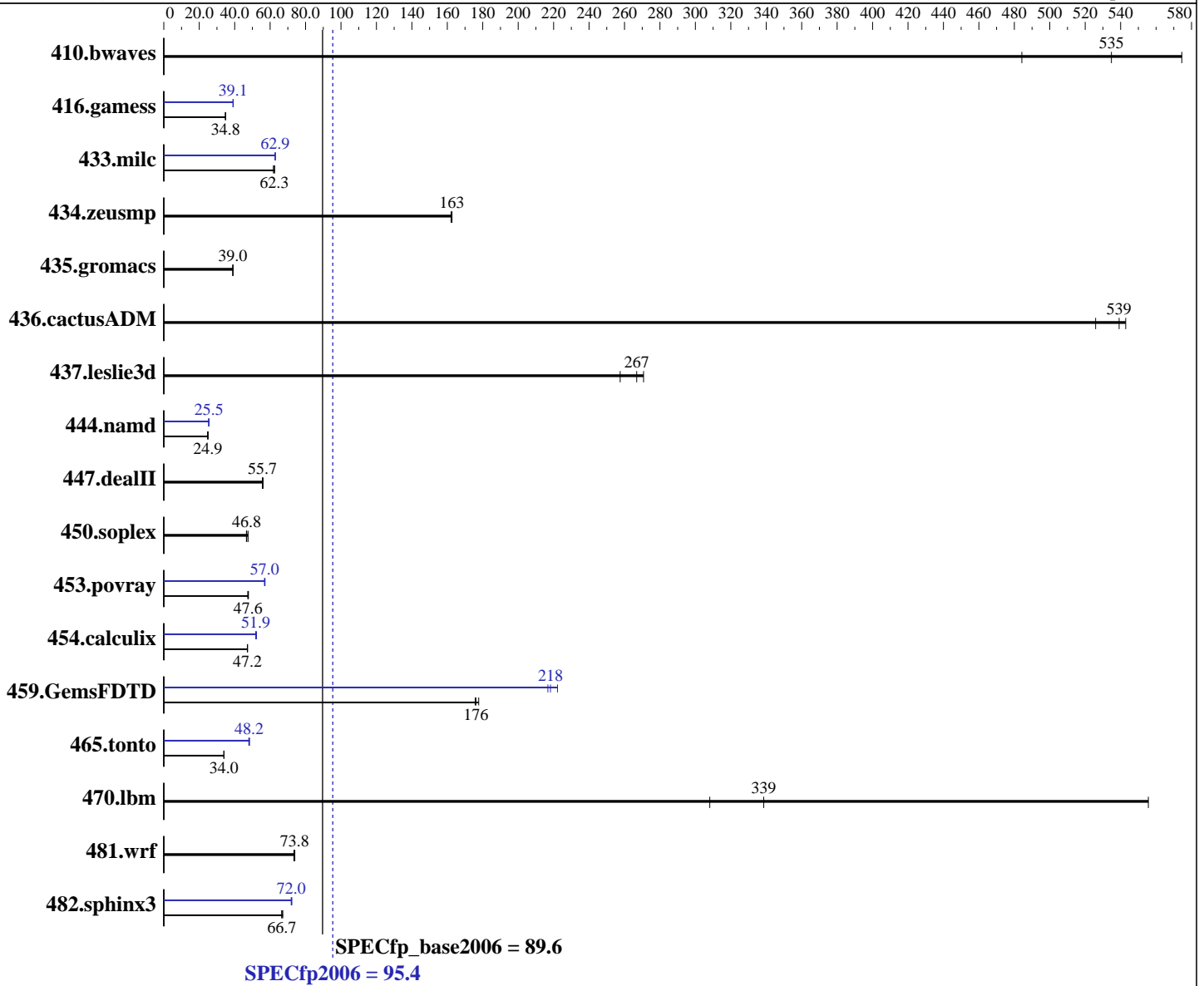
Test sponsor: Huawei

Tested by: Huawei

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E7-8857 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs

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L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	23.7	575	<b>25.4</b>	<b>535</b>	28.1	484	23.7	575	<b>25.4</b>	<b>535</b>	28.1	484
416.gamess	563	34.8	562	34.9	<b>563</b>	<b>34.8</b>	502	39.0	499	39.2	<b>501</b>	<b>39.1</b>
433.milc	148	61.8	<b>147</b>	<b>62.3</b>	147	62.6	146	62.8	146	63.0	<b>146</b>	<b>62.9</b>
434.zeusmp	56.1	162	<b>56.0</b>	<b>163</b>	56.0	163	56.1	162	<b>56.0</b>	<b>163</b>	56.0	163
435.gromacs	<b>183</b>	<b>39.0</b>	183	39.1	183	39.0	<b>183</b>	<b>39.0</b>	183	39.1	183	39.0
436.cactusADM	22.0	543	22.7	526	<b>22.2</b>	<b>539</b>	22.0	543	22.7	526	<b>22.2</b>	<b>539</b>
437.leslie3d	34.7	271	36.5	258	<b>35.2</b>	<b>267</b>	34.7	271	36.5	258	<b>35.2</b>	<b>267</b>
444.namd	321	25.0	<b>321</b>	<b>24.9</b>	325	24.7	314	25.5	<b>315</b>	<b>25.5</b>	318	25.3
447.dealII	<b>205</b>	<b>55.7</b>	205	55.7	204	56.0	<b>205</b>	<b>55.7</b>	205	55.7	204	56.0
450.soplex	175	47.6	179	46.6	<b>178</b>	<b>46.8</b>	175	47.6	179	46.6	<b>178</b>	<b>46.8</b>
453.povray	<b>112</b>	<b>47.6</b>	112	47.4	111	47.8	93.7	56.8	<b>93.3</b>	<b>57.0</b>	93.1	57.1
454.calculix	175	47.3	<b>175</b>	<b>47.2</b>	175	47.1	<b>159</b>	<b>51.9</b>	158	52.3	159	51.9
459.GemsFDTD	<b>60.2</b>	<b>176</b>	59.7	178	60.4	176	47.8	222	<b>48.6</b>	<b>218</b>	48.9	217
465.tonto	290	34.0	290	33.9	<b>290</b>	<b>34.0</b>	205	48.0	<b>204</b>	<b>48.2</b>	203	48.4
470.lbm	24.7	556	44.6	308	<b>40.6</b>	<b>339</b>	24.7	556	44.6	308	<b>40.6</b>	<b>339</b>
481.wrf	152	73.4	151	73.9	<b>151</b>	<b>73.8</b>	152	73.4	151	73.9	<b>151</b>	<b>73.8</b>
482.sphinx3	<b>292</b>	<b>66.7</b>	290	67.2	293	66.5	270	72.3	<b>271</b>	<b>72.0</b>	271	71.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Power Efficiency Mode to Custom  
 Baseboard Management Controller used to adjust the fan speed to 100%  
 Sysinfo program /spec/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
 running on localhost.localdomain Wed Feb 4 14:48:55 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-8857 v2 @ 3.00GHz

4 "physical id"s (chips)

48 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 12

siblings : 12

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13

physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13

cache size : 30720 KB

From /proc/meminfo

MemTotal: 263801328 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.0 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.0"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.0:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.0:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-123.el7.x86\_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Feb 4 09:11

SPEC is set to: /spec

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda2 xfs 445G 233G 212G 53% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS American Megatrends Inc. BLISV308 11/28/2014

Memory:

32x Micron 36KSF1G72PZ-1G6K1 8 GB 2 rank 1600 MHz, configured at 1333 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

OMP\_NUM\_THREADS = "48"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main

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## Base Portability Flags (Continued)

```

459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

```

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

```

## Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

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Software Availability: Sep-2014

## Peak Optimization Flags (Continued)

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

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Software Availability: Sep-2014

## Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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