



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint®_rate2006 = 2750

ProLiant DL580 Gen9
(2.30 GHz, Intel Xeon E7-8880 v3)

SPECint_rate_base2006 = 2650

CPU2006 license: 3

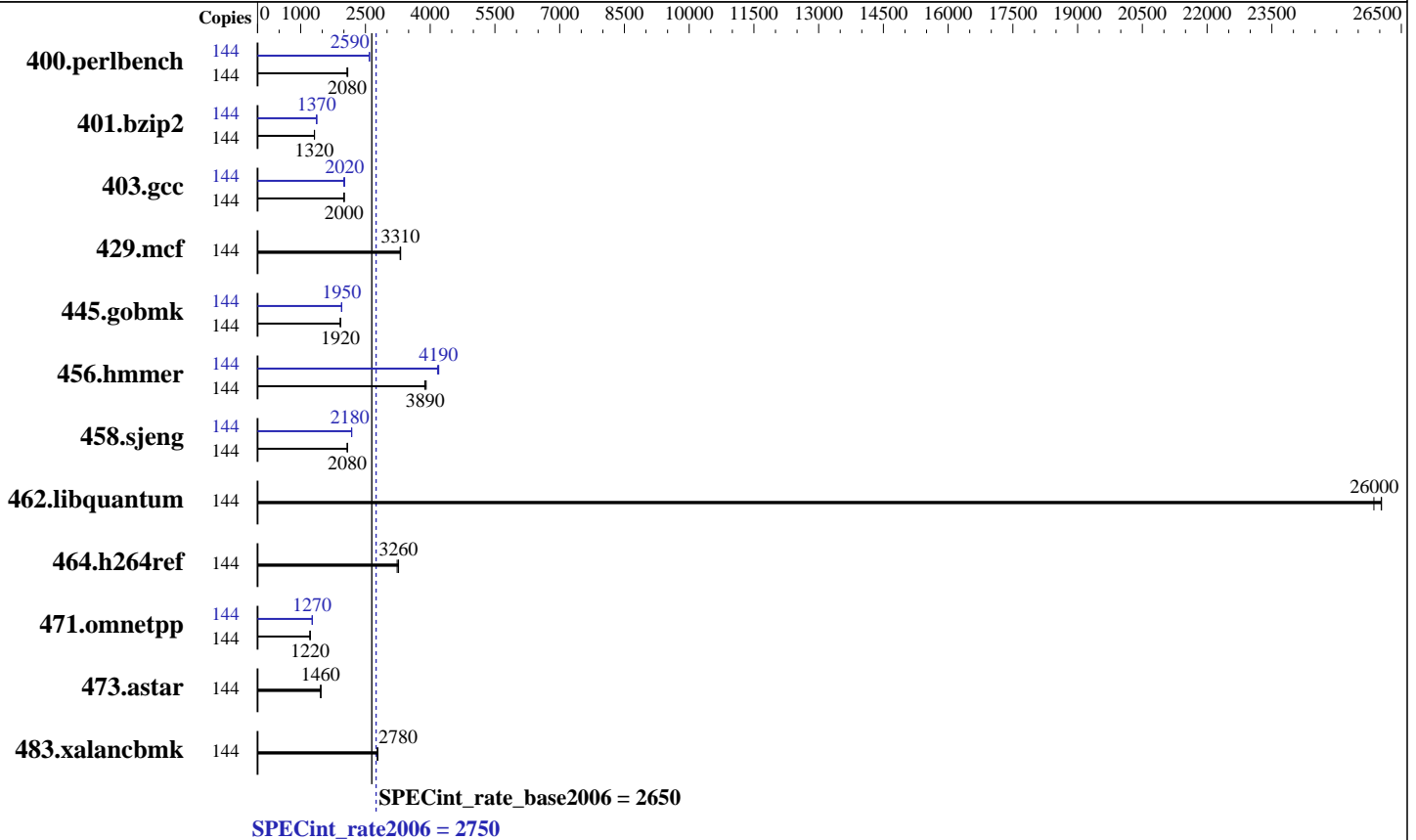
Test date: May-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: May-2015

Tested by: Hewlett-Packard Company

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E7-8880 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 72 cores, 4 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 45 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 Kernel 3.12.28-4-default
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2750

ProLiant DL580 Gen9
(2.30 GHz, Intel Xeon E7-8880 v3)

SPECint_rate_base2006 = 2650

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	144	677	2080	673	2090	678	2080	144	543	2590	543	2590	543	2590
401.bzip2	144	1053	1320	1056	1320	1054	1320	144	1014	1370	1015	1370	1016	1370
403.gcc	144	579	2000	577	2010	579	2000	144	580	2000	575	2020	575	2020
429.mcf	144	396	3320	397	3310	397	3310	144	396	3320	397	3310	397	3310
445.gobmk	144	787	1920	786	1920	787	1920	144	775	1950	775	1950	777	1950
456.hammer	144	346	3890	347	3870	344	3910	144	322	4170	321	4190	320	4200
458.sjeng	144	837	2080	837	2080	838	2080	144	800	2180	799	2180	799	2180
462.libquantum	144	115	26000	115	26000	115	25900	144	115	26000	115	26000	115	25900
464.h264ref	144	977	3260	976	3270	985	3230	144	977	3260	976	3270	985	3230
471.omnetpp	144	737	1220	741	1210	741	1220	144	711	1270	708	1270	711	1270
473.astar	144	691	1460	689	1470	691	1460	144	691	1460	689	1470	691	1460
483.xalanbmk	144	357	2780	358	2780	357	2780	144	357	2780	358	2780	357	2780

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2750

ProLiant DL580 Gen9
(2.30 GHz, Intel Xeon E7-8880 v3)

SPECint_rate_base2006 = 2650

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Platform Notes (Continued)

Sysinfo program /cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on dl580gen9-4-sles12 Tue May 5 16:27:23 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8880 v3 @ 2.30GHz
 4 "physical id"s (chips)
144 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 18
siblings : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB
```

```
From /proc/meminfo
MemTotal: 529156152 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux dl580gen9-4-sles12 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC
2014 (9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2750

ProLiant DL580 Gen9
(2.30 GHz, Intel Xeon E7-8880 v3)

SPECint_rate_base2006 = 2650

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Platform Notes (Continued)

run-level 3 May 5 11:44

SPEC is set to: /cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	371G	9.8G	361G	3%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U17 03/13/2015

Memory:

32x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1600 MHz
64x UNKNOWN NOT AVAILABLE

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have two lines reading as:
32x HP 752369-081 16 GB 2 rank 2133 MHz, configured at 1600 MHz

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2750

ProLiant DL580 Gen9
(2.30 GHz, Intel Xeon E7-8880 v3)

SPECint_rate_base2006 = 2650

CPU2006 license: 3

Test date: May-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: May-2015

Tested by: Hewlett-Packard Company

Software Availability: Oct-2014

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2750

ProLiant DL580 Gen9
(2.30 GHz, Intel Xeon E7-8880 v3)

SPECint_rate_base2006 = 2650

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: May-2015
Software Availability: Oct-2014

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2750

ProLiant DL580 Gen9
(2.30 GHz, Intel Xeon E7-8880 v3)

SPECint_rate_base2006 = 2650

CPU2006 license: 3

Test date: May-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: May-2015

Tested by: Hewlett-Packard Company

Software Availability: Oct-2014

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 2 13:49:08 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 June 2015.