



# SPEC® CINT2006 Result

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**NEC Corporation**

**SPECint\_rate2006 = 2800**

Express5800/A2040c (Intel Xeon E7-8890 v3)

**SPECint\_rate\_base2006 = 2720**

**CPU2006 license:** 9006

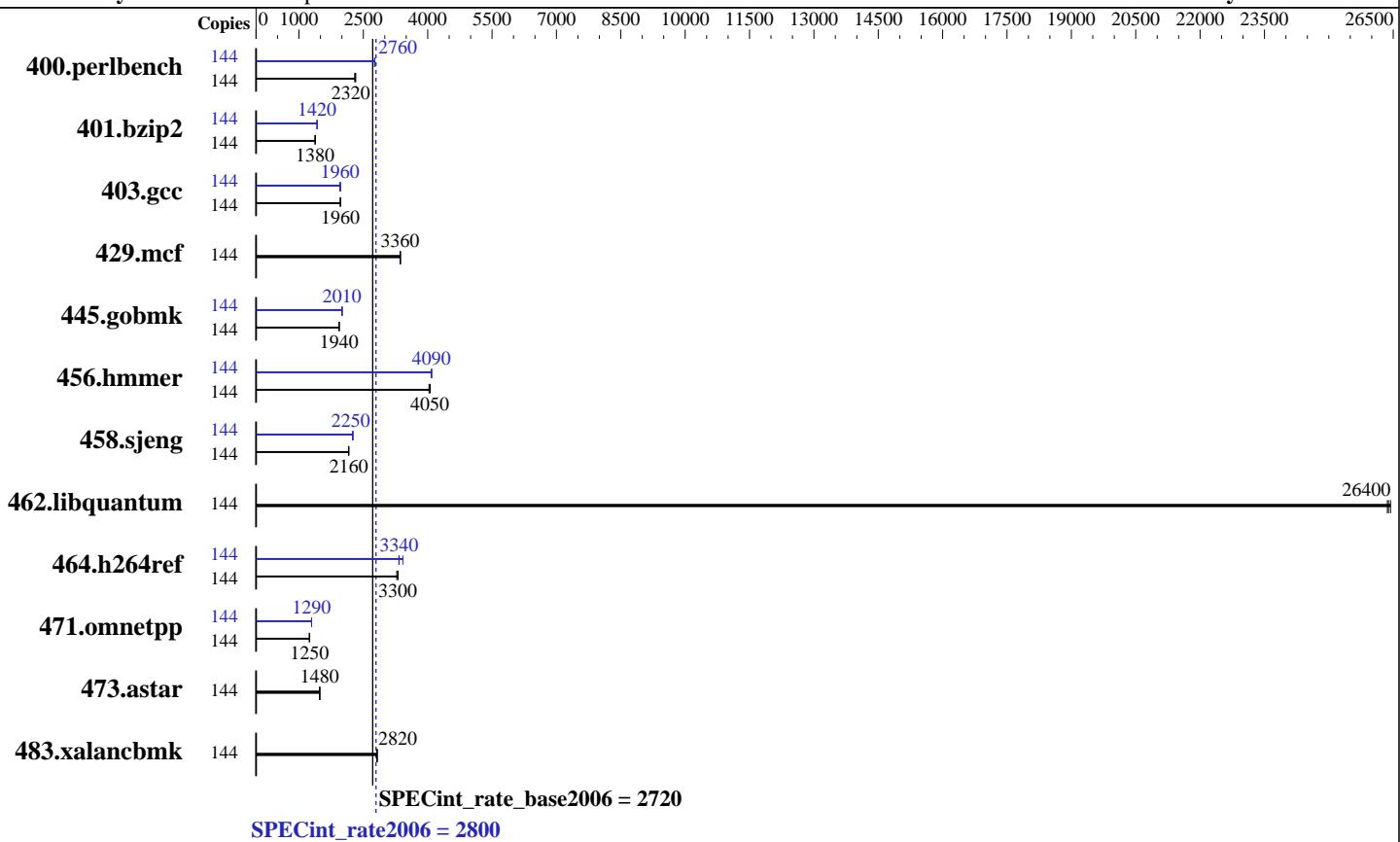
**Test date:** May-2015

**Test sponsor:** NEC Corporation

**Hardware Availability:** Jul-2015

**Tested by:** NEC Corporation

**Software Availability:** Mar-2015



## Hardware

CPU Name:	Intel Xeon E7-8890 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	72 cores, 4 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	45 MB I+D on chip per chip
Other Cache:	None
Memory:	1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	3 x 300 GB SAS, 15000 RPM, RAID 0
Other Hardware:	None

## Software

Operating System:	Red Hat Enterprise Linux Server release 6.6 (Santiago) Kernel 2.6.32-504.12.2.el6.x86_64
Compiler:	C/C++: Version 14.0.3.174 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap Multi-Core V10.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	144	<b>607</b>	<b>2320</b>	612	2300	606	2320	144	<b>509</b>	<b>2760</b>	508	2770	509	2760
401.bzip2	144	1013	1370	<b>1008</b>	<b>1380</b>	1005	1380	144	<b>977</b>	<b>1420</b>	977	1420	978	1420
403.gcc	144	589	1970	<b>590</b>	<b>1960</b>	590	1960	144	<b>595</b>	<b>1950</b>	589	1970	<b>591</b>	<b>1960</b>
429.mcf	144	<b>391</b>	<b>3360</b>	390	3370	391	3360	144	<b>391</b>	<b>3360</b>	390	3370	391	3360
445.gobmk	144	780	1940	<b>780</b>	<b>1940</b>	780	1940	144	<b>753</b>	<b>2010</b>	755	2000	<b>752</b>	2010
456.hammer	144	331	4060	333	4030	<b>332</b>	<b>4050</b>	144	<b>329</b>	<b>4090</b>	328	4090	329	4080
458.sjeng	144	808	2160	<b>807</b>	<b>2160</b>	806	2160	144	<b>774</b>	<b>2250</b>	771	2260	774	2250
462.libquantum	144	113	26400	<b>113</b>	<b>26400</b>	113	26400	144	113	26400	<b>113</b>	<b>26400</b>	113	26400
464.h264ref	144	<b>965</b>	<b>3300</b>	963	3310	972	3280	144	931	3420	957	3330	<b>955</b>	<b>3340</b>
471.omnetpp	144	<b>721</b>	<b>1250</b>	720	1250	727	1240	144	<b>695</b>	1300	<b>696</b>	<b>1290</b>	696	1290
473.astar	144	680	1490	682	1480	<b>681</b>	<b>1480</b>	144	680	1490	682	1480	<b>681</b>	<b>1480</b>
483.xalancbmk	144	353	2820	<b>352</b>	<b>2820</b>	352	2820	144	353	2820	<b>352</b>	<b>2820</b>	352	2820

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS Settings:

Memory RAS Mode: Independent  
VT-x : Disabled  
Processor C6 Report : Disabled  
OS Performance Tuning : Disabled  
Energy Performance : Performance  
Patrol Scrub : Disabled  
Demand Scrub : Disabled  
Memory P.E. Retry : Disabled



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## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/opt/SmartHeap_10mc/lib:/opt/SmartHeap_10mc/lib64:/opt/intel/composer_xe_2013_sp1.3.174/compiler/lib/ia32:/opt/intel/composer_xe_2013_sp1.3.174/compiler/lib/intel64"
```

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enable  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m32
```

C++ benchmarks:

```
icpc -m32
```

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/opt/SmartHeap_10mc/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/SmartHeap\_10mc/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040c-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040c-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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