



# SPEC® CINT2006 Result

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## ACTION S.A.

ACTINA SOLAR 820 X6 (Intel Xeon E5-2680 v3, 2.50 GHz)

**SPECint®2006 = 60.0**

**SPECint\_base2006 = 57.3**

CPU2006 license: 9008

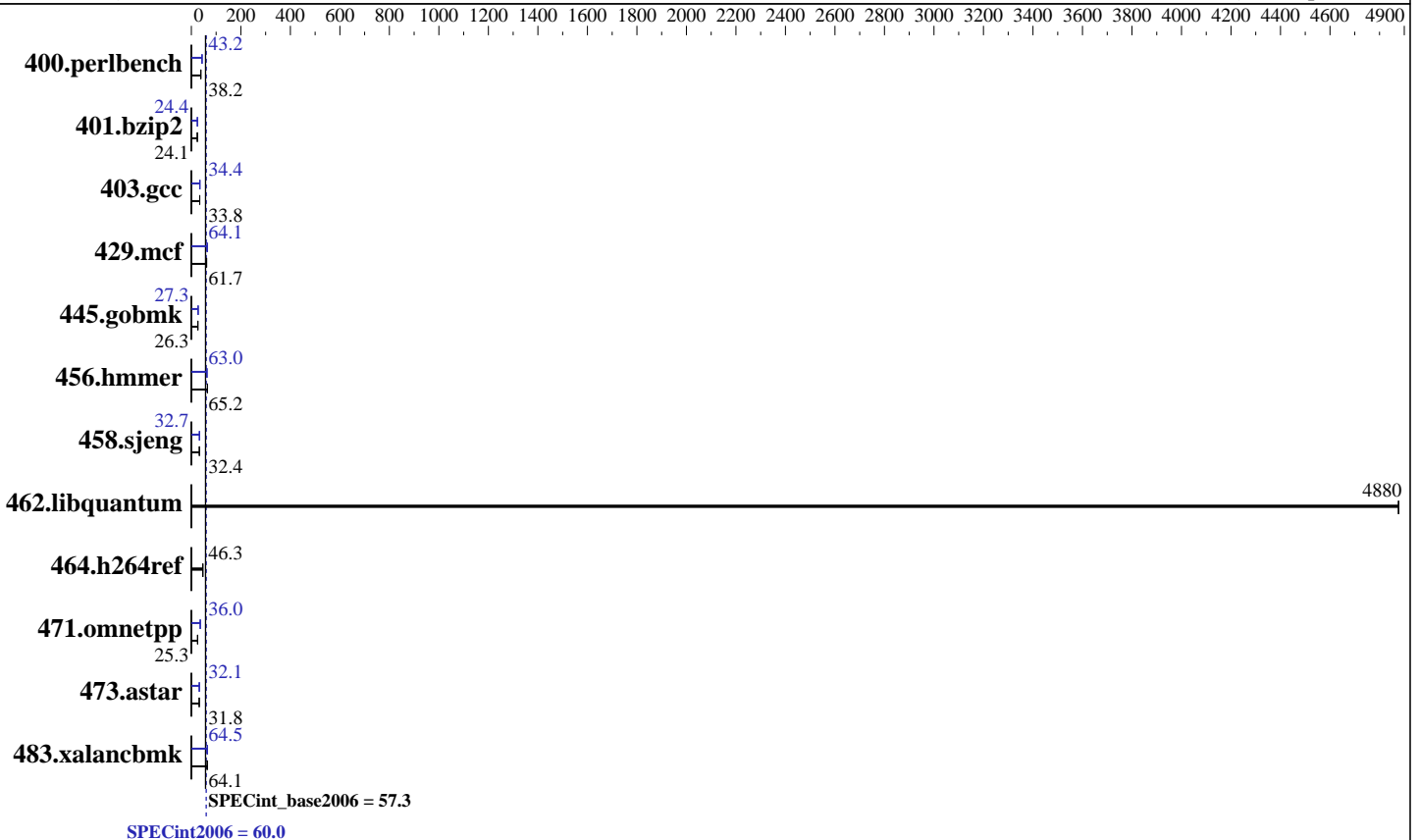
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Aug-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2680 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 240 GB SATA II SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-504.8.1.el6.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>255</u></b>	<b><u>38.2</u></b>	254	38.5	256	38.1	<b><u>226</u></b>	<b><u>43.2</u></b>	226	43.1	226	43.2
401.bzip2	399	24.2	402	24.0	<b><u>400</u></b>	<b><u>24.1</u></b>	396	24.4	<b><u>396</u></b>	<b><u>24.4</u></b>	396	24.4
403.gcc	238	33.9	<b><u>238</u></b>	<b><u>33.8</u></b>	238	33.8	<b><u>234</u></b>	<b><u>34.4</u></b>	235	34.3	233	34.6
429.mcf	147	61.9	<b><u>148</u></b>	<b><u>61.7</u></b>	148	61.5	143	63.7	<b><u>142</u></b>	<b><u>64.1</u></b>	142	64.3
445.gobmk	399	26.3	398	26.4	<b><u>398</u></b>	<b><u>26.3</u></b>	384	27.3	<b><u>384</u></b>	<b><u>27.3</u></b>	384	27.3
456.hammer	<b><u>143</u></b>	<b><u>65.2</u></b>	143	65.3	143	65.0	149	62.8	148	63.1	<b><u>148</u></b>	<b><u>63.0</u></b>
458.sjeng	373	32.4	373	32.4	<b><u>373</u></b>	<b><u>32.4</u></b>	370	32.7	<b><u>370</u></b>	<b><u>32.7</u></b>	370	32.7
462.libquantum	<b><u>4.25</u></b>	<b><u>4880</u></b>	4.25	4880	4.25	4880	<b><u>4.25</u></b>	<b><u>4880</u></b>	4.25	4880	4.25	4880
464.h264ref	476	46.5	<b><u>478</u></b>	<b><u>46.3</u></b>	479	46.2	476	46.5	<b><u>478</u></b>	<b><u>46.3</u></b>	479	46.2
471.omnetpp	249	25.1	<b><u>247</u></b>	<b><u>25.3</u></b>	247	25.3	174	36.0	<b><u>174</u></b>	<b><u>36.0</u></b>	175	35.8
473.astar	222	31.7	220	31.9	<b><u>221</u></b>	<b><u>31.8</u></b>	<b><u>219</u></b>	<b><u>32.1</u></b>	219	32.0	218	32.3
483.xalancbmk	107	64.2	<b><u>108</u></b>	<b><u>64.1</u></b>	108	64.0	<b><u>107</u></b>	<b><u>64.5</u></b>	107	64.7	107	64.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### Bios Settings:

Power & Performance = Performance  
Intel(R) Hyper-Threading Tech = Disabled  
Enforce POR = Disabled  
Memory Operating Speed Selection = Auto  
Cluster-on-Die = Enabled  
Set Fan Profile = Performance  
Fan PWM Offset = 0

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on SUT Fri Aug 7 14:06:03 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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### Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2680 v3 @ 2.50GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 12
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 15360 KB

```

```

From /proc/meminfo
MemTotal:      264423228 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux SUT 2.6.32-504.8.1.el6.x86_64 #1 SMP Wed Mar 11 12:12:13 CET 2015
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 7 14:04

```

SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext4  212G  40G  161G  20% /

```

```

Additional information from dmidecode:
BIOS Intel Corporation SE5C610.86B.01.01.0009.060120151350 06/01/2015
Memory:
 16x 16 GB
 16x Micron 36ASF2G72PZ-2G1A2 16 GB 2134 MHz 2 rank

```

(End of data from sysinfo program)  
dmidecode does not properly display info about memory modules  
16 modules of 16 GB were used to run the test (256 GB total)



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## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"  
OMP_NUM_THREADS = "24"
```

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Binaries compiled on a system with 2x Xeon E5-2650 v3 chips + 256 GB memory using RedHat EL 6.6

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
471.omnetpp: -DSPEC_CPU_LP64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap64
```



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L /opt/intel/composer\_xe\_2015/lib/ia32

445.gobmk: icc -m32 -L /opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m32 -L /opt/intel/composer\_xe\_2015/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32

-opt-prefetch -ansi-alias

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## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmarheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.xml>



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