



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL120 Gen9  
(2.20 GHz, Intel Xeon E5-2699 v4)

**SPECint®2006 = 72.4**

**SPECint\_base2006 = 70.5**

CPU2006 license: 3

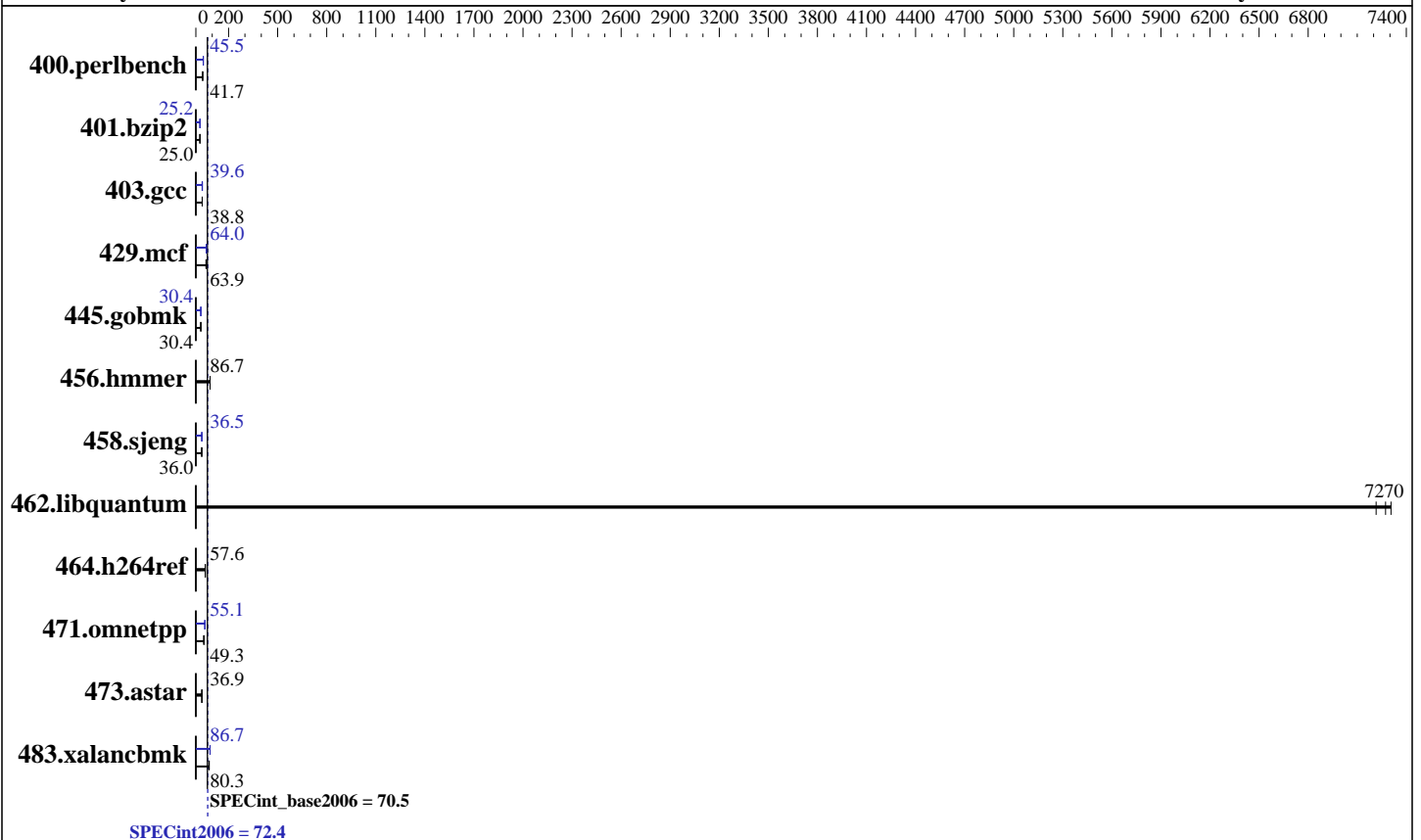
Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



## Hardware

## Software

CPU Name: Intel Xeon E5-2699 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 22 cores, 1 chip, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 55 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (4 x 32 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1  
 Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>234</u></b>	<b><u>41.7</u></b>	234	41.7	234	41.7	<b><u>215</u></b>	<b><u>45.5</u></b>	215	45.4	214	45.6
401.bzip2	391	24.7	<b><u>385</u></b>	<b><u>25.0</u></b>	383	25.2	<b><u>384</u></b>	<b><u>25.2</u></b>	384	25.2	384	25.1
403.gcc	207	38.9	<b><u>207</u></b>	<b><u>38.8</u></b>	208	38.7	211	38.2	<b><u>203</u></b>	<b><u>39.6</u></b>	202	39.8
429.mcf	<b><u>143</u></b>	<b><u>63.9</u></b>	140	65.0	148	61.6	143	63.8	<b><u>142</u></b>	<b><u>64.0</u></b>	142	64.1
445.gobmk	345	30.4	346	30.3	<b><u>345</u></b>	<b><u>30.4</u></b>	<b><u>345</u></b>	<b><u>30.4</u></b>	344	30.5	345	30.4
456.hmmer	107	87.0	108	86.7	<b><u>108</u></b>	<b><u>86.7</u></b>	107	87.0	108	86.7	<b><u>108</u></b>	<b><u>86.7</u></b>
458.sjeng	336	36.0	<b><u>336</u></b>	<b><u>36.0</u></b>	335	36.1	331	36.5	331	36.5	<b><u>331</u></b>	<b><u>36.5</u></b>
462.libquantum	2.84	7310	2.87	7220	<b><u>2.85</u></b>	<b><u>7270</u></b>	2.84	7310	2.87	7220	<b><u>2.85</u></b>	<b><u>7270</u></b>
464.h264ref	<b><u>384</u></b>	<b><u>57.6</u></b>	384	57.6	385	57.5	<b><u>384</u></b>	<b><u>57.6</u></b>	384	57.6	385	57.5
471.omnetpp	126	49.6	<b><u>127</u></b>	<b><u>49.3</u></b>	129	48.6	114	55.0	<b><u>114</u></b>	<b><u>55.1</u></b>	113	55.5
473.astar	<b><u>190</u></b>	<b><u>36.9</u></b>	189	37.1	192	36.6	<b><u>190</u></b>	<b><u>36.9</u></b>	189	37.1	192	36.6
483.xalancbmk	84.1	82.0	<b><u>85.9</u></b>	<b><u>80.3</u></b>	86.0	80.2	<b><u>79.2</u></b>	<b><u>87.2</u></b>	<b><u>79.6</u></b>	<b><u>86.7</u></b>	79.8	86.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

Intel Hyperthreading set to Enabled

QPI Snoop Configuration set to Home Snoop

Sysinfo program /cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on DL120.Nishanth Wed May 4 03:38:55 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page

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Page 2



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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz

1 "physical id"s (chips)

44 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 22

siblings : 44

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

cache size : 56320 KB

From /proc/meminfo

MemTotal: 131597360 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.2 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.2"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.2:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.2:ga:server

uname -a:

Linux DL120.Nishanth 3.10.0-327.el7.x86\_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 May 4 03:38

SPEC is set to: /cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-root	xfs	50G	12G	39G	23%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P86 02/22/2016

Memory:

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## Platform Notes (Continued)

4x UNKNOWN NOT AVAILABLE

4x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:

4x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

OMP\_NUM\_THREADS = "22"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

Continued on next page

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Page 4



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## Base Optimization Flags (Continued)

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

Continued on next page



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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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