



# SPEC<sup>®</sup> CFP2006 Result

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## Lenovo Group Limited

**SPECfp<sup>®</sup>2006 = 113**

Lenovo Flex System x240 M5  
(3.50 GHz, Intel Xeon E5-2637 v4)

**SPECfp\_base2006 = 110**

CPU2006 license: 9017

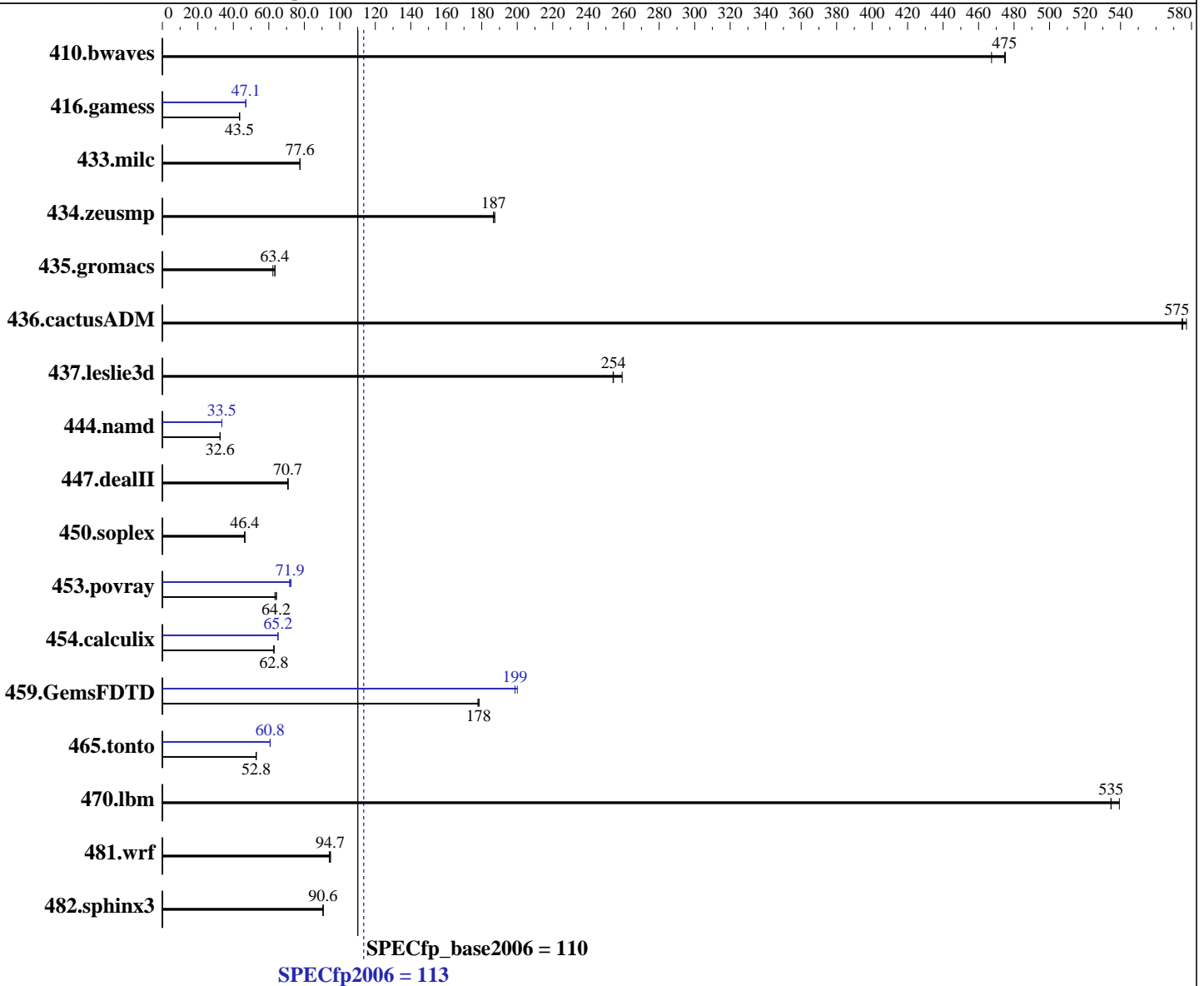
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2637 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86\_64)  
 Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID 0  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	29.1	467	<b>28.6</b>	<b>475</b>	28.6	475	29.1	467	<b>28.6</b>	<b>475</b>	28.6	475
416.gamess	450	43.5	<b>450</b>	<b>43.5</b>	450	43.5	417	47.0	<b>416</b>	<b>47.1</b>	416	47.1
433.milc	118	77.6	118	77.5	<b>118</b>	<b>77.6</b>	118	77.6	118	77.5	<b>118</b>	<b>77.6</b>
434.zeusmp	48.7	187	48.6	187	<b>48.7</b>	<b>187</b>	48.7	187	48.6	187	<b>48.7</b>	<b>187</b>
435.gromacs	115	62.3	112	63.5	<b>113</b>	<b>63.4</b>	115	62.3	112	63.5	<b>113</b>	<b>63.4</b>
436.cactusADM	<b>20.8</b>	<b>575</b>	20.7	577	20.8	575	<b>20.8</b>	<b>575</b>	20.7	577	20.8	575
437.leslie3d	37.0	254	<b>37.0</b>	<b>254</b>	36.3	259	37.0	254	<b>37.0</b>	<b>254</b>	36.3	259
444.namd	247	32.5	246	32.6	<b>246</b>	<b>32.6</b>	<b>239</b>	<b>33.5</b>	239	33.5	239	33.5
447.dealII	<b>162</b>	<b>70.7</b>	161	70.9	162	70.7	<b>162</b>	<b>70.7</b>	161	70.9	162	70.7
450.soplex	180	46.4	<b>180</b>	<b>46.4</b>	179	46.5	180	46.4	<b>180</b>	<b>46.4</b>	179	46.5
453.povray	83.9	63.4	<b>82.9</b>	<b>64.2</b>	82.7	64.3	74.2	71.7	<b>74.0</b>	<b>71.9</b>	73.3	72.5
454.calculix	131	62.9	131	62.8	<b>131</b>	<b>62.8</b>	127	65.2	127	65.1	<b>127</b>	<b>65.2</b>
459.GemsFDTD	<b>59.5</b>	<b>178</b>	59.4	179	59.7	178	53.0	200	<b>53.4</b>	<b>199</b>	53.4	199
465.tonto	<b>186</b>	<b>52.8</b>	186	53.0	186	52.8	162	60.8	<b>162</b>	<b>60.8</b>	162	60.7
470.lbm	25.7	535	<b>25.7</b>	<b>535</b>	25.5	539	25.7	535	<b>25.7</b>	<b>535</b>	25.5	539
481.wrf	<b>118</b>	<b>94.7</b>	118	94.7	119	94.0	<b>118</b>	<b>94.7</b>	118	94.7	119	94.0
482.sphinx3	<b>215</b>	<b>90.6</b>	215	90.5	215	90.7	<b>215</b>	<b>90.6</b>	215	90.5	215	90.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:  
Operating Mode set to Maximum Performance  
Intel Hyperthreading set to Disabled  
COD Preference set to Disable  
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on x240m5-cpu2006 Mon May 30 00:02:56 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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### Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2637 v4 @ 3.50GHz
 2 "physical id"s (chips)
 8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings  : 4
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal: 263830888 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

From /etc/\*release\* /etc/\*version\*

SuSE-release:

```
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
```

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

```
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

uname -a:

```
Linux x240m5-cpu2006 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 29 19:20

SPEC is set to: /home/cpu2006-1.2-ic16.0

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 515G 7.4G 508G 2% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS LENOVO -[C4E123H-2.10]- 03/25/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

8x NO DIMM Unknown

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP\_NUM\_THREADS = "8"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB  
memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



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## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

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## Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>

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Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jun 30 14:07:30 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 June 2016.