



SPEC® CFP2006 Result

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Lenovo Group Limited

SPECfp®2006 = **99.5**

Lenovo System x3550 M5
(1.80 GHz, Intel Xeon E5-2648L v4)

SPECfp_base2006 = **94.2**

CPU2006 license: 9017

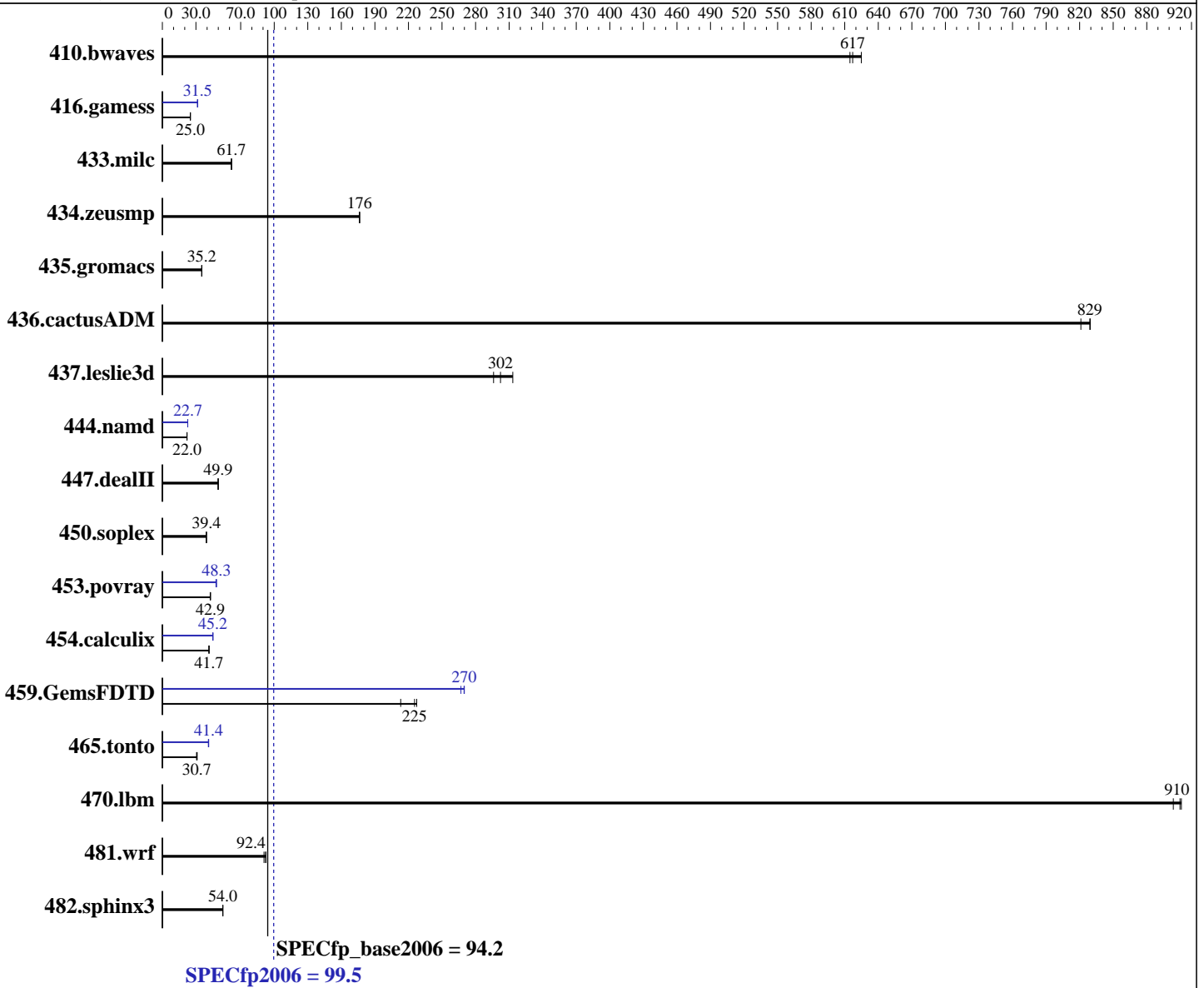
Test date: May-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



Hardware	
CPU Name:	Intel Xeon E5-2648L v4
CPU Characteristics:	Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	28 cores, 2 chips, 14 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Software	
Operating System:	SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.1	615	<u>22.0</u>	<u>617</u>	21.8	625	22.1	615	<u>22.0</u>	<u>617</u>	21.8	625
416.gamess	781	25.1	783	25.0	<u>783</u>	<u>25.0</u>	<u>622</u>	<u>31.5</u>	621	31.5	623	31.4
433.milc	149	61.7	148	61.9	<u>149</u>	<u>61.7</u>	149	61.7	148	61.9	<u>149</u>	<u>61.7</u>
434.zeusmp	51.7	176	<u>51.6</u>	<u>176</u>	51.6	176	51.7	176	<u>51.6</u>	<u>176</u>	51.6	176
435.gromacs	203	35.2	<u>203</u>	<u>35.2</u>	203	35.3	203	35.2	<u>203</u>	<u>35.2</u>	203	35.3
436.cactusADM	14.6	821	<u>14.4</u>	<u>829</u>	14.4	829	14.6	821	<u>14.4</u>	<u>829</u>	14.4	829
437.leslie3d	30.0	313	31.7	296	<u>31.1</u>	<u>302</u>	30.0	313	31.7	296	<u>31.1</u>	<u>302</u>
444.namd	<u>364</u>	<u>22.0</u>	364	22.0	365	22.0	354	22.7	353	22.7	<u>353</u>	<u>22.7</u>
447.dealII	230	49.8	229	50.0	<u>229</u>	<u>49.9</u>	230	49.8	229	50.0	<u>229</u>	<u>49.9</u>
450.soplex	<u>212</u>	<u>39.4</u>	212	39.3	212	39.4	<u>212</u>	<u>39.4</u>	212	39.3	212	39.4
453.povray	<u>124</u>	<u>42.9</u>	124	43.1	124	42.8	110	48.3	111	47.9	<u>110</u>	<u>48.3</u>
454.calculix	<u>198</u>	<u>41.7</u>	198	41.6	198	41.7	183	45.1	<u>183</u>	<u>45.2</u>	182	45.2
459.GemsFDTD	<u>47.1</u>	<u>225</u>	46.7	227	49.8	213	<u>39.3</u>	<u>270</u>	39.8	267	39.3	270
465.tonto	316	31.1	<u>320</u>	<u>30.7</u>	321	30.7	<u>238</u>	<u>41.4</u>	239	41.1	238	41.4
470.lbm	15.2	904	<u>15.1</u>	<u>910</u>	15.1	911	15.2	904	<u>15.1</u>	<u>910</u>	15.1	911
481.wrf	<u>121</u>	<u>92.4</u>	121	92.5	123	90.9	<u>121</u>	<u>92.4</u>	121	92.5	123	90.9
482.sphinx3	361	54.0	<u>361</u>	<u>54.0</u>	360	54.1	361	54.0	<u>361</u>	<u>54.0</u>	360	54.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:

Operating Mode set to Maximum Performance

Intel Hyperthreading set to Disabled

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on DaAn-04 Wed May 25 20:36:18 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2648L v4@ 1.80GHz
 2 "physical id"s (chips)
28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 14
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

```

```

From /proc/meminfo
MemTotal:      263829056 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP1"
  VERSION_ID="12.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux DaAn-04 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 May 25 14:01

```

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   688G   28G  661G   5% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

BIOS LENOVO -[TBE124K-2.10]- 05/10/2016

Memory:

16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

447.dealII: -DSPEC_CPU_LP64

450.soplex: -DSPEC_CPU_LP64

453.povray: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
 -ansi-alias

C++ benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
 -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
 -ansi-alias

Peak Compiler Invocation

C benchmarks:
 icc -m64

C++ benchmarks:
 icpc -m64

Fortran benchmarks:
 ifort -m64

Benchmarks using both Fortran and C:
 icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

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