



SPEC® CFP2006 Result

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Lenovo Group Limited

SPECfp®2006 = 117

Lenovo System x3950 X6
(2.10 GHz, Intel Xeon E7-8870 v4)

SPECfp_base2006 = 109

CPU2006 license: 9017

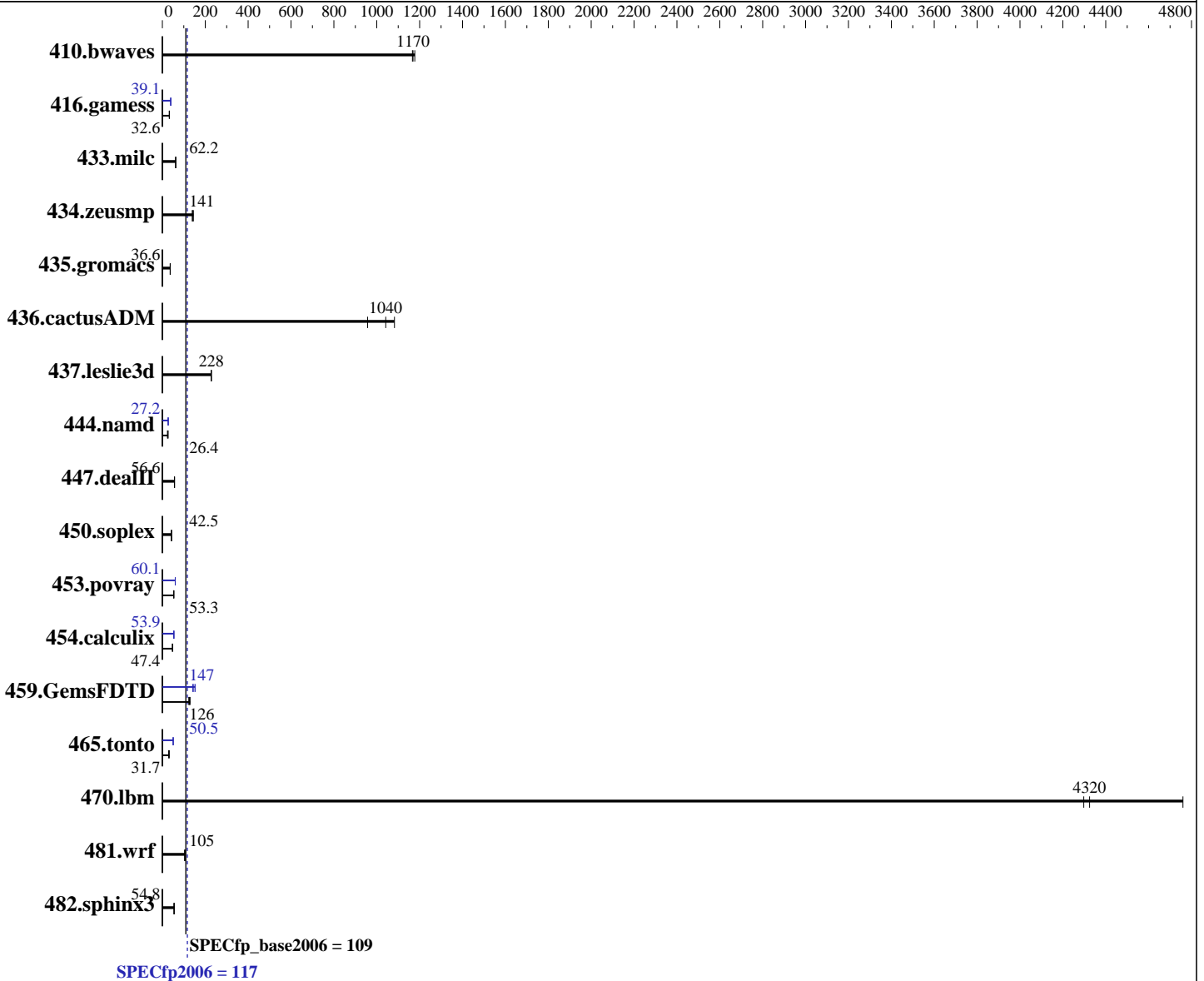
Test date: Jul-2016

Test sponsor: Lenovo Group Limited

Hardware Availability: Jun-2016

Tested by: Lenovo Group Limited

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E7-8870 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 160 cores, 8 chips, 20 cores/chip
 CPU(s) orderable: 4,8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 50 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem: 2 x 600 GB 15000 RPM SAS, RAID 1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	11.7	1170	11.6	1170	11.5	1180	11.7	1170	11.6	1170	11.5	1180
416.gamess	601	32.6	601	32.6	601	32.6	501	39.1	501	39.0	501	39.1
433.milc	147	62.3	147	62.2	148	62.1	147	62.3	147	62.2	148	62.1
434.zeusmp	65.3	139	64.5	141	62.9	145	65.3	139	64.5	141	62.9	145
435.gromacs	194	36.7	195	36.6	195	36.5	194	36.7	195	36.6	195	36.5
436.cactusADM	12.5	957	11.5	1040	11.0	1080	12.5	957	11.5	1040	11.0	1080
437.leslie3d	41.2	228	41.1	229	41.3	227	41.2	228	41.1	229	41.3	227
444.namd	338	23.7	304	26.4	304	26.4	295	27.2	295	27.2	295	27.2
447.dealII	201	56.9	202	56.6	203	56.5	201	56.9	202	56.6	203	56.5
450.soplex	196	42.5	197	42.3	196	42.5	196	42.5	197	42.3	196	42.5
453.povray	99.7	53.3	99.9	53.3	99.5	53.5	88.3	60.3	88.5	60.1	88.5	60.1
454.calculix	174	47.4	175	47.2	174	47.4	152	54.2	153	53.9	156	52.9
459.GemsFDTD	86.2	123	84.1	126	82.0	129	74.8	142	72.2	147	69.4	153
465.tonto	338	29.1	310	31.7	309	31.8	195	50.4	195	50.5	194	50.6
470.lbm	2.89	4760	3.20	4300	3.18	4320	2.89	4760	3.20	4300	3.18	4320
481.wrf	107	104	106	106	107	105	107	104	106	106	107	105
482.sphinx3	350	55.7	355	54.8	362	53.8	350	55.7	355	54.8	362	53.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:

Operating Mode set to "Maximum Performance"

Hyper-Threading set to Disable

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on X3950-01-SLES12SP1 Thu Jul 21 23:25:52 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```

model name : Intel(R) Xeon(R) CPU E7-8870 v4 @ 2.10GHz
 8 "physical id"s (chips)
160 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 20
  siblings  : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 4: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 5: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 6: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 7: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
cache size : 51200 KB

```

From /proc/meminfo

```

MemTotal:      1058553356 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

uname -a:

```

Linux X3950-01-SLES12SP1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC
2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 21 17:54

SPEC is set to: /home/cpu2006-1.2-ic16.0

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   512G  6.8G  505G   2% /home

```

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Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[A9E135CUS-3.10]- 06/16/2016

Memory:

128x NO DIMM Unknown

64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "160"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):

`-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Tue Aug 9 17:04:48 2016 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 9 August 2016.