



SPEC[®] CFP2006 Result

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Lenovo Group Limited

Lenovo ThinkServer TD350
(2.40 GHz, Intel Xeon E5-2680 v4)

SPECfp[®]2006 = **121**

SPECfp_base2006 = **115**

CPU2006 license: 9017

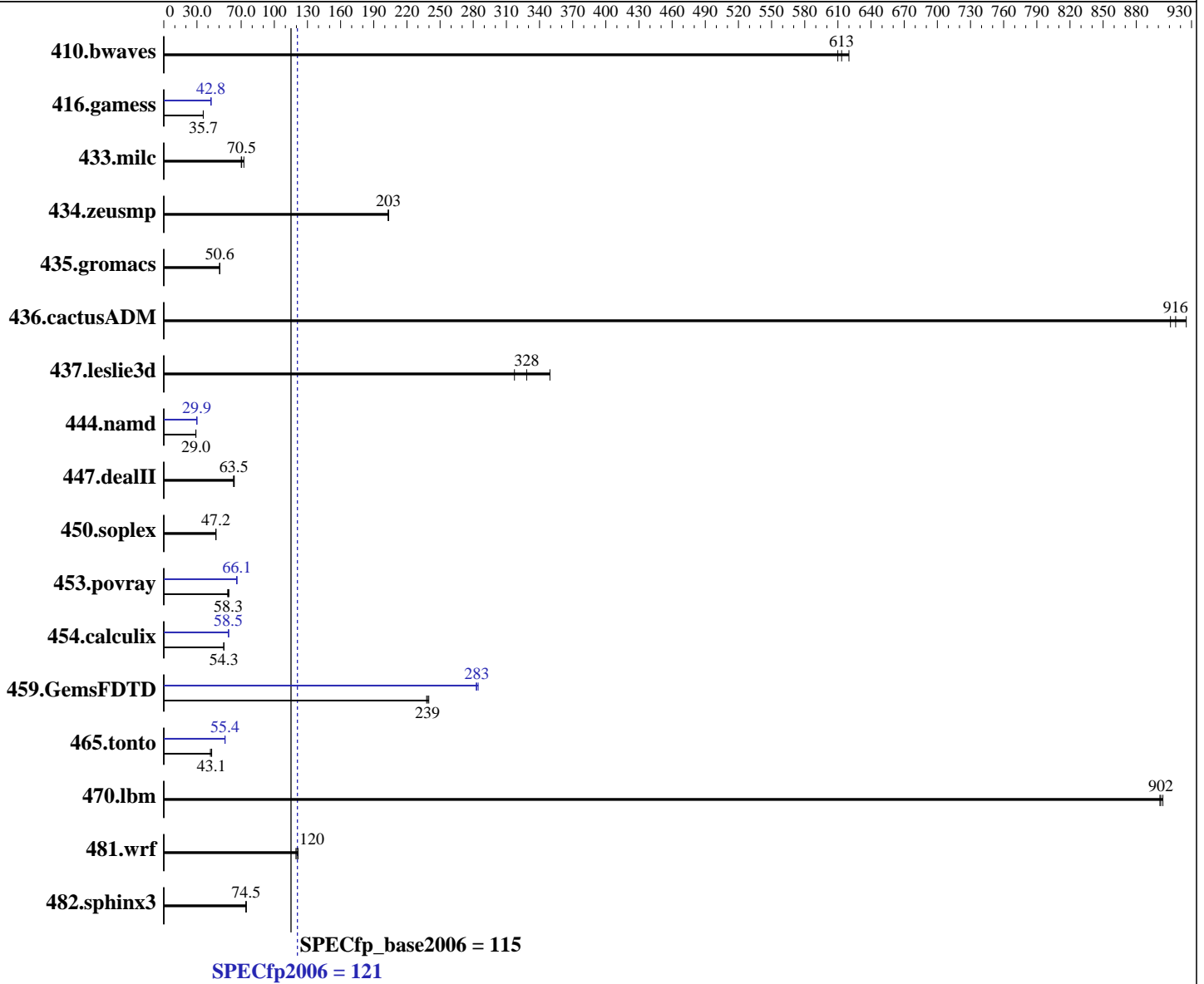
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Oct-2016

Hardware Availability: Mar-2016

Software Availability: Mar-2016



Hardware

CPU Name: Intel Xeon E5-2680 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Kernel 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)

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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	21.9	620	<u>22.2</u>	<u>613</u>	22.3	610	21.9	620	<u>22.2</u>	<u>613</u>	22.3	610
416.gamess	549	35.7	547	35.8	<u>548</u>	<u>35.7</u>	<u>458</u>	<u>42.8</u>	458	42.8	459	42.6
433.milc	<u>130</u>	<u>70.5</u>	127	72.5	131	70.1	<u>130</u>	<u>70.5</u>	127	72.5	131	70.1
434.zeusmp	44.7	203	44.8	203	<u>44.8</u>	<u>203</u>	44.7	203	44.8	203	<u>44.8</u>	<u>203</u>
435.gromacs	<u>141</u>	<u>50.6</u>	141	50.7	141	50.5	<u>141</u>	<u>50.6</u>	141	50.7	141	50.5
436.cactusADM	<u>13.1</u>	<u>916</u>	13.1	911	12.9	925	<u>13.1</u>	<u>916</u>	13.1	911	12.9	925
437.leslie3d	26.9	349	<u>28.6</u>	<u>328</u>	29.6	317	26.9	349	<u>28.6</u>	<u>328</u>	29.6	317
444.namd	276	29.0	276	29.0	<u>276</u>	<u>29.0</u>	<u>268</u>	<u>29.9</u>	268	29.9	268	29.9
447.dealII	181	63.1	<u>180</u>	<u>63.5</u>	179	63.8	181	63.1	<u>180</u>	<u>63.5</u>	179	63.8
450.soplex	177	47.1	177	47.2	<u>177</u>	<u>47.2</u>	177	47.1	177	47.2	<u>177</u>	<u>47.2</u>
453.povray	92.0	57.8	<u>91.2</u>	<u>58.3</u>	90.3	58.9	<u>80.4</u>	<u>66.1</u>	80.8	65.8	80.3	66.2
454.calculix	152	54.4	152	54.3	<u>152</u>	<u>54.3</u>	<u>141</u>	<u>58.5</u>	141	58.5	141	58.7
459.GemsFDTD	44.6	238	44.2	240	<u>44.5</u>	<u>239</u>	37.5	283	37.3	285	<u>37.5</u>	<u>283</u>
465.tonto	227	43.3	233	42.2	<u>228</u>	<u>43.1</u>	178	55.4	178	55.4	<u>178</u>	<u>55.4</u>
470.lbm	15.2	904	<u>15.2</u>	<u>902</u>	15.2	901	15.2	904	<u>15.2</u>	<u>902</u>	15.2	901
481.wrf	93.4	120	<u>92.8</u>	<u>120</u>	92.0	121	93.4	120	<u>92.8</u>	<u>120</u>	92.0	121
482.sphinx3	262	74.3	261	74.7	<u>262</u>	<u>74.5</u>	262	74.3	261	74.7	<u>262</u>	<u>74.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Cluster On Die set to Disabled
Early Snoop set to Enabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled

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Platform Notes (Continued)

Thermal Profile set to Max Performance
Memory Power Savings set to Disabled
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on TD350-03 Wed Oct 12 18:45:12 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2680 v4@ 2.40GHz
 2 "physical id"s (chips)
 28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores      : 14
siblings       : 14
physical 0:    cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1:    cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size     : 35840 KB
```

```
From /proc/meminfo
MemTotal:      264558968 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux TD350-03 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 12 13:41

SPEC is set to: /home/cpu2006-1.2-ic16.0
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Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	689G	8.1G	681G	2%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO TB5TS362 03/24/2016

Memory:

16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

```

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):

`-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml>

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