



# SPEC® CINT2006 Result

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## Huawei

### SPECint®\_rate2006 = 737

### Huawei RH1288 V3 (Intel Xeon E5-2643 v4)

### SPECint\_rate\_base2006 = 700

CPU2006 license: 3175

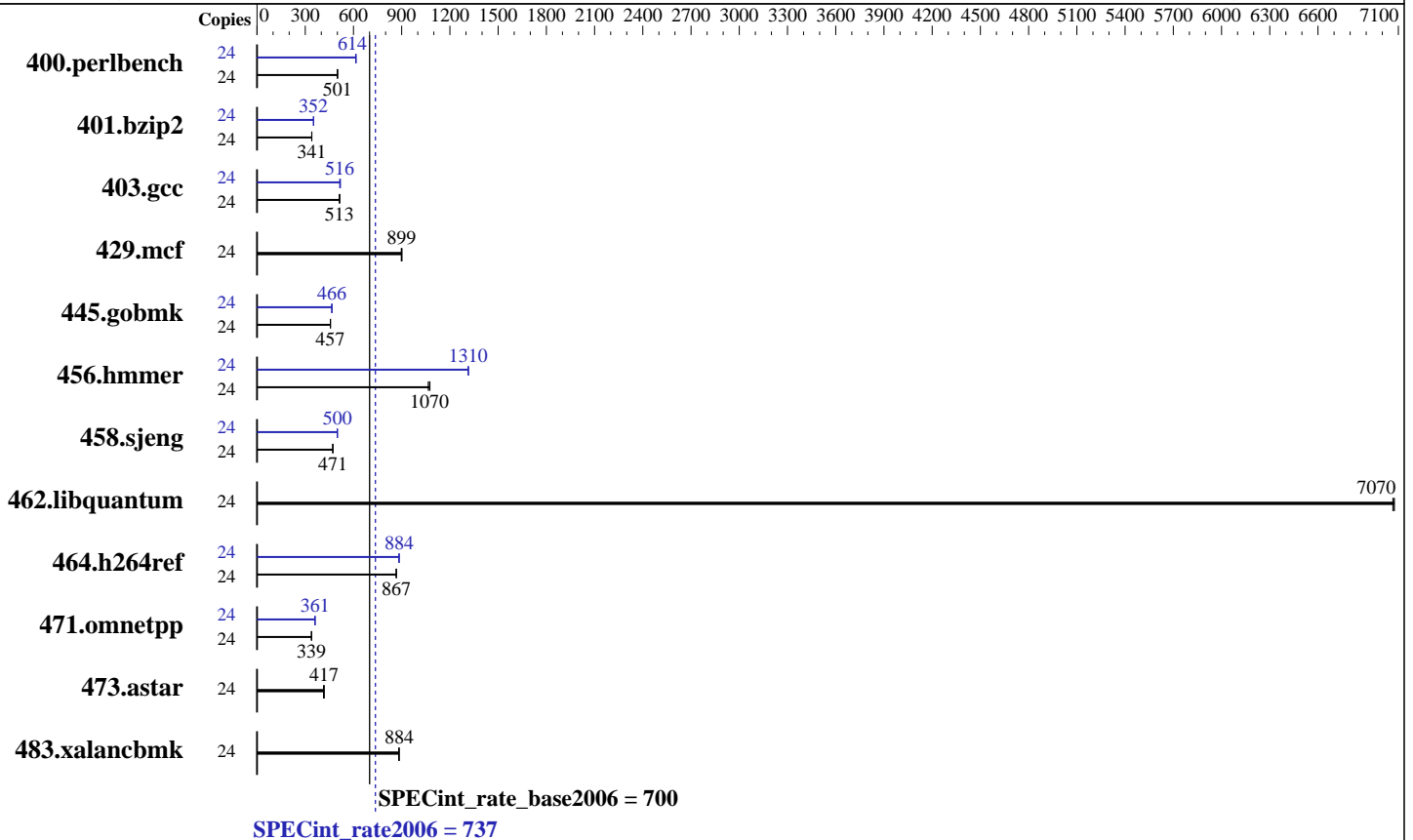
Test sponsor: Huawei

Tested by: Huawei

Test date: Oct-2016

Hardware Availability: Mar-2016

Software Availability: Mar-2016



### Hardware

CPU Name: Intel Xeon E5-2643 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (8 x 32 GB 2Rx8 PC4-2400T-R)  
 Disk Subsystem: 2 x 600 GB SAS, 10K RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

| Benchmark      | Base   |             |             |            |             |         |       | Peak   |             |             |            |             |            |            |
|----------------|--------|-------------|-------------|------------|-------------|---------|-------|--------|-------------|-------------|------------|-------------|------------|------------|
|                | Copies | Seconds     | Ratio       | Seconds    | Ratio       | Seconds | Ratio | Copies | Seconds     | Ratio       | Seconds    | Ratio       | Seconds    | Ratio      |
| 400.perlbench  | 24     | 467         | 502         | <b>468</b> | <b>501</b>  | 470     | 499   | 24     | 380         | 617         | <b>382</b> | <b>614</b>  | 382        | 614        |
| 401.bzip2      | 24     | 681         | 340         | <b>680</b> | <b>341</b>  | 679     | 341   | 24     | <b>659</b>  | <b>352</b>  | 660        | 351         | 658        | 352        |
| 403.gcc        | 24     | 375         | 515         | <b>377</b> | <b>513</b>  | 378     | 512   | 24     | 375         | 515         | 372        | 519         | <b>375</b> | <b>516</b> |
| 429.mcf        | 24     | 244         | 897         | <b>243</b> | <b>899</b>  | 243     | 902   | 24     | 244         | 897         | <b>243</b> | <b>899</b>  | 243        | 902        |
| 445.gobmk      | 24     | <b>551</b>  | <b>457</b>  | 551        | 457         | 551     | 457   | 24     | <b>541</b>  | <b>466</b>  | 541        | 465         | 540        | 466        |
| 456.hammer     | 24     | 208         | 1080        | <b>209</b> | <b>1070</b> | 210     | 1070  | 24     | 170         | 1310        | <b>170</b> | <b>1310</b> | 170        | 1320       |
| 458.sjeng      | 24     | <b>616</b>  | <b>471</b>  | 616        | 471         | 615     | 472   | 24     | 581         | 500         | <b>581</b> | <b>500</b>  | 581        | 500        |
| 462.libquantum | 24     | <b>70.3</b> | <b>7070</b> | 70.3       | 7080        | 70.4    | 7070  | 24     | <b>70.3</b> | <b>7070</b> | 70.3       | 7080        | 70.4       | 7070       |
| 464.h264ref    | 24     | 614         | 865         | <b>613</b> | <b>867</b>  | 612     | 867   | 24     | <b>601</b>  | <b>884</b>  | 600        | 885         | 602        | 882        |
| 471.omnetpp    | 24     | 443         | 339         | <b>442</b> | <b>339</b>  | 442     | 340   | 24     | 416         | 360         | <b>415</b> | <b>361</b>  | 415        | 361        |
| 473.astar      | 24     | 407         | 414         | <b>404</b> | <b>417</b>  | 403     | 418   | 24     | 407         | 414         | <b>404</b> | <b>417</b>  | 403        | 418        |
| 483.xalancbmk  | 24     | 188         | 881         | <b>187</b> | <b>884</b>  | 187     | 884   | 24     | 188         | 881         | <b>187</b> | <b>884</b>  | 187        | 884        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Set Power Efficiency Mode to Performance  
Set Snoop Mode to ES mode  
Set Hyper-Threading to Enable  
Sysinfo program /spec16/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-lg2g Thu Oct 27 22:04:31 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz  
2 "physical id"s (chips)  
24 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with  
Continued on next page



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## Platform Notes (Continued)

```

caution.)
  cpu cores : 6
  siblings  : 12
  physical 0: cores 0 1 2 3 6 7
  physical 1: cores 0 1 2 3 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      264076772 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-1g2g 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 27 10:21

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext4  542G  49G  493G   9% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

```

8x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

```

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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmarthearp

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

458.sjeng: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
 -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias  
 -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias  
 -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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