



SPEC® CFP2006 Result

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Huawei

SPECfp®2006 = **118**

Huawei XH620 V3 (Intel Xeon E5-2643 v4)

SPECfp_base2006 = **114**

CPU2006 license: 3175

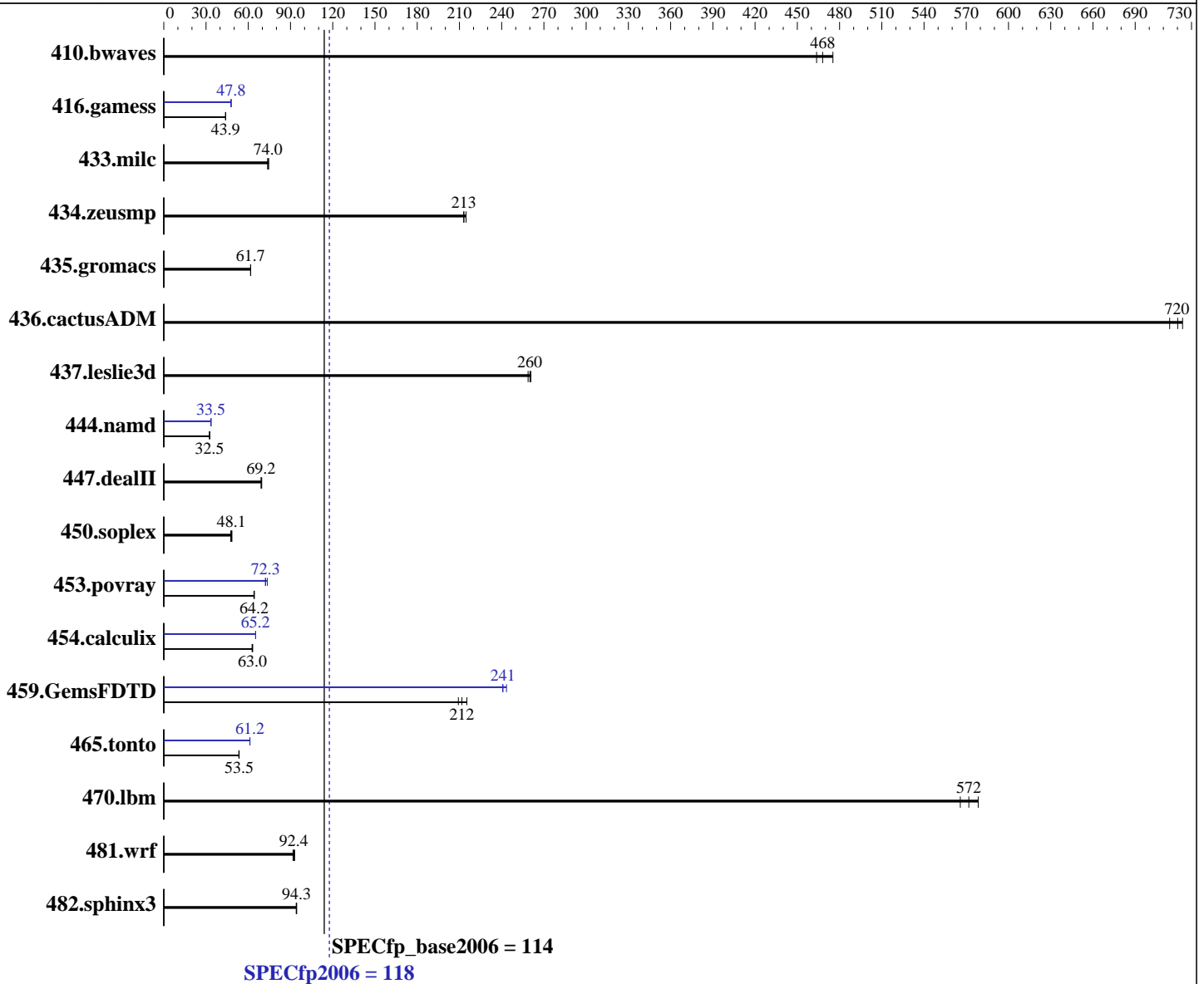
Test sponsor: Huawei

Tested by: Huawei

Test date: Nov-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E5-2643 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 600 GB SAS, 10000 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>29.0</u>	<u>468</u>	28.6	475	29.3	464	<u>29.0</u>	<u>468</u>	28.6	475	29.3	464
416.gamess	<u>446</u>	<u>43.9</u>	445	44.0	446	43.9	<u>410</u>	<u>47.8</u>	410	47.8	409	47.8
433.milc	123	74.5	124	73.8	<u>124</u>	<u>74.0</u>	123	74.5	124	73.8	<u>124</u>	<u>74.0</u>
434.zeusmp	<u>42.7</u>	<u>213</u>	42.4	215	42.7	213	<u>42.7</u>	<u>213</u>	42.4	215	42.7	213
435.gromacs	116	61.7	116	61.6	<u>116</u>	<u>61.7</u>	116	61.7	116	61.6	<u>116</u>	<u>61.7</u>
436.cactusADM	16.7	714	16.5	724	<u>16.6</u>	<u>720</u>	16.7	714	16.5	724	<u>16.6</u>	<u>720</u>
437.leslie3d	<u>36.2</u>	<u>260</u>	36.3	259	36.1	261	<u>36.2</u>	<u>260</u>	36.3	259	36.1	261
444.namd	247	32.5	246	32.6	<u>247</u>	<u>32.5</u>	239	33.5	239	33.5	<u>239</u>	<u>33.5</u>
447.dealII	165	69.2	164	69.6	<u>165</u>	<u>69.2</u>	165	69.2	164	69.6	<u>165</u>	<u>69.2</u>
450.soplex	172	48.4	175	47.6	<u>173</u>	<u>48.1</u>	172	48.4	175	47.6	<u>173</u>	<u>48.1</u>
453.povray	<u>82.9</u>	<u>64.2</u>	83.1	64.0	82.8	64.3	<u>73.6</u>	<u>72.3</u>	73.7	72.1	72.3	73.6
454.calculix	<u>131</u>	<u>63.0</u>	132	62.7	131	63.1	127	65.2	<u>126</u>	<u>65.2</u>	126	65.3
459.GemsFDTD	50.7	209	<u>50.1</u>	<u>212</u>	49.3	215	<u>44.0</u>	<u>241</u>	43.6	243	44.1	241
465.tonto	<u>184</u>	<u>53.5</u>	185	53.3	184	53.5	<u>161</u>	<u>61.2</u>	161	61.2	161	61.1
470.lbm	<u>24.0</u>	<u>572</u>	23.8	578	24.3	566	<u>24.0</u>	<u>572</u>	23.8	578	24.3	566
481.wrf	120	93.0	122	91.8	<u>121</u>	<u>92.4</u>	120	93.0	122	91.8	<u>121</u>	<u>92.4</u>
482.sphinx3	207	94.0	<u>207</u>	<u>94.3</u>	207	94.4	207	94.0	<u>207</u>	<u>94.3</u>	207	94.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set Power Efficiency Mode to Custom
 Set Snoop Mode to HS mode
 Set Patrol Scrub to Disable
 Set Hyper-Threading to Disable
 Sysinfo program /spec16/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-a2sq Tue Nov 29 09:28:23 2016

This section contains SUT (System Under Test) info as seen by
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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
  2 "physical id"s (chips)
  12 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 6
    siblings  : 6
    physical 0: cores 0 1 2 3 6 7
    physical 1: cores 0 1 2 3 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      264063680 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP1"
  VERSION_ID="12.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp1"

```

```

uname -a:
Linux linux-a2sq 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 29 09:27

```

SPEC is set to: /spec16
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2       xfs       558G      30G  529G   6% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016

Memory:

8x Hynix HMA84GR7MFR4N-UH 32 GB 2 rank 2400 MHz

8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):

-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>

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