



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY RX4770 M3, Intel Xeon E7-8891 v4, 2.80 GHz

SPECfp®2006 = 138
SPECfp_base2006 = 132

CPU2006 license: 19

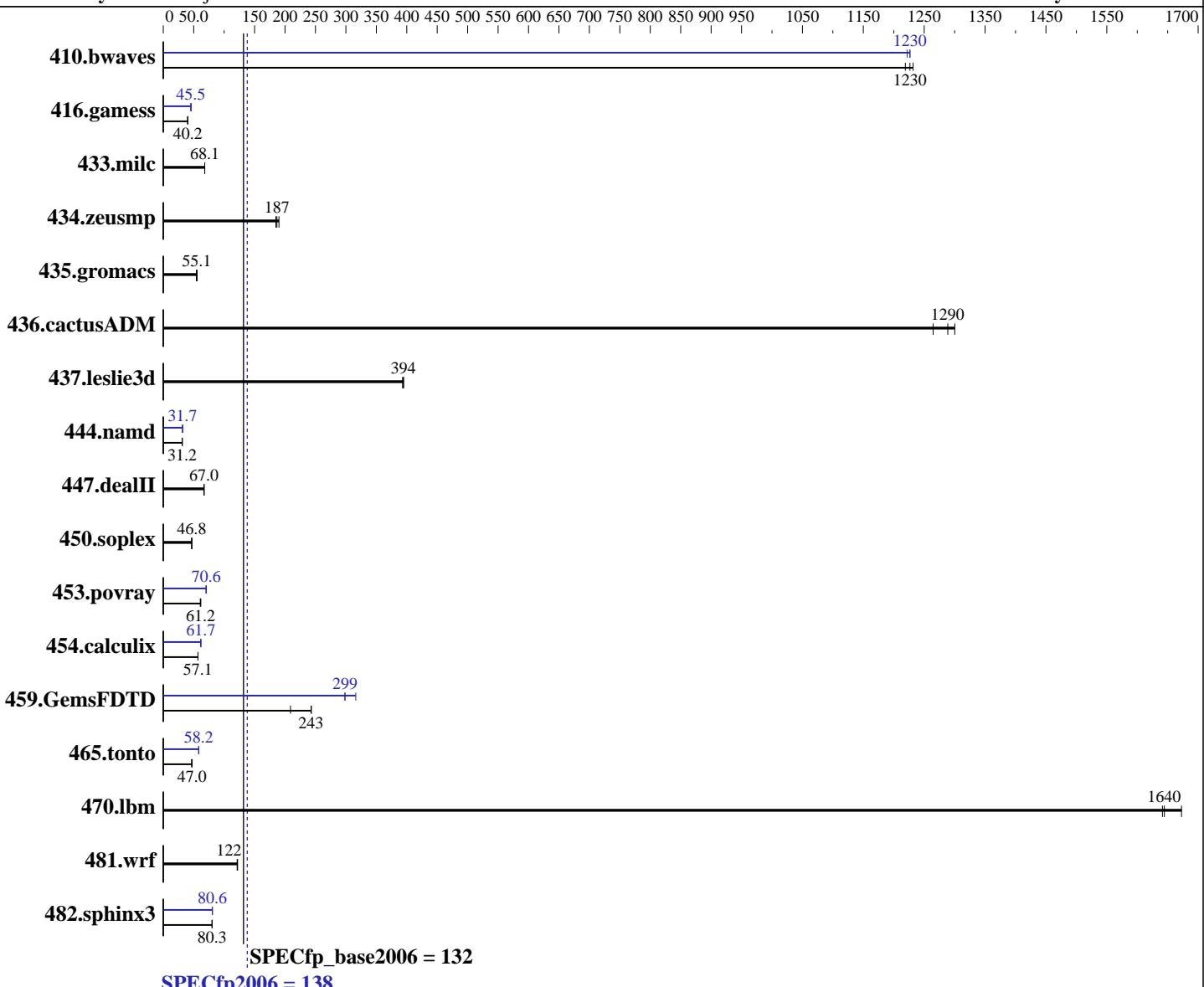
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2017

Hardware Availability: Jun-2016

Software Availability: Dec-2015



Hardware

CPU Name: Intel Xeon E7-8891 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
 Compiler: Kernel 3.12.49-11-default
 C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: tmpfs
 System State: Run level 3 (multi-user)

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L3 Cache: 60 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2400T-R, running at 1600 MHz)
 Disk Subsystem: 1008 GB tmpfs
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	11.1	1220	11.0	1230	<u>11.1</u>	<u>1230</u>	11.1	1230	11.1	1220	<u>11.1</u>	<u>1230</u>
416.gamess	487	40.2	488	40.2	486	40.3	429	45.6	432	45.3	431	45.5
433.milc	135	68.1	135	68.1	<u>135</u>	<u>68.1</u>	135	68.1	135	68.1	<u>135</u>	<u>68.1</u>
434.zeusmp	48.7	187	49.2	185	47.9	190	48.7	187	49.2	185	47.9	190
435.gromacs	131	54.5	<u>130</u>	<u>55.1</u>	128	55.6	<u>131</u>	<u>54.5</u>	<u>130</u>	<u>55.1</u>	128	55.6
436.cactusADM	9.45	1260	9.19	1300	<u>9.27</u>	<u>1290</u>	9.45	1260	9.19	1300	<u>9.27</u>	<u>1290</u>
437.leslie3d	23.8	394	23.8	395	23.9	393	23.8	394	23.8	395	23.9	393
444.namd	257	31.2	<u>257</u>	<u>31.2</u>	257	31.2	253	31.7	253	31.7	<u>253</u>	<u>31.7</u>
447.dealII	171	67.0	171	67.0	<u>171</u>	<u>67.0</u>	171	67.0	171	67.0	<u>171</u>	<u>67.0</u>
450.soplex	178	46.8	178	46.8	<u>178</u>	<u>46.8</u>	178	46.8	178	46.8	<u>178</u>	<u>46.8</u>
453.povray	86.2	61.7	87.5	60.8	<u>86.9</u>	<u>61.2</u>	<u>75.3</u>	<u>70.6</u>	75.9	70.1	75.2	70.8
454.calculix	145	57.0	144	57.1	<u>145</u>	<u>57.1</u>	134	61.6	<u>134</u>	<u>61.7</u>	134	61.7
459.GemsFDTD	43.6	243	<u>43.7</u>	<u>243</u>	50.8	209	33.5	316	<u>35.5</u>	<u>299</u>	35.6	298
465.tonto	209	47.0	209	47.2	211	46.6	169	58.2	169	58.3	169	58.2
470.lbm	8.36	1640	8.37	1640	8.21	1670	8.36	1640	8.37	1640	8.21	1670
481.wrf	91.9	122	91.9	121	91.4	122	91.9	122	91.9	121	91.4	122
482.sphinx3	243	80.3	243	80.3	243	80.1	242	80.6	241	80.8	242	80.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Turbo mode set with :

cpupower -c all frequency-set -g performance

Tmpfs filesystem can be set with:

mkdir /home/memory

mount -t tmpfs -o size=1008g,rw tmpfs /home/memory

Process tunning setting:

echo 10000000 > /proc/sys/kernel/sched_min_granularity_ns

echo 15000000 > /proc/sys/kernel/sched_wakeup_granularity_ns

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled



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Platform Notes

BIOS configuration:

Energy Performance = Performance
Utilization Profile = Unbalanced
Uncore Frequency Override = Maximum
Package C State limit = C0

COD Enable = Disabled

Sysinfo program /home/memory/speccpu/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-z26t Sun Mar 5 14:50:19 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8891 v4 @ 2.80GHz
 4 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 10
 siblings : 10
 physical 0: cores 5 9 10 11 13 18 24 26 28 29
 physical 1: cores 5 9 10 11 13 18 24 26 28 29
 physical 2: cores 5 9 10 11 13 18 24 26 28 29
 physical 3: cores 5 9 10 11 13 18 24 26 28 29
cache size : 61440 KB

From /proc/meminfo
MemTotal: 529056500 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
 SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
This file is deprecated and will be removed in a future service pack or release.
Please check /etc/os-release for details about this release.
os-release:
 NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"

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Platform Notes (Continued)

CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

```
Linux linux-z26t 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Mar 4 01:17

SPEC is set to: /home/memory/speccpu

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	1008G	6.5G	1002G	1%	/home/memory

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.8.94 for D3749-A1x
11/11/2016

Memory:

32x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 1600 MHz
64x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

OMP_NUM_THREADS = "40"

LD_LIBRARY_PATH = "/home/memory/speccpu/libs/32:/home/memory/speccpu/libs/64:/home/memory/speccpu/sh"

Binaries compiled on a system with 4x Intel Xeon E7-8891 v4 CPU + 512GB memory using SUSE 12 SP1

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
  450.soplex: -DSPEC_CPU_LP64
  453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint -auto-ilp32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-qopt-calloc
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
  -opt-prefetch -ansi-alias -qopt-prefetch-issue-excl-hint
  -auto-ilp32 -fp-model fast=2
  -nofor_main(*)
```

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Base Optimization Flags (Continued)

436.cactusADM: Same as 435.gromacs

454.calculix: Same as 435.gromacs

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias -qopt-prefetch-issue-excl-hint
-auto-ilp32 -fp-model fast=2

(*) Indicates an optimization flag that was found in a portability variable.

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops

-nofor-main

C++ benchmarks:

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Peak Optimization Flags (Continued)

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
            -opt-prefetch
            -fp-model fast=2
            -qopt-prefetch-issue-excl-hint -funroll-all-loops
```

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
            -inline-level=0 -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
               -inline-level=0 -opt-prefetch -parallel
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll4
```

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

```
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
               -nofor_main(*)
```

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

(*) Indicates an optimization flag that was found in a portability variable.

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-compiler-flags.ic16.0-linux64.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-compiler-flags.ic16.0-linux64.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevE.xml>

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