



# SPEC® CINT2006 Result

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## Cisco Systems

Cisco UCS C480 M5 (Intel Xeon Gold 5115, 2.40GHz)

**SPECint®2006 = 66.5**

**SPECint\_base2006 = 63.8**

**CPU2006 license:** 9019

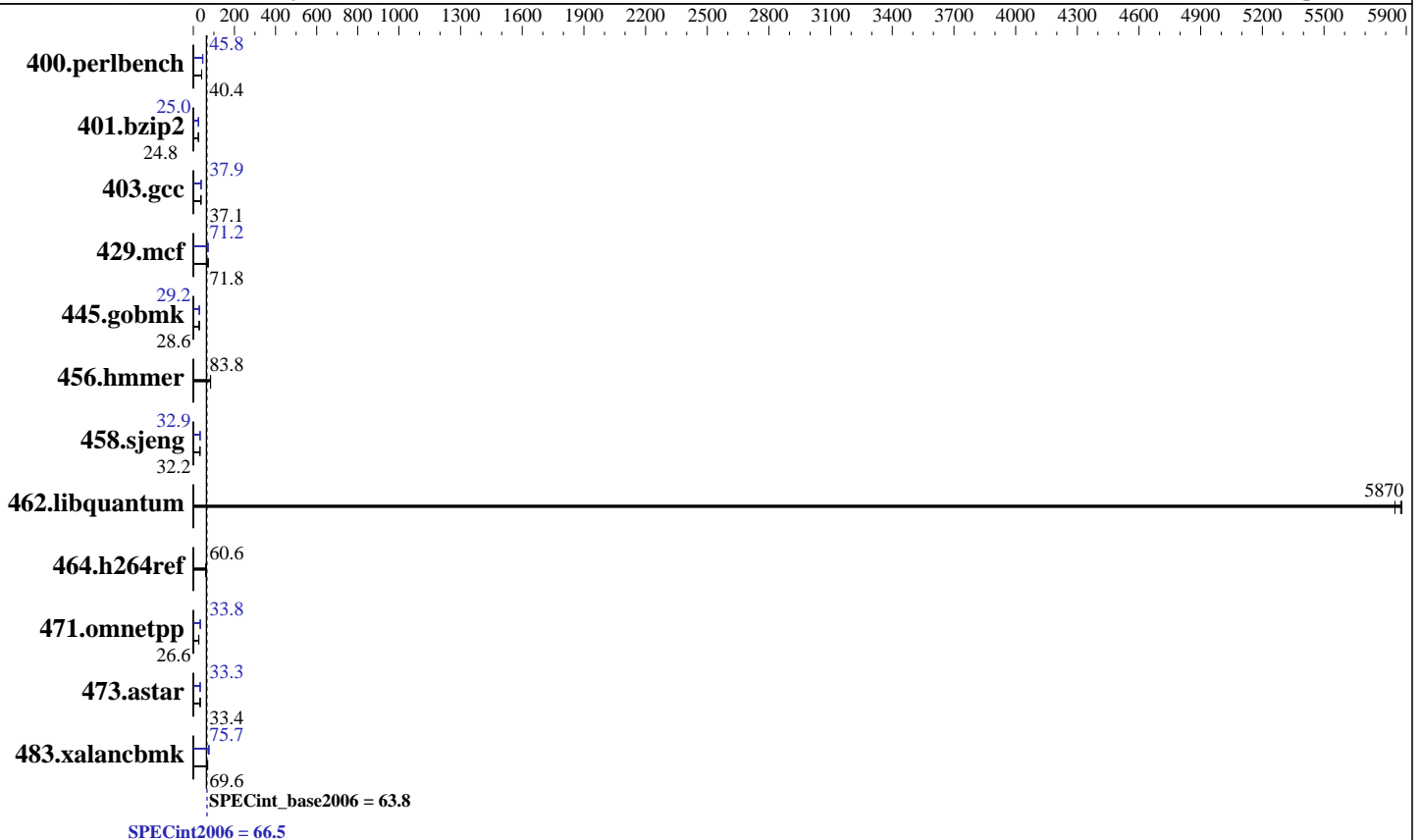
**Test sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test date:** Sep-2017

**Hardware Availability:** Aug-2017

**Software Availability:** Apr-2017



### Hardware

**CPU Name:** Intel Xeon Gold 5115  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.20 GHz  
**CPU MHz:** 2400  
**FPU:** Integrated  
**CPU(s) enabled:** 40 cores, 4 chips, 10 cores/chip  
**CPU(s) orderable:** 2,4 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 1 MB I+D on chip per core  
**L3 Cache:** 13.75 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 768 GB (48 x 16 GB 2Rx4 PC4-2666V-R, running at 2400 MHz)  
**Disk Subsystem:** 1 x 600 GB SAS HDD, 10K RPM  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-514.el7.x86\_64  
**Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
**Auto Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	242	40.4	<b><u>242</u></b>	<b><u>40.4</u></b>	241	40.6	214	45.7	<b><u>213</u></b>	<b><u>45.8</u></b>	212	46.0
401.bzip2	388	24.9	389	24.8	<b><u>389</u></b>	<b><u>24.8</u></b>	<b><u>386</u></b>	<b><u>25.0</u></b>	386	25.0	386	25.0
403.gcc	<b><u>217</u></b>	<b><u>37.1</u></b>	217	37.2	217	37.1	<b><u>212</u></b>	<b><u>37.9</u></b>	212	38.0	212	37.9
429.mcf	128	71.1	127	71.8	<b><u>127</u></b>	<b><u>71.8</u></b>	127	71.8	<b><u>128</u></b>	<b><u>71.2</u></b>	129	71.0
445.gobmk	<b><u>367</u></b>	<b><u>28.6</u></b>	367	28.6	367	28.6	360	29.2	<b><u>360</u></b>	<b><u>29.2</u></b>	360	29.2
456.hammer	<b><u>111</u></b>	<b><u>83.8</u></b>	112	83.6	111	83.9	<b><u>111</u></b>	<b><u>83.8</u></b>	112	83.6	111	83.9
458.sjeng	376	32.2	376	32.2	<b><u>376</u></b>	<b><u>32.2</u></b>	368	32.9	<b><u>368</u></b>	<b><u>32.9</u></b>	368	32.9
462.libquantum	<b><u>3.53</u></b>	<b><u>5870</u></b>	3.52	5880	3.54	5840	<b><u>3.53</u></b>	<b><u>5870</u></b>	3.52	5880	3.54	5840
464.h264ref	364	60.8	365	60.6	<b><u>365</u></b>	<b><u>60.6</u></b>	364	60.8	365	60.6	<b><u>365</u></b>	<b><u>60.6</u></b>
471.omnetpp	235	26.6	<b><u>235</u></b>	<b><u>26.6</u></b>	235	26.6	<b><u>185</u></b>	<b><u>33.8</u></b>	185	33.8	185	33.7
473.astar	<b><u>210</u></b>	<b><u>33.4</u></b>	210	33.4	211	33.3	212	33.1	<b><u>211</u></b>	<b><u>33.3</u></b>	211	33.3
483.xalancbmk	99.1	69.6	99.2	69.5	<b><u>99.2</u></b>	<b><u>69.6</u></b>	<b><u>91.1</u></b>	<b><u>75.7</u></b>	91.1	75.7	91.2	75.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### BIOS Settings:

Intel HyperThreading Technology set to Disabled  
 CPU performance set to Enterprise  
 Power Performance Tuning set to OS  
 SNC set to Disabled  
 IMC Interleaving set to Auto  
 Patrol Scrub set to Disabled  
 Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on rhel73-spec Sat Sep 9 17:52:12 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
 model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz  
 4 "physical id"s (chips)  
 40 "processors"

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### Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings  : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
physical 2: cores 0 1 2 3 4 8 9 10 11 12
physical 3: cores 0 1 2 3 4 8 9 10 11 12
cache size : 14080 KB
```

From /proc/meminfo

```
MemTotal:      790981288 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

uname -a:

```
Linux rhel73-spec 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 9 17:49

SPEC is set to: /home/cpu2006-1.2

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sdb5        xfs       503G   30G  474G   6% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C480M5.3.1.0.272.0613172154 06/13/2017Cisco Systems, Inc. C480M5.3.1.0.272.0613172154 06/13/2017

Memory:

96x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

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### Platform Notes (Continued)

The correct amount of Memory installed is 768 GB (48 x 16 GB) and the dmidecode is reporting invalid number of DIMMs installed  
Installed Memory:  
48x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

### General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact"  
LD\_LIBRARY\_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"  
OMP\_NUM\_THREADS = "40"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

### Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

### Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

### Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

- 400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch
- 401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -auto-ilp32 -qopt-prefetch
- 403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-qopt-malloc-options=3 -auto-ilp32
- 429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-qopt-prefetch -auto-p32
- 445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)
- 456.hmmer: basepeak = yes
- 458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4
- 462.libquantum: basepeak = yes
- 464.h264ref: basepeak = yes

C++ benchmarks:

- 471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-ra-region-strategy=block  
-Wl,-z,muldefs -L/sh10.2 -lsmarheap
- 473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmarheap64
- 483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-Wl,-z,muldefs -L/sh10.2 -lsmarheap

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml>

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Tested with SPEC CPU2006 v1.2.  
Report generated on Fri Oct 13 10:13:08 2017 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 12 October 2017.