



# CINT2000 Result

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**Dell**  
Precision WorkStation 340 (2.0A GHz P4)

SPECint\_rate2000 = 8.81  
SPECint\_rate\_base2000 = 8.57

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Jan-2002 | Hardware Avail: Jan-2002 | Software Avail: Jun-2001

| Benchmark   | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip    | 1           | 185          | 8.77       | 1      | 183     | 8.88  |
| 175.vpr     | 1           | 326          | 4.99       | 1      | 314     | 5.17  |
| 176.gcc     | 1           | 125          | 10.2       | 1      | 124     | 10.3  |
| 181.mcf     | 1           | 319          | 6.55       | 1      | 319     | 6.55  |
| 186.crafty  | 1           | 146          | 7.95       | 1      | 145     | 7.99  |
| 197.parser  | 1           | 250          | 8.36       | 1      | 248     | 8.41  |
| 252.eon     | 1           | 141          | 10.7       | 1      | 122     | 12.4  |
| 253.perlbnk | 1           | 189          | 11.1       | 1      | 189     | 11.0  |
| 254.gap     | 1           | 119          | 10.8       | 1      | 116     | 11.0  |
| 255.vortex  | 1           | 174          | 12.7       | 1      | 158     | 14.0  |
| 256.bzip2   | 1           | 255          | 6.82       | 1      | 255     | 6.83  |
| 300.twolf   | 1           | 478          | 7.29       | 1      | 475     | 7.33  |

### Hardware

CPU: Intel Pentium 4  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip  
CPU(s) orderable: 1  
Parallel: No  
Primary Cache: 12K(I) micro-ops + 8KB(D) on chip  
Secondary Cache: 512KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 2 x 128MB PC800 ECC RDRAM  
Disk Subsystem: 1 x 20GB Maxtor 7.2K ATA/5  
Other Hardware:

### Software

Operating System: Windows 2000 Professional (SP2)  
Compiler: Intel C++ Compiler 5.0.1 (010922Z)  
Microsoft Visual C++ 6.0 (SP5)  
MicroQuill SmartHeap Library 5.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000  
186.crafty: -DNT\_i386  
253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### FEEDBACK-DIRECTED OPTIMIZATION

FDO: PASS1= -Qprof\_gen PASS2= -Qprof\_use

### BASE TUNING

C: -Qipo +FDO -QxW shlW32M.lib  
C++: -Qipo -QxW -GX -GR

### PEAK TUNING

164.gzip: -Qipo +FDO -QxW -Oi- -Oa  
175.vpr: -Qwp\_ipo +FDO -QxW  
176.gcc: -Qipo +FDO -QxW shlW32M.lib  
181.mcf: -Qipo +FDO -QxW shlW32M.lib  
186.crafty: -Qipo +FDO -QxW -Oa shlW32M.lib  
197.parser: -Qipo +FDO -QxW  
252.eon: -Qipo +FDO -QxW  
253.perlbnk: -Qipo +FDO -QxW shlW32M.lib  
254.gap: -Qipo +FDO -QaxW



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## Notes/Tuning Information (Continued)

```
255.vortex: -Qwp_ipo +FDO -QxW -Oa
256.bzip2:  -Qipo +FDO -QxW shlw32M.lib
300.twolf:  -Qipo +FDO -QxW shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib: MicroQuill SmartHeap Library 5.0
              www.microquill.com
```