



CINT2000 Result

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SGI
SGI Altix 3000 (1500MHz, Itanium 2)

SPECint2000 = 1077
SPECint_base2000 = 1077

SPEC license #: 4 Tested by: SGI Test date: Jun-2003 Hardware Avail: Jun-2003 Software Avail: Jun-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	154	909	154	909	
175.vpr	1400	135	1039	135	1039	
176.gcc	1100	88.2	1248	88.2	1248	
181.mcf	1800	138	1301	138	1301	
186.crafty	1000	95.6	1046	95.6	1046	
197.parser	1800	234	770	234	770	
252.eon	1300	104	1252	104	1252	
253.perlbmk	1800	169	1064	169	1064	
254.gap	1100	141	778	141	778	
255.vortex	1900	122	1554	122	1554	
256.bzip2	1500	145	1034	145	1034	
300.twolf	3000	252	1190	252	1190	

Hardware

CPU: Intel Itanium 2
 CPU MHz: 1500
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 4-64
 Parallel: No
 Primary Cache: 16KBI + 16KBD (on chip) per CPU
 Secondary Cache: 256KB (on chip) per CPU
 L3 Cache: 6.0MB (on chip) per CPU
 Other Cache: N/A
 Memory: 4 GB
 Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
 Other Hardware: None

Software

Operating System: SGI ProPack(TM) v2.2
 Compiler: Intel(R) C++ Compiler for Linux 7.1 (Build 20030507)
 File System: xfs
 System State: Single-user

Notes/Tuning Information

+FDO: PASS1=-prof_gen PASS2=-prof_use

Baseline optimization flags:

C programs: -ipo -O3 +FDO
 C++ programs: -ipo -O2 +FDO -ansi_alias

Portability Flags:

176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
 186.crafty: -DLINUX_i386
 252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
 253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
 -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
 254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
 -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
 255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).