



CINT2000 Result

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Fujitsu Siemens Computers CELSIUS V810

SPECint2000 = 1354
SPECint_base2000 = 1248

SPEC license #: 22 Tested by: Fujitsu Siemens Computers Test date: Nov-2003 Hardware Avail: Dec-2003 Software Avail: Nov-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	122	1148	119	1177	
175.vpr	1400	132	1059	132	1061	
176.gcc	1100	72.7	1513	72.7	1513	
181.mcf	1800	286	629	182	988	
186.crafty	1000	53.3	1875	52.5	1905	
197.parser	1800	175	1027	170	1060	
252.eon	1300	68.7	1892	63.7	2040	
253.perlbnk	1800	119	1509	119	1509	
254.gap	1100	92.6	1188	92.6	1188	
255.vortex	1900	121	1576	107	1768	
256.bzip2	1500	131	1147	130	1158	
300.twolf	3000	287	1046	223	1345	

Hardware

CPU: AMD Opteron (TM) 248
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 64KBI + 64KBD on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: --
Memory: 4 x 1 GB PC2700 DDR SDRAM, ECC, Registered
Disk Subsystem: Maxtor ST020H2
Other Hardware: None

Software

Operating System: SuSE Linux 9.0 (AMD64) 2.4.21-102-default
Compiler: SuSE gcc 3.3.1 compiler (from SuSE Linux 9.0)
File System: Linux/reiserfs
System State: Multi-user SuSE Run level 3

Notes/Tuning Information

+FDO: PASS1=-fprofile-arcs PASS2=-fbranch-probabilities

Portability:

PORTABILITY=-DSPEC_CPU2000_LP64 is applied to all benchmarks, except for peak runs of 181.mcf, 197.parser and 300.twolf, which are compiled for 32-bit in peak.

186.crafty: CPORABILITY=-DLINUX_i386

176.gcc: Approved src.alt is used.

252.eon: CXXPORTABILITY=-DHAS_ERRLIST -DFMAX_IS_DOUBLE

Approved src.alt is used.

253.perlbnk: CPORABILITY=-DSPEC_CPU2000_LINUX_I386 -DSPEC_CPU2000_NEED_BOOL -DSPEC_CPU2000_GLIBC22

254.gap: CPORABILITY=-DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO -DSYS_HAS_ANSI -DSYS_HAS_CALLOC_PROTO

Baseline C: gcc -O3 -funroll-all-loops +FDO

Baseline C++: g++ -O3 -funroll-all-loops +FDO

Peak tuning:

gzip: -O3 -funroll-all-loops -finline-limit=900 -freduce-all-givs +FDO

vpr: -O3 -funroll-loops -finline-limit=1000 +FDO

gcc: basepeak=true

mcf: -O3 -funroll-all-loops -m32 +FDO

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SPECint2000 =	1354
SPECint_base2000 =	1248

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Notes/Tuning Information (Continued)

```
crafty: -O3 -funroll-loops -fprefetch-loop-arrays
parser: -O3 -funroll-all-loops -m32 +FDO
eon: -O3 -funroll-all-loops -ffast-math -finline-limit=3000 +FDO
perlbmk: basepeak=true
gap: basepeak=true
vortex: -O3 -funroll-all-loops -finline-limit=1000 +FDO
```

System tested with only one CPU present.
BIOS default settings have been used. These include:
ECC memory checking disabled,
node interleave disabled, SRAT enabled.

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