



CINT2000 Result

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SGI

SGI Altix 350 (1400MHz/1.5MB, Itanium 2)

SPECint2000 = --

SPECint_base2000 = 986

SPEC license #: 4 Tested by: SGI Test date: Dec-2003 Hardware Avail: Jan-2004 Software Avail: Dec-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	500 1000 1500 2000			
164.gzip	1400	161	870			[Bar chart showing ratio]			
175.vpr	1400	187	747			[Bar chart showing ratio]			
176.gcc	1100	102	1079			[Bar chart showing ratio]			
181.mcf	1800	222	810			[Bar chart showing ratio]			
186.crafty	1000	90.0	1111			[Bar chart showing ratio]			
197.parser	1800	246	733			[Bar chart showing ratio]			
252.eon	1300	91.8	1416			[Bar chart showing ratio]			
253.perlbnk	1800	169	1062			[Bar chart showing ratio]			
254.gap	1100	138	798			[Bar chart showing ratio]			
255.vortex	1900	120	1579			[Bar chart showing ratio]			
256.bzip2	1500	175	859			[Bar chart showing ratio]			
300.twolf	3000	269	1114			[Bar chart showing ratio]			

Hardware

CPU: Intel Itanium 2
 CPU MHz: 1400
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 2-16
 Parallel: No
 Primary Cache: 16KBI + 16KBD (on chip) per CPU
 Secondary Cache: 256KB (on chip) per CPU
 L3 Cache: 1.5MB (on chip) per CPU
 Other Cache: N/A
 Memory: 4 GB (8*512MB PC2700 DIMMS)
 Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
 Other Hardware: None

Software

Operating System: SGI ProPack(TM) v2.3
 Compiler: Intel(R) C++ Compiler for Linux 8.0 (Build 20031017)
 MicroQuill SmartHeap Library 7.01 (www.microquill.com)
 File System: xfs
 System State: Single-user

Notes/Tuning Information

+FDO: PASS1=--prof_gen PASS2=--prof_use

Baseline optimization flags:

C programs: -fast -auto_ilp32 +FDO
 C++ programs: -fast -ansi_alias +FDO
 Extra Libraries: libsmartheap64.a

Portability Flags:

176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
 186.crafty: -DLINUX_i386
 252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST -DFMAX_IS_DOUBLE
 253.perlbnk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
 -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
 254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
 -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
 255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using runon.
 Extra CPUs were disabled at firmware.