



CINT2000 Result

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Supermicro
SUPER X5DPA-GG (3.06GHz, 533MHz bus)

SPECint_rate2000 = 15.3
SPECint_rate_base2000 = 15.2

SPEC license #01176 | Test Site: Micro computer, Inc., San Jose, CA | Test date: Jan-2004 | Hardware Avail: Nov-2003 | Software Avail: Aug-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	173	18.8	2	154	21.1
175.vpr	2	394	8.24	2	411	7.90
176.gcc	2	134	19.0	2	172	14.8
181.mcf	2	555	7.52	2	556	7.51
186.crafty	2	136	17.1	2	141	16.4
197.parser	2	272	15.3	2	265	15.8
252.eon	2	137	21.9	2	138	21.9
253.perlbnk	2	232	18.0	2	194	21.6
254.gap	2	124	20.6	2	111	23.0
255.vortex	2	177	24.9	2	162	27.2
256.bzip2	2	321	10.8	2	315	11.1
300.twolf	2	607	11.5	2	646	10.8

Hardware

CPU: Intel Xeon (3.06GHz, 533MHz bus)
CPU MHz: 3066
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12K(I) micro-ops + 8KB(D) on chip
Secondary Cache: 512KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 2x1024MB PC2100 Registered ECC Double-sided CL2
Disk Subsystem: 1 x IBM Deskstar 123.5GB IDE
Other Hardware:

Software

Operating System: Windows XP Professional W/ SP1a
Compiler: Intel C++ Compiler 7.1 Build 20030801Z
Microsoft Visual Studio Enterprise Edition 6.0(for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -QxW -Qipo +FDO shlw32m.lib
Base tuning for C++ programs: -Qipo -GX -GR +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLLOC_PROTO -DSYS_HAS_MALLOC_PROTO
164.gzip: -O3 -QxW -Qipo +FDO
175.vpr: -QxW -Qipo -O3 +FDO
176.gcc: -QxW -Qipo -Oi- -O3 +FDO shlw32M.lib
181.mcf: -Oa -Qipo +FDO shlw32M.lib
186.crafty: -QxW -Qipo -O3 +FDO
197.parser: -QxW -Qipo -Oa -O3 +FDO
252.eon: -QxW -Qipo -O3 +FDO
253.perlbnk: -Qipo -Oa -O3 +FDO shlw32M.lib
254.gap: -QxW -Qipo -O3 +FDO
255.vortex: -QxW -Qipo -O3 +FDO
256.bzip2: -Qunroll1 -Oa -Qipo +FDO
300.twolf: -Qxi -Qipo +FDO shlw32M.lib
```



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Notes/Tuning Information (Continued)

Hyper-Threading Technology is turned ON
ONESTEP is used for all base and peak runs
The tested system can be assembled using an 1U Chassis such as the Supermicro SC811i-350,
with 350W power supply. See Link for more chassis compatibility lists at
<http://www.supermicro.com/PRODUCT/Chassis/compatibility.htm>.
The System bus runs up to 8GB ECC registered DDR-200/266 memory in 4 DIMM slots.
shlW32M.lib is the SmartHeap library V7.0 from MicroQuill www.microquill.com