



CINT2000 Result

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Supermicro
X6DHR-8XG (3.40 GHz Xeon, 1MB L3 Cache)

SPECint2000 = **1374**
SPECint_base2000 = **1290**

SPEC license #01176 Tested by: Supermicro Inc., San Jose, CA Test date: Jun-2004 Hardware Avail: Jun-2004 Software Avail: Dec-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	126	1111	126	1111	
175.vpr	1400	164	852	164	852	
176.gcc	1100	65.6	1677	65.6	1677	
181.mcf	1800	203	888	199	903	
186.crafty	1000	80.9	1237	81.1	1233	
197.parser	1800	148	1214	148	1214	
252.eon	1300	78.9	1647	66.3	1961	
253.perlbmk	1800	114	1577	105	1714	
254.gap	1100	64.3	1711	64.3	1711	
255.vortex	1900	83.3	2281	83.3	2281	
256.bzip2	1500	159	941	154	977	
300.twolf	3000	284	1055	182	1652	

Hardware

CPU: Intel Xeon (3.4GHz, 800 MHz system bus)
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (HT Technology enabled)
CPU(s) orderable: 1, 2
Parallel: No
Primary Cache: 12K(I) micro-ops + 8KB(D) on chip
Secondary Cache: 512KB(I+D) on chip
L3 Cache: 1024KB(I+D) on chip
Other Cache: N/A
Memory: 8 x 2048MB ECC CL2 PC 2100 DDR SDRAM
Disk Subsystem: 1 x 73GB U320 SCSI, 10K RPM
Other Hardware:

Software

Operating System: Windows 2003 Enterprise Server
Compiler: Intel C/C++ Compiler 8.0 (20031229Z)
Microsoft Visual Studio .NET (7.0.9466)
File System: NTFS
System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

FEEDBACK DIRECTED OPTIMIZATION

ONESTEP=yes
FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

BASE TUNING

C: -QxN -Qipo -O3 +FDO
C++: -QxN -Qipo -GX -GR

PEAK TUNING

164.gzip: -QxN -Qipo -O3 -Oa +FDO
175.vpr: basepeak=yes
176.gcc: basepeak=yes
181.mcf: -QxN -Qipo -O3 +FDO shlw32M.lib
186.crafty: -QxN -Qipo -O3 -Oa +FDO shlw32M.lib
197.parser: basepeak=yes
252.eon: -QxN -Qipo -O3 +FDO
253.perlbmk: -QxN -Qipo -O3 +FDO shlw32M.lib



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Supermicro
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SPECint2000 = 1374
SPECint_base2000 = 1290

SPEC license #01176 | Tested by: Supermicro Inc., San Jose, CA | Test date: Jun-2004 | Hardware Avail: Jun-2004 | Software Avail: Dec-2003

Notes/Tuning Information (Continued)

```
254.gap:      basepeak=yes
255.vortex    basepeak=yes
256.bzip2:    -Qipo      -Oa -Qunroll1 +FDO shlw32M.lib
300.twolf:    -QxN -Qipo -O3      +FDO shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib:  MicroQuill SmartHeap Library 7.1
              www.microquill.com
```