



CFP2000 Result

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ION

LN20J (3.6GHz Xeon processor w. 2MB L2 cache)

SPECfp2000 = 1868

SPECfp_base2000 = 1846

SPEC license #: 2697 Tested by: ION Test date: Jul-2005 Hardware Avail: Apr-2005 Software Avail: May-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
168.wupwise	1600	55.5	2881	55.5	2881	
171.swim	3100	154	2015	154	2015	
172.mgrid	1800	113	1598	113	1598	
173.applu	2100	145	1450	145	1450	
177.mesa	1400	76.0	1842	68.0	2059	
178.galgel	2900	84.8	3421	84.8	3421	
179.art	2600	49.6	5246	49.6	5246	
183.equake	1300	60.0	2167	56.8	2290	
187.facerec	1900	98.9	1920	98.9	1920	
188.amp	2200	163	1353	163	1353	
189.lucas	2000	114	1747	114	1747	
191.fma3d	2100	131	1604	131	1604	
200.sixtrack	1100	173	637	173	637	
301.apsi	2600	217	1196	217	1196	

Hardware

CPU: Intel Xeon processor, 800MHz system bus
CPU MHz: 3591
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1-2
Parallel: No
Primary Cache: 12K(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB (I+D) on chip
L3 Cache: None
Other Cache: None
Memory: 2 GB (4x512MB DDR2-400, 3-3-3 DIMMs)
Disk Subsystem: 80GB Seagate SATA disk
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux 4 ES, 2.6.9-5.0.5.ELsmp EM64T
Compiler: Intel(R) Fortran Compiler for EM64T, V8.1 (Build 20050517)
Intel(R) C Compiler for EM64T, V8.1 (Build 20050517)
File System: ext3
System State: Multi-user, runlevel 3

Notes/Tuning Information

Tested by ION Computer Systems
Hyper-Threading Technology disabled in BIOS
Hardware Prefetcher Enabled in BIOS
Adjacent Cache Line Prefetch Enabled in BIOS
System BIOS is V8.10 Build 81

Feedback optimization:
+FDO: PASS1 = -prof_gen PASS2 = -prof_use

FP Portability Flags:
178.galgel: -FI

FP Baseline optimization flags:
C programs: -fast -auto_ilp32 -march=pentium4 +FDO
Fortran programs: -fast +FDO
Fortran-77 programs: -fast -Zp8 +FDO



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Notes/Tuning Information (Continued)

FP Peak optimization flags:

```
168.wupwise: basepeak=yes
171.swim: basepeak=yes
72.mgrid: basepeak=yes
173.applu: basepeak=yes
177.mesa: -fast -auto_ilp32 +FDO
178.galgel: basepeak=yes
179.art: basepeak=yes
183.quake: -fast -auto_ilp32 +FDO
187.facerec: basepeak=yes
188.ammmp: basepeak=yes
189.lucas: basepeak=yes
191.fma3d: basepeak=yes
200.sixtrack: basepeak=yes
301.apsi: basepeak=yes
```